B.E. 4/4 (EEE/EIE) II-Semester (Main) Examination, May / June 2017 Subject: Electronics Instrumentation Systems

Time: 3 hours Max. Marks: 75

Note: Answer all questions from Part-A. Answer any FIVE questions from Part-B.

7 8 9	refe Me Wh Exp Dra Wh Wh Giv	PART – A (25 Marks) Itermine the conversion time of 16-bit ADC if clock frequency is 1 MHz and erence voltage is 5V. Intion the specifications of isolation amplifiers. Inat is quantization error? Inplain the principle of time interval measurement. Inplain the principle of wave analyzer. Inat with each diagram of spectrum analyzer. Inat is relay switched attenuator? Inat are the bus management signals in IEEE 488 bus? Inversion of the conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the conversion of the clock frequency is 1 MHz and erence voltage is 5V. In a conversion of the conversion of the conversion of the conversion of the conversio	3 2 2 3 2 3 2 3 2 3
11		PART – B (50 Marks) plain time base generator with neat circuit diagram and wave forms, for a ntinuous sweep CRO and triggered sweep CRO.	10
12		Explain the role of IEEE 488 electrical interface in computer controlled test systems. Explain the procedure for testing an audio amplifier.	5 5
13	,	Explain the operation of the frequency synthesizer uses a Phase Locked Loop (PLL) system, with neat diagram. Write notes on Log IF amplifier.	7
14	,	Explain basic operation of digital multimeter with neat block diagram. Explain with neat diagram digital frequency meter.	6 4
15	,	Explain the operation of successive approximation ADC. Suppose the converter can measure a maximum of 5V i.e. 5V corresponds to the maximum count of 11111111, if the test voltage is $V_{in} = 1V$. Show the steps take place in table format in the measurement for the successive approximation type converter.	6
16	•	Draw and explain instrumentation amplifier, with bridge input. Derive the expression for its output voltage. Explain the importance of isolation amplifiers in measuring systems.	7 3
17	a) b)	rite short notes on the following : Current to voltage converter Spectrum analyzer Storage oscilloscope	3 3 4

B.E. 4/4 (Mech.) II-Semester (Main) Examination, May / June 2017 Subject : Production Drawing

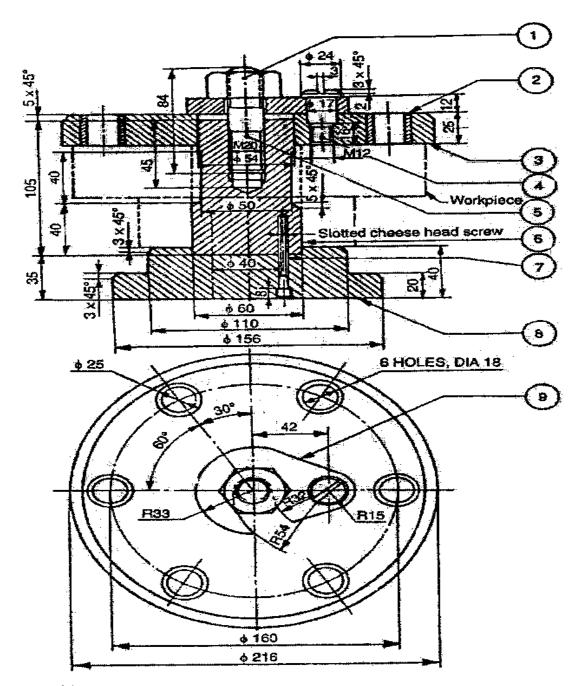
Time: 3 hours Max. Marks: 75

Note: Answer all questions from Part-A. Answer all questions from Part-B.

PART – A (25 Marks)

	11 (= 0 mamo)	
1	Compare the Engineering Drawing, Machine Drawing and Production Drawing.	3
2	What is Fit? Type of fits with examples.	3
3	Draw the symbol for surface roughness and abbreviate a, b, c, d, e and f.	3
4	What are the elements in preparing the process sheet?	3
5	What is the symbol for concentricity?	2
6	What is the conventional representation for bearings?	2
7	How to represent the material composition in production drawing sheet?	3
8	Draw the tool movement for multidirectional (direction of lay).	2
9	Draw the Hydraulic symbol for 2 way direction control valve.	2
10	Define tolerance, types with examples.	2
	PART – B (50 Marks)	
11	Draw the part drawings for the given assembly of Drill JIG and suggest the fits between mating parts. A) Stem and Jig Plate B) Jig plate and Bush C) Stem and Base plate.	35
12	Prepare the process sheet for stainless steel bar with having operations like, step turning, taper turning, threading, slitting, Knurling, internal threading, chamfering and under cutting with orientation.	15

Contd..2



Parts List

Part No.	Qty.	Name	Mati.
1	1	Nut	-
2	6	Bush	MCS
3	. 1	Jig plate	CI
4	1	Screw	MS
5	1	Stud	MS

Part No.	Qty.	Name	Mati.
6	1	Stem	MS
7	3	Screw	MS
8	#	Base	CI
p	1	Latch washer	MS
<u>}</u> .			

B.E. 4/4 (Prod.) II-Semester (Main) Examination, May / June 2017 Subject: Tool Design

Time: 3 hours Max. Marks: 75

Note: Answer all questions from Part-A. Answer any FIVE questions from Part-B.

PART – A (25 Marks)

- 1 Give a brief classification of cutting tool materials.
- 2 State the process capabilities of Lapping.
- 3 Sketch a single point cutting tool and indicate various elements on it.
- 4 Differentiate between form milling cutter and face milling cutter.
- 5 Describe the nomenclature of twist drill.
- 6 Name the considerable features while designing a tap.
- 7 What is the result of excessive die clearance?
- 8 Which type of press is useful for simple banking operation?
- 9 Discuss the significance of stripper.
- 10 Differentiate between a drill jig and fixture.

$PART - B (5 \times 10 = 50 Marks)$

- 11 a) Explain principle involved in EDM with the help of a neat sketch.
 - b) What are super-finishing operations? Explain any one process in detail.
- 12 a) Sketch and explain the elements of a standard milling cutter.
 - b) Explain the procedure of estimating the cutting forces during milling operation.
- 13 a) Differentiate between the push type and pull type broaches.
 - b) Discuss the functions of various elements of a twist drill.
- 14 a) Elucidate the designing features of reamers.
 - b) How a drawing dies is designed? Explain the procedure.
- 15 a) State and explain the elements of a Combination Die.
 - b) Explain the locating methods associated with the cylindrical surfaces.
- 16 a) Explain the principle of a grinding jig with neat sketch.
 - b) Differentiate between magnetic and vacuum clamping.
- 17 Write short notes on the following:
 - a) Grinding the milling cutter
 - b) Sketch of a bottoming tap
 - c) Redundant location

B.E. 4/4 (AE) II - Semester (Main) Examination, May / June 2017

Subject: Alternative Fuels and Energy System for Automobiles

Time: 3 Hours Max. Marks: 75

Note: Answer all questions from Part-A and answer any five questions from Part-B.

PART-A (25 marks)

- 1 List out some prominent alternate fuels.
- 2 Why hydrogen is referred to as a "Freedom Fuel".
- 3 Differentiate between methanol and DME.
- 4 Enumerate five applications of CNG gas.
- 5 Which materials are compatible with LPG?
- 6 Write a short note on gas turbine hybrid vehicle.
- 7 Briefly explain vegetable oil esterification process.
- 8 What are the safety checks in usage for CNG vehicles?
- 9 What are the advantages of hybrid vehicles over gasoline engines?
- 10 Describe the five benefits of vegetable oils for automobiles.

PART-B (50 marks)

- 11 Describe any three methods of hydrogen production in detail with diagram.
- 12 Describe production of DEE.
- 13 Discuss the storage facilities for DME.
- 14 a) write a short note on biogas digester.
 - b) What are the ten applications of biogas?
- 15 What are the modifications required in an SI engine for it to run it on biogas.
- 16 Explain about DME fuel injection equipment in detail with neat sketch.
- 17 Describe the solar car in great detail.

B.E. 4/4 (CSE) II - Semester (Main) Examination, May / June 2017

Subject: Information Storage and Management (Elective – II)

Time: 3 Hours Max.Marks: 75 Note: Answer all guestions from Part A. Answer any five guestions from Part B. PART – A (25 Marks) 1 Give the maximum possible number of node ports in a switched fabric. 2 2 List the key challenges in Information Management. 2 3 Define Information Lifecycle. 2 4 Give different factors affecting NAS performance. 3 5 Describe Business Impact Analysis. 3 6 What are various recovery considerations while taking a backup? 3 7 What are the benefits of cloud computing? 2 8 What are various programming models of cloud computing? 3 9 What are the 3 security domains of data storage? 2 10 What are various parameters to be monitored in a storage infrastructure? 3 PART - B (5x10 = 50 Marks)11 a) Briefly explain about the core elements of data centre infrastructure. 4 b) Compare and contrast different levels of RAID. 6 12 a) Define SAN. Explain about Fibre Channel Architecture with a diagram. 5 b) Explain about zoning function of FC switch. Illustrate the types of zoning. 5 13 a) Explain NAS file sharing protocols. 5 b) Calculate the number of disks required at RAID 5. Consider an application is generating 6,600 IOPS with 75 percent of reads. (Maximum of 180 IOPS for a Disk Drive). 5 14 a) How is backup taken in a NAS based environment? 5 b) What are different local replication technologies? Explain in detail. 5 15 a) Describe various cloud service models. Give suitable examples for each. 5 b) Discuss on different critical challenges of cloud computing. 5 16 a) Explain the challenges involved in securing storage infrastructure. 5 b) Briefly write about a Risk Triad and BURA. 5 17 Write short notes on the following: a) Illustrate SNIA Storage Virtualization Taxonomy. 5 b) Explain various phases of a BC planning lifecycle. 5

B.E. 4/4 (I.T.) II - Semester (Main & Backlog) Examination, May / June 2017

Subject: Information Storage and Management (Elective – IV)

Time: 3 Hours Max. Marks: 75

Note: Answer all questions from Part-A and answer any five questions from Part-B.

PART - A (25 Marks)

1.	Explain the significance of Information Storage and management in different domain.	(2)
2.	What is information lifecycle?	(3)
3.	What are the components of Storage system environment?	(3)
4.	Write in detail about volume manager.	(2)
5.	What is zoning? Explain the categories of Zones.	(3)
6.	Explain the protocols used for sharing data in NAS.	(2)
7.	What is Disaster recovery?	(3)
8.	What are the parameters monitored for storage infrastructure.	(2)
9.	Write about incremental and cumulative backup techniques.	(2)
10	. Why virtualization is needed. List the forms of virtualization.	(3)

PART – B (5x10=50 Marks)

- 11. Explain the role of each element in establishing data centre infrastructure.
- 12. Explain the components of intelligent storage system.
- 13. Differentiate SAN and NAS. Explain the components of SAN architecture in detail.
- 14. What is IP-SAN? Why there is need of IP-SAN? How IPSAN is implemented. Write about FCIP topologies.
- 15. How backup is performed in NAS environment.
- 16. Write about BC planning lifecycle in detail using an appropriate diagram.
- 17. List and analyse the threats in each security domain. Explain critical security attributes for information system.

B.E. 4/4 (IT) II-Semester (Main/Backlog) Examination, May / June 2017 Subject: Simulation and Modeling (Elective – IV)

Time: 3 hours Max. Marks: 75

Note: Answer all questions from Part-A and any FIVE questions from Part-B.

PART – A (25 Marks)

5 6 7	Wh De Wh De Wh De	escribe system environment. hat is simulation? efine queuing system. hat is a model? efine Pseudo-Random numbers. hat is probability distribution? efine Time series input model. hat is Data Analysis?	3 2 3 2 3 2 3 2 3 2
9	De	efine validation of Models. hat is Turing test?	3 2
		PART – B (50 Marks)	
11		Explain about advantages and disadvantages of simulation. Write the simulation examples.	7 3
12	,	Describe SIMAN. Explain briefly about SLAM II.	5 5
13	,	Discuss about Weibul distribution. Explain Poisson Distribution.	5 5
14	,	Explain the types of simulation with respect to output analysis. Write about Goodness of Fit tests.	7
15	Dis	scuss in detail input / output validation using historical data.	10
16	,	Explain Descrete-Event system simulation. Write the simulation areas of application.	7
17	,	Explain about discrete probability distributions. Write notes on uniform distributions.	7 3

B.E. 4/4 (I.T.) II – Semester (Main) Examination, May / June 2017

Subject: Advanced Computer Architecture (Elective – IV)

Time: 3 Hours Max.Marks: 75

Note: Answer all questions from Part A. Answer any five questions from Part B.

			PART – A (25 Marks)	
4 5 6 7 8	Ske Lis De Ho Sta De Lis De	etch t ou fine w d ate scri t th fine	on the techniques available to measure the performance. In the architecture of vector super computer. It salient features of vector processor. It inclusion property and coherence property. It is interconnection networks differ from dynamic interconnection networks? It is coherence problem. It is the multistage networks with a diagram. It is three types of loop optimizations. It is optimistic Concurrency. It is a performance. It is performance. It	[3] [2] [2] [2] [3] [3] [3] [3]
			PART - B (5x10 = 50 Marks)	
11	a)		mpare Data flow and control flow computers. Describe the various variant PRAM model.	[5] [5]
	12	,	Describe various overlapped FDE stages of superscalar execution. Explain different pipeline hazards.	[5] [5]
	13	,	Explain Routing Omega Network. Describe the scheduling of fine grain and coarse grain.	[5] [5]
	14	,	Describe the working of vector processing instructions with example. Explain remote loads and synchronization loads.	[5] [5]
			Compare Multiprocessor and Multicomputer. Describe the various types of vector instructions.	[4] [5]
	16		Describe the features of parallel language and parallel programming. What are various message type and parameters in message passing programme development?	[5] [5]
		a)	ite short notes on: Role of loop parallelization in vectorization. Design of Crossbar network.	[5] [5]

B.E. 4/4 (IT) II-Semester (Makeup) Examination, May / June 2017Subject: Natural Language Processing (Elective – IV)

Time: 3 hours Max. Marks: 75

Note: Answer all questions from Part-A and any FIVE questions from Part-B.

PART – A (25 Marks)

4 5 6 7 8 9	What are the features of good grammar? Define Lexicon. What is its significance in Natural Language Processing? What is simple noun phrase? What are the main issues in semantic interpretation? Explain Noun – Noun modifiers. What is Case grammar? Define Natural language processing. List any two applications of Natural Language Processing. What is logical form? How is it helpful in sentence interpretation? Draw and explain derived tree for "the boy kicked the bucket"? Explain Context – Free grammars.	2 3 2 2 3 3 3 3 2
	PART – B (50 Marks)	
11	a) What is Natural Language processing understanding? Explain with an example.b) What is a phrase? Explain prepositional phrase and compliments with example.	5 5
12	a) Explain Shift Reduce Parser.b) What is Look ahead parser?	5 5
13	Explain semantic networks with example? What is the advantage in removing inheritance property in semantic network.	10
14	Explain the different types of logical forms. Discuss the features of logical forms.	10
15	Discuss rule – by rule semantic interpretation based on lambda calculus.	10
16	Write short notes on : a) Word senses ambiguity b) Encoding ambiguity in logical forms.	10
17	Write short notes on : a) Problems of Machine translation b) Draw the block schematic of Anusaraka	5 5