B.E. 2/4 (Civil) I-Semester (Suppl.) Examination, May / June 2017

Subject : Building Planning and Drawing

Time : 3 hours

Max. Marks : 75

Note: Answer all questions from Part-A. Answer any FIVE questions from Part-B.

PART – A (25 Marks)

1	Draw the conventional symbol for existing brick and wood.	3
2	Draw a plan and elevation of a square footing for reinforced concrete column.	3
3	Sketch the elevation of a fully paneled window.	3
4	Draw a line diagram of queen post truss.	3
5	Draw the isometric view of king closer.	3
6	What are the different types of stair case?	2
7	What are the important points to be considered while locating a door and a window?	2
8	Differentiate header bond and stretcher bond.	2
9	What are the principles of planning?	2
10	What are the aspects of building planning?	2

PART – B (50 Marks)

11	Draw the plan and isometric view of wall junction for one and a half brick wall in English bond. Draw minimum 3 layers	10
	English bond. Draw minimum s layers.	10
12	Draw to a suitable scale, elevation and section of a glazed door of 1.2m x 2.1m.	10
13	Draw a plan and elevation of spiral stairs to a suitable scale.	10
14	Draw the elevation and details of king post truss of span of 15 m.	10
15	Draw a plan and elevation of wall foundation to a suitable scale.	10

- 16 Draw a plan and sectional elevation along shorter span of an RCC roof slab of size 4.0 x 6.0 m, to a suitable scale. 10
- 17 The line diagram of a building is shown in the figure. Draw plan and elevation to a suitable scale and locate doors and windows at appropriate locations. Take thickness of wall as 300mm. 10

Bed room 10x8		bed room 10x8
kit 6x6		
toi 6x4	nali	14x10

B.E. 2/4 (EE/Inst.) I – Semester (Suppl.) Examination, May / June 2017

Subject: Electronic Engineering – I

Time: 3 Hours

Max.Marks: 75

Note: Answer all questions from Part – A and any five questions from Part – B.

PART – A (25 Marks)

1	Find the ac resistance for a semiconductor diode having a forward bias of 200 mV and			
	reverse saturation current of 1µA at room temperature.	2		
2	Calculate the band gap energy of silicon at 450° K.	2		
3	Define the following terms:	2		
	a) Ripple factor			
	b) Transformer Utilization Factor			
4	Draw the circuit of bridge rectifier and explain its advantages over other rectifiers.	2		
5	Define r and s for a transistor and derive the relation between them.	3		
6	What is early effect?	3		
7 Derive the expression for the stability factor.				
8	8 How does TRIAC differs from an SCR?			
9	9 In a JFET the I_{D} changes from 1.2 mA to 1.5 mA when V_{GS} is varied from -4.2 V to			
	-4.10V keeping the V_{DS} constant. Determine g_m for a given JFET.	2		
10 Draw the V-I characteristics of N-channel enhancement MOSFET.				
PART – B (5x10 = 50 Marks)				
11	a) Explain V-I characteristics of p-n junction diode. Discuss the temperature dependence of p n characteristics.	э 5+3		
	b) What is Zener breakdown?	2		
12	a) A diode with a forward voltage 0.7 volts is connected as half wave rectifier. The load resistance is 500 ohms and rms ac input is 22 volts. Determine the peak output voltage, peak load current and diode peak inverse voltage.	d It 5		

- b) Draw a neat block diagram of a general purpose CRT and explain function of each block.
- 13 a) Draw and explain output characteristics of common base configuration for npn transistor.
 - b) What is operating point? Derive stability factor for collector to base bias and emitter bias circuits?

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- 14 a) A transistor has its h-parameters given by 1 K Ω , 50, 2.50x10⁻⁴ and 25 μ A/V in common emitter configuration using a load resistance of 5 K Ω and a source resistance of 1 K Ω . Calculate A_V, A_{vs}, A_{Is}, A_{Is}, R_i and R_o.
 - b) Draw the block and symbolic representation for UJT. Sketch the V-I characteristics of UJT.
- 15 a) Draw the structure of a JFET and explain its principle of operation with neat diagrams along with the V-I characteristics. Define pinch-off voltage and mark it on the characteristics.
 - b) Explain the difference between construction of an enhancement type MOSFET and depletion type MOSFET.
- 16 a) Explain the working principle of SCR.b) Draw the circuit diagram of TRIAC and explain its characteristics.

- 17 Write any two of the following:
 - a) Junction breakdown in diodes
 - b) LLC and CLC filters
 - c) BJT as an amplifier
 - d) Tunnel diode.

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B.E. 2/4 (ECE) I - Semester (Suppl.) Examination, May /June 2017

Subject : Electromagnetic Theory

Max. Marks: 75

Note: Answer all questions from Part-A and answer any five questions from Part-B.

Time: 3 Hours

PART – A (25 Marks)

1	Describe the three orthogonal surfaces that define the cylindrical coordinates of a point	(3)
2	Given a vector function $F = a_y(3y - c_1z) + a_y(c_2x - 2z) - a_z(c_3y + z)$, determine	(0)
_	the constants c_1 , c_2 and c_3 if 'F' is irrotational.	(2)
3	Define Electric scalar potential.	(1)
4	What is the basis of Magnetic Scalar Potential?	(2)
5	State Biot-Savart's Law.	(2)
6	Write the integral form of Maxwell's equations, and identify each equation with	า้
	proper experimental law.	(4)
7	What are boundary conditions ? How do boundary conditions arise and how	()
	are they derived?	(3)
8	What is the inconsistency in Ampere's Law? How it is rectified by Maxwell?	(3)
a	A microwave oven operates at 2.45GHz. Assuming $\sigma = 1.1 \times 10^6 75/m$ and	. ,
0	$\mu_{\rm c} = 600$ for the stainless steel interior find the depth of penetration	(3)
10	μ_1 = 000 for the stallness steel interior find the depth of penetration. What is the consequence of a Wave incident on a Perfect Conductor?	(3)
10	what is the consequence of a wave incluent of a reflect conductor.	(~)
	PART – B (50 Marks)	
11	(a) Determine the Electric field intensity of an infinitely long straight, line	
	charge of uniform density Q in air	(5)
	charge of uniform density p_L in all.	(C)
	(b) Point charge find and -2mC are located at $(3, 2, -1)$ and $(-1, -1, 4)$, respectively.	cuvery.
	cloctric field intensity at that point	(5)
		(5)
12	(a) A point charge 5nc is located at $(-3, 4, 0)$ while line $y = 1, z = 1$ carries	
12	(a) A point charge one is located at (-5, $+$, 0) while line y = 1, 2 = 1 carries uniform charge 2nc	(6)
	(i) If $V = 0V$ at $O(0, 0, 0)$ find V at $A(5, 0, 1)$	(0)
	(i) If $V = 100$ V at B (1, 2, 1) find V at C (-2, 5, 3)	
	(b) Derive the expression for the energy density in electrostatic field	(4)
		(')
13	(a) Determine the capacitance per unit length between two long parallel, circu	ılar
	wires of radius 'a'. The axes of the wires are separated by a distance 'D.	(5)
	(b) State and prove uniqueness theorem.	(5)
		~ /
14	(a) Explain the nature of line, surface and volume current distribution as appli	cable
	to static magnetic fields. List out the expressions for the magnetic field inte	ensity
	in these three cases.	(5)

(b) An infinitely long straight conducting rod of radius 'a' carries a current of 'I' in +z direction using Ampere's circuital law find 'H' in all regions.
 (5)

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(5)

- 15 (a) From the Maxell's curl's equation derive the wave equations for an Electromagnetic wave in conducting media. (5)
 - (b) In an medium $E = 16e^{-x/20} \sin (2x10^8 t-2x)i_z V/m$. Find the direction of propagation, the propagation constant, wavelength, speed of the wave and skin depth.
- 16 (a) What is Lorentz's condition? Show that time varying Electric scalar potential and magnetic vector potential satisfy wave equation if Lorentz's condition is assumed.
 (6)
 - (b) A sinusoidal electric intensity of amplitude 250 (V/m) and frequency 1(GHz) exists in a lossy dielectric that has a relative permittivity of 2.5 and a loss tangent of 0.0001. Find the average power dissipated in the medium per cubic meter.
- 17 (a) State and prove Poynting Theorem.

- (5)
- (b) Discuss the determination of the reflected and transmitted wave fields of a uniform plane wave incident normally onto a plane boundary between two material media.
 (5)

B.E. 2/4 (M / P / A.E.) I – Semester (Suppl.) Examination, May / June 2017

Subject: Mechanics of Materials

Time: 3 Hours

Max.Marks: 75

Note: Answer all questions from Part A. Answer any five questions from Part B.

PART – A (25 Marks)

- 1 Write the difference between elasticity and plasticity.
- 2 Write the relation between E, K and [.
- 3 Find the max BM of a simply supported beam subjected to UDL 20 KN/m throughout the span of 20 M.
- 4 Write down the assumption made in theory of pure bending.
- 5 Write down the limitation of double integration method.
- 6 Define modulus of rupture and shear rigidity.
- 7 What is the ratio of maximum shear to average shear of a rectangular section?
- 8 Write the demerits of Mohr's circle.
- 9 Write down the importance of Rankin's constant and reduction factor of a column.
- 10 State principle of super position.

PART – B (5x10 = 50 Marks)

- 11 Derive E = $\frac{9 \text{KG}}{3 \text{K} + \text{G}}$ from the fundamentals. where 'K' is Bulk modulus, 'G' shear modulus, 'E' Young's modulus.
- 12 Draw SFD and BMD of a cantilever beam of span 10 Meters subjected to an UDL of 20 KN/m upto 2 M from the free end and a point load of 30 KN is acting at mid point of a cantilever beam.
- 13 Find the max. deflection of a simply supported beam of span 12 m subjected to two point loads of 10 KN and 5 KN at a distance of 2 m and 6 m from left support.
- 14 At a certain section of a shaft of 100 mm in dia there is twisting moment of 50 KNm and BM of 75 KNm. Determine the principal stresses in the shaft at the section and specify the position of the plane on which they act.
- 15 An I-section beam, has 200 mm wide flanges and an overall depth of 500 mm. Each flange is 25 mm, thick, while the web is 20 mm thick. At a certain section the shear force is 50 KN. Draw shear stress distribution diagram.
- 16 A cylindrical air receiver for a compressor is 2 M internal diameter and made of 12 mm thick, if the hoop stress is not to exceed 95 MPa and axial stress is not to exceed 65 MPa find the maximum safe air pressure.

17 Write short notes on:

- i) Euler's column theory
- ii) Middle third rule
- iii) Bars of uniform strength.

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B.E. 2/4 (CSE) I - Semester (Suppl.) Examination, May/June 2017

Subject : Data Structures Using C+ +

Max. Marks: 75

Note: Answer all questions from Part-A and answer any five questions from Part-B.

Time : 3 Hours

PART – A (25 Marks)

1	Specify the ADT for an Array.	(2)
2	List additional factors that may influence the space complexity of a program.	(3)
3	Identify two real world examples of the applications of a stack.	(2)
4	Give the conditions for identifying a circular queue to be full and empty,	when
	implemented using an array.	(3)
5	When do a pointer in a circular linked list node has a "NULL" valve in it. Justify.	(2)
6	Sparse matrix implementation is benefits using a linked list. Explain why?	(3)
7	Worst case time complexity while using the technique hashing is 0(1) or 0(n).	()
	Justify.	(2)
8	Differentiate between BST, AVL and Red black trees.	(3)
9	Justify the data structure used for computing BFS in a graph.	(2)
10	Summarize the different internal sorting algorithm.	(3)
		. ,
	PART – B (50 Marks)	
	Define Asymptotic metations. Etchenete the role of them in sheeping the right also	
11	Define Asymptotic notations. Elaborate the role of them in choosing the right alg	
	and performance measurement.	(10)
12	(a) Write an algorithm to domenstrate the operations of a stack	(5)
12	(a) Write an algorithm to demonstrate the operations of a stack. (b) Write an algorithm to implement a gueue using 2 stacks	(5)
	(b) white an algorithm to implement a queue using 2 stacks.	(3)
13	Explain the ADT of a doubly linked list and illustrate the process of insertio	n and
	deletion operations, demonstrating full and empty conditions.	(10)
		()
14	(a) Construct a binary search tree for the following integers.	(5)
	10, 4, 9, 20, 30, 26, 29, 27, 25	()
	(b) Delete the following nodes and show the restructuring of BST	(5)
	4, 15, 26, 20, 10	()
15	Compare Prim's and Kruskal's algorithm for finding minimum cost spanning tree.	(10)
4.0		
16	(a) Write an algorithm to represent a lower triangle and tridiagonal matrix using	(5)
	arrays.	(5)
	(b) Explain few applications of queues.	(5)
17	Write short notes on the following:	(10)
.,	(a) Heap	(10)
	(b) Equivalence classes	
	(c) Graphs Representation	

FACULTY OF INFORMATICS

BE 2/4 (IT) I Semester (Suppl.) Examination, May / June 2017 Sub: Micro Electronics

		SUD: MICTO Electronics.	
1	Fime : 3	3 Hrs. Max. Marl	ks : 75
Not	e: Ans	wer All Questions From Part- A and Any Five Questions from Pa PART – 'A' (25 Marks)	rt-B.
1.	List diffe	erent types of diode models.	(2)
Ζ.	Filter ?		(3)
3.	Define	α and β .	(2)
4. 5.	Using p Disting	otential barrier diagram define 'early effect' iish between negative/degenerative and positive/regenerative	(3)
	feedbac	xk.	(3)
6. 7.	Define I What a	oop gain. e the ideal characteristics of an ideal op. Amp.	(2) (2)
8.	Draw th	e circuit of an op. Amp. As summer, for adding two voltages.	(3)
9.	List diffe	erent types of logic families.	(2)
10.	Define I	Noise Margins for logical '0' and logical 'l' state (ie NM _L and NM _H)	(3).
		PART – B (50 Marks)	
11.	a)	Explain the behavior of p-n junction diode under forward and	(5)
	b)	Draw a 2-way clipper circuit and explain.	(5)
	2)		(0)
12.	a)	Draw the input and output characteristics of a transistor under CE	
		Configuration and explain.	(5)
	b)	Why biasing is required ? How do you fix the Q-point.	(5)
13.	a)	What is the significance of Barkhausan's criteria for oscillations.	(4)
	b)	With a neat circuit diagram explain the working of R.C. phase	$\langle \mathbf{O} \rangle$
		shift oscillator.	(6)
14.	a)	Draw a comparator circuit using an op. Amp. And explain. How	(-)
		Do you construct a zero level detector using comparator?	(5)
	D)	as a mono-stable multi-vibrator.	(5)
15	a)	Give the general structure of CMOS logic circuit and explain	(5)
15.	a) b)	Design a CMOS for the expression $f=[a+be]'$	(5)
	0)		(0)
16.	a)	Draw the circuit of a full wave rectifier and explain its operation.	(6)
		Draw the input and output wave forms. Derive expressions for	
		Its average value, rms value of output voltage and current, ripple	
	b)	Recipion the MOSEET model in linear and saturation region	(A)
			(+)
17.	Write de	etailed notes on the following :	
	(a) (b)	Ulass o push pull amplifier. Hybrid model and its application	(5) (5)
	(0)	*****	. (5)