Code No : 11380

FACULTY OF ENGINEERING

B.E II-Semester (Suppl.) Examination, November / December 2018

BUSINESS COMMUNICATION AND PRESENTATION SKILLS

TIME: 3 Hours

- Note: (i) Answer all questions from Part-A and any Five questions from Part-B
 - (i) Answers to the questions of Part-A must be at one place and in the same order as they occur in the question paper
 - (ii) Missing data, if any, may be suitably assumed.

PART-A (20 Marks)

a. Choose the right option for the sentences given below.

- Which of the following is best explains downward communication?

 a.message sent from the board of directors to the lowest level of employees
 - b. a message from a subordinate to a superior
 - c. a message from a company officer to a company employee
 - d. a message that follows along an organization's chain of command
- 2. Which of the following is a disadvantage of downward communication?
 - a. Efficiency
 - b. Case a delegation
 - c. Clarity
 - d. Support

b. Choose the right option for the sentences given below

- 1. To be a successful conversationalist one should be the workplace
 - a. submissive
 - b. assertive
 - c. aggressive
 - d. rude
- 2. "Tease" or "stress" questions are intended to judge
 - a. the candidate's intelligence quotient
 - b. how the candidate handles them
 - c. the candidate's technical skill
 - d. the candidate's stress level

c. State whether the following are true or false.

- a. The first objective in a group discussion is to act as a self appointed leader of the group.
- b. The group discussion evaluates the candidate's ability to argue with others.

d. Choose the right option for the sentences given below

- 1. The best way to apply for a job is to submit a resume that is
 - a. full of personal information
 - b. self-recommending
 - c. specifically written for that particular job
 - d. suitable for any job

Max. Marks: 70

(1x2=2)

(1x2=2) eader of

(1x2=2)

(1x2=2)

2. A summary placed at the beginning of the CV acts as a

- a. preface
- b. statement of objectives
- c. synopsis
 - d. letter of recommendation

e. Match the following

А

- 1. Bonding a. asking questions to gain information
- 2. Experimenting

b. In

- b. formal announcement of the relationship
 - c. get into a shared relational identity

B

d. revealing themselves fully

f. Fill in the blanks with appropriate words

a. Which quadrant of the Johari window reveals information about one's secret dreams and ambitions______

_____ stage team members work according to rules.

g. Directions:

3. Intensifying

4. Integrating

- 1. Use all the phrases/hints given
- 2. Minimum words should be 50 otherwise your email cannot be validated
- 3. Addressing and signing should be done as in the question given.
- 4. Common grammatical rules, punctuation should be according to standard English.
- 5. You can use your own phrases along with the phrases given below.

As an intern at ABC consulting Pvt.Ltd, write an email to your internship project Manager.

Mr. Ramesh, informing about the progress that you are making and some difficulties that your are encountering. Sign the email as Sharma.

Outline:

Thank – challenging – progress – tight schedule – support – report – analytics – guidance- access – doubt – requirements – design.

Write short notes on the following:

h. Importance of Time Management

- i. How is Persuasion Technique useful for you as an engineering student? Discuss with an example.
- j. Why is Group Discussion conducted in job selection? What are different traits that are observed during the Group Discussion?

PART-B (10x5=50 Marks)

- 11 Discuss Johari Window and its various quadrants giving suitable examples.
- 12. Elucidate different styles of communication? Which communication style do you think is healthy? Why?

(2x1=2)

(1x2=2)

(1/2x4=2)

(2x3=6)

6)

- 13. Explain the differences between Delphi technique and nominal group technique in detail?
- 14. Write statement of purpose as a student who aspires to pursue MS in Computer Science at the Boston University.
- 15. Write a job application letter along with suitable resume to Sofsol Solutions, Banglore applying for the position of a software engineer.
- 16. What are the ins and outs of a good and well rated group discussion?
- 17. As the Head, Department of English, RKCT, write a report to the Principal of the college on the feasibility of opening a new multimedia language laboratory in your department.

B.E. 2/4 (Civil) I - Semester (Backlog) Examination, November / December 2018

Subject : Engineering Materials and Construction

Time : 3 Hours

Max. Marks: 75

Note: Answer all questions of Part - A and answer any five questions from Part-B.

PART – A (25 Marks)

- 1 List the requirements of good stones?
- 2 What are the requirements of good earth for making bricks?
- 3 What are the advantages and disadvantages of concrete block masonry over brick masonry?
- 4 List out the various tests on aggregates.
- 5 How the reinforced steel is stored and handled?
- 6 Write on hydration of cement.
- 8 List out the different types of floors.
- 9 How many types of distempers are there? What are they?
- 10 Distinguish between 'form work' and 'scaffolding

PART – B (50 Marks)

- 11 (a) Discuss various types of brick and stone masonry with neat sketches.(b) How are bricks classified? How do they differ in compressive strength?
- 12 (a) Explain the chemical composition of cement?(b) Explain different methods of measurement of moisture content of aggregates.
- 13 (a) Explain any two tests conducted to ensure the quality of motars?(b) What is meant by workability of concrete? What are factors affecting workability?
- 14 (a) Explain the method of constructing of concrete and marble flooring.(b) What are scaffoldings? Explain the different types of scaffolding with neat sketches.
- 15 What is Varnish ? Discuss in detail the various types of Varnishes and its applications.
- 16 Name any five common stones used in civil engineering works and explain their characteristics and used.
- 17 Write short note on the following:
 - (a) Light weight aggregate
 - (b) Recycled materials
 - (c) White Washing
 - (d) Form work reuses and maintenance
 - (e) Bleeding

BE 2/4 (EEE) I-Semester (Backlog) Examination, November / December 2018

Subject : Electrical Circuits-I

Time: 3 Hours

Note: Answer All Questions From Part-A & Any Five Questions From Part-B

PART-A (25 Marks)

- 1 Explain Linear and Active elements with examples. 2 2 Define Real and Reactive power 2 3 Derive the expression for energy stored in capacitor 3 3 4 Obtain the RMS and Average values of periodic waveform shown V(t) 20Vt(S)Зл 2π π
- 5 Write the relation between line and phase voltages and Currents in Star and Delta circuits 2 3
- What are the advantages of three phase over single phase 6
- State Milliman's theorem 7
- Obtain the Incidence matrix of the following figure 8



- 9 Define coupling coefficient and write the relation between mutual inductance and coupling coefficient 2
- 10 List the formulae of resonant frequency, Quality factor and Bandwidth of a series RLC circuit 3

PART-B (50 Marks)

Explain Determine the mesh currents in the circuit shown below 11



....2

10

3

2

Max. Marks: 75

7

3

6

4

3

7

- 12 A series combination of 12Ω , 600 μ F and 100 mH are connected to a 220 V, 50 Hz supply. Estimate the current, active power, reactive power and apparent power and also draw the phasor diagram 10
- 13 a) A3- 400V load has a power factor of 0.4lag. Two wattmeters are connected to measure the power. If the input power is 10KW, find the reading of the each instrument.
 - b) Differentiate Balanced and Unbalanced loads.
- 14 a) State and explain thevenins theorem with exampleb) Obtain the Cut-set matrix of the graph shown



- 15 a) What is resonance? Derive the expression for resonant frequency in parallel RLC circuit
 - b) Determine the voltage across the capacitor in the circuit shown below.

- 16 a) What is Locus diagram? Sketch the Locus diagram of series RC circuit with variable capacitance
 - b) State and explain Tellegen's theorem with an example
- 17 Write short notes on
 - a) Tie-set matrix
 - b) Star Delta transformation
 - c) Self and Mutual Inductance

- 3 4

3

5

5

B.E. 2/4 (EIE) I-Semester (Backlog) Examination, November / December 2018

Subject : Network Theory

Max. Marks: 75

(2)

(2)

(3)

(3)

(3)

(3)

(2)

Note: Answer all questions from Part-A & any five questions from Part-B.

PART – A (25 Marks)

- 1 State and explain superposition theorems.
- 2 Draw dual of a series RL circuits.

Time : 3 Hours

- 3 Explain response to arbitrary inputs by convolution.
- 4 Draw power triangle for AC circuit and explain each one of it.
- 5 Write two port equation for ABCD parameters in terms of Z-parameters.
- 6 Explain impedance curve for series RLC circuits.
- 7 Determine the value of IR if the potential drop across it is 25V.



Figure

- 8 The power in $3-\phi$ circuit is measured using 2-Wattmeters. If the total power is 100KW and power factor is 0.66 leading, what will be the reading of each wattmeter. (2)
- 9 The Z-parameters of circuit are given by $Z_{11} = 4\Omega$, $Z_{12} = Z_{21} = 10\Omega \& Z_{22} = 6\Omega$, obtain transmission parameters.
- 10 Derive impedance of series RL circuits also draw vector diagram for the same. (3)

PART – B (50 Marks)

11 Find Thevenin's equivalent for the circuit shown.



(10)

(2)

(10)

12 Find R.M.S. value, Average value, Peak factor & Form Factor for the given waveform.



13 (a) Find total current in the circuit shown also find voltage across terminals A & B. (6)



(b) Find Req between terminals A & B.



14 In the circuit shown, the switch is moved to position b at t = 0, determine the current (t) also find $i_{L}(0)$.



(4)

(10)

- 15 Prove that power factor of a $3-\phi$ star connected load is Tan⁻¹[$\sqrt{3}(W_2 - W_1) / (W_1 + W_2)$] with the help of neat circuit and phasor diagram.
- 16 Find Y-Parameters for the circuit shown.



B.E 2/4 (ECE) I semester (Backlog) Examination, November/ December 2018

Subject : Basic Circuit Analysis

Time: 3 Hours

Note : Answer all Questions From Part-A, & Any Five Questions From Part-B.

Part – A (25 Marks)

1.	A DC voltage of 8V exists across a 10 Ω resistor from t=0 onwards. Calculate the	
	current through the resistor & Power dissipated by it.	2M
2.	Define the following terms and give one example for each.	ЗM
		-
	A.	I
	ree of a graph B. Fundamental He-Set.	
3.	Explain the three types of damping in RLC CRts & give conditions.	3M
0.		0
4.	In the circuit of figure (1) find the current I in the steady state.	2M



- 5. Explain Dot convention used for the analysis of magnetically coupled circuits. 2M
- 6. The current in a series circuit of 10 Ω and 1 mH is 5 Cos (100t + 30⁰);Amp. Find the value of power factor. 3M
- 7. For the given Π -net work of figure 2 find the equivalent T-network. 2M



- 8. Draw the equivalent circuit and a z parameter network.
- 9. Identify all the complex frequencies of the current signal $i(t) = 10 + e^{-3t} + \cos 20t$. 3M

Contd...2

Max. Marks :75

10. Define resonance in a series RLC circuit



$$i_L(0^-) = 5A$$

b) In the circuit of figure 6 find di/dt (o+) and d^2i / dt^2 (0⁺) 4M

14. Find the phasor current I using Thevenin's theorem in the circuit of figure (7) 10M



15 a) The Two port network of figure (8) is connected in series with an exactly similar two-port network. Find the overall Z-parameters of this overall Two-port network. 5M



- b) Express the hybrid parameters h₁₁ and h₂₁ in terms of Z-parameters. 5M
- 16. a) Derive an expression for the resonant frequency of the circuit of figure (9). 4M

Figure-9



c. Maximum Power Transfer Theorem. 3M

B.E 2 /4 (M / P /A.E) I-Semester (Backlog) Examination, November/December 2018

Subject: Metallurgy and Material Science

Time: 3 Hours

Max. Marks: 75

Note: Answer all questions from Part A and any five questions from Part B PART-A (25 Marks)

- 1. List various types of defects in crystals.
- 2. Differentiate ductile fracture with brittle fracture.
- 3. Define Fatigue? List the factors that affect the fatigue of metals.
- 4. State and explain Fick's Ist law of diffusion.
- 5. Write the three invariant reactions that occur in Iron-Iron carbon alloy system.
- 6. Explain the classification of plain carbon steels.
- 7. What is full annealing?
- 8. Explain the need of a tempering a hardened steel.
- 9. Draw a neat sketch of Puddling furnace.
- 10. What are the various composite materials?

PART-B (5 x 10= 50 Marks)

11.a) What is critical shear stress? Derive the equation relating tensile stress and critical resolved shear stress

- b) Distinguish between cold working and hot working. What are the advantages of cold working compare to hot working.
- 12.a) Explain the experimental determination of fatigue strength with the help of a sketch.
 - b) Discuss the applications of diffusion in mechanical engineering field.
- 13.a) Explain the construction of phase diagram of lead (Pb) and Tin (Sn) alloy system with a neat sketch.
 - b) What are the different types of cast irons? List the characteristics of Cast Iron.
- 14.a) Discuss Normalizing as heat treatment process and mention its applications.
 - b) Explain Austenite to Martensite transformation of eutectoid steel with the help of TTT diagram.
- 15. Explain with neat diagram the production of cast iron in cupola furnace.
- 16.a) What is surface hardening? Explain the flame hardening process.
 - b) Explain the method of production of steel by Electric arc furnace.
- 17. Write short notes on the following
 - a) Bauschinger effect
 - b) Stress rupture curve.
 - c) High speed steels.

B.E. 2/4 (CSE) I - Semester (Backlog) Examination, November / December 2018

Subject : Discrete Structures

Max. Marks: 75

Note: Answer all questions from Part-A & any five questions from Part-B. PART – A (25 Marks)

1	Show the implication $(P \land Q) \Rightarrow (P \rightarrow Q)$.	(2)		
2	Obtain contra positive and inverse for the statement:	(3)		
	If you have exam then you must work hard			
3	Let f be the function from $\{a, b, c, d\}$ to $\{1, 2, 3, 4\}$ with $f(a) = 4$, $f(b) = 2$, $f(c) = 1$			
	and f(d) = 3. Is f bijecitve function? How?	(3)		
4	What is Asymmetric relation ? Give example.	(2)		
5	Obtain Recurrence relation for the closed form	(2)		
	$A(K) = K^2 - K$	()		
6	Find the $co=efficient$ of x^5 in $(1 - 2x)^{-7}$?	(3)		
7	Obtain a multiplication table for a monoid using binary operation			
	* on set $A = \{a, b\}$.	(3)		
8	What is Automorphism?	(2)		
9	Find the chromatic number of a wheel graph.	(3)		
10	Draw the complement graph for K_3 .	(2)		
PART – B (50 Marks)				
11	Prove the validity of the argument	(10)		
	$\forall x[p(x) \lor q(x)]$			
	$\forall x[(\sim n(x) \land n(x)] \rightarrow r(x)]$			
	$\therefore \forall x[\sim r(x) \rightarrow p(x)]$			

- 12 Find the no. of integers between 1 and 1000 inclusive that are non divisible by 2, 3 and 5?
- 13 Solve D(k) 8D(k-1)+16D(k-2)=0 where D(2)=16, D(3)=80? (10)
- 14 Define the binary operation o on Z by x o y = x + y + 1. Verify that < Z, o > is an abelian group. (10)
- 15 State and prove Lagrange's theorem with an example.
- 16 Use Grinberg's theorem show that there are no planar Hamiltonian graphs with region of degree 5, 8, and 9 with exactly one region of degree 9? (10)
- 17 (a) Find the dual of the graph shown below:

Time: 3 Hours



(b) Prove that intersection of two sub-monoids is a monoid.

(4)

(10)

(10)

(6)

B.E. 2/4 (I.T) I-Semester (Backlog) Examination, November/December 2018

Subject : Digital Electronics and Logic Design

Time: 3 Hours

Max. Marks: 75

Note: Answer all questions from Part-A and any five questions from Part-B.

PART – A (25 Marks)

1. 2. 3. 4. 5. 6. 7. 8. 9. 10.	State Demorgan's Laws. Neatly draw the general structure of PAL. What are universal gates? Explain. Design a Full Adder circuit. What is a state table.Give an example. Give the functionality of SR-Latch. List the elements of ASM Charts. Write the VHDL Code for 4x1 multiplexer. Define setup and hold time of a Flip-Flop. Explain about dynamic hazard with example.	 (2) (3) (2) (3) (2) (3) (2) (3) (2) (3) (2) (3)
	PART-B (50 Marks)	
11.	 a). Simplify the four- variable function using K-Map and implement using AOI gates. f (x1, x2, x3, x4) = ∑m (1,4,5,6,7,10,11,14). b). Draw the logic circuit of a 2x4 decoder using logic gates. 	(7) (3)
12	a) Explain the structure of CPLD in detail.b) Neatly draw the 3-input LUT	(5) (5)
13.	a) Explain the edge-triggered D Flip-Flop.b) Design a 3-bit Down Counter.	(5) (5)
14.	a) Explain in detail about JK-Flip-Flop and T-Flip-Flop.b) Explain the operation of parallel access shift register with a neat diagram.	(5) (5)
15.	a) Explain about state assignment problem in detail.b) Explain about the ASM charts.	(5) (5)
16.	Explain in detail about Analysis, Synthesis, State reduction of Asynchronous sequential circuits.	(10)
17.	Write Short notes on the following. a) Clock Synchronization. b) Asynchronous Sequential Circuits.	(10)
