

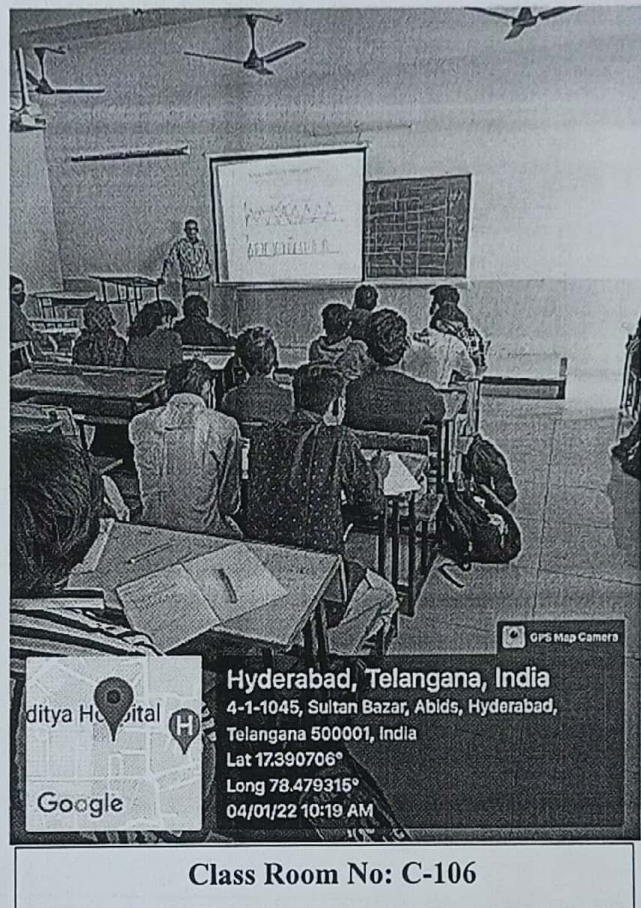


Estd : 2008

Accredited by NAAC with A+ and NBA
 Affiliated to Osmania University & Approved by AICTE

Classroom:

A.Y.: 2021-22



[Signature]
Faculty In-charge

[Signature]
Criterion In-charge



METHODIST COLLEGE OF ENGINEERING & TECHNOLOGY
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE FOR B.E V-SEMESTER FOR THE ACADEMIC YEAR 2021-2022.

Class Time Table

Class: B.E V-SEM ECE-A

A.Y: 2021-22

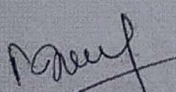
W.E.F:- 08-11-2021

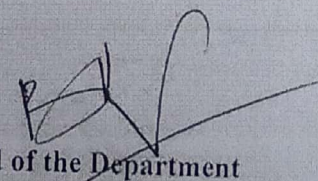
LH:- C-106

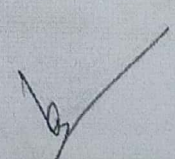
	I 9:30-10:30	II 10:30 - 11:30	III 11:30-12:30	IV 12:30-1:15	V 1:15-2:15	VI 2:15-3:15	VII 3:15-4:15	VIII 4:15-5:15
MON	SSP LAB-A2/MPMC LAB-A1		DSP	L U N C H	ACS	MPMC	AWP	MP/RC
TUE	AC	ACS	MPMC		DSP	AWP	AWP	MP/RC
WED	AC	MPMC	ACS		AWP	AC	DSP	MP/RC
THU	DSP	AWP	AC		MPMC	SSP LAB-A1/MPMC LAB-A2		MENTOR
FRI	ACS	AWP	AC		MPMC	ACS	DSP	MENTOR
SAT	MPMC	DSP	ACS		AC	MINI PROJECT		MENTOR

*(T) – Tutorial *(A) – Additional Class *(RC) – Remedial Class *(MP) – Mini Projects

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
PC501EC	Analog Communication	Ch Suresh	LIB	LIBRARY	Maharshi Sanand
PC502EC	Digital Signal Processing	I Srikanth	SPORTS	SPORTS	P Ramesh Babu
PC503EC	Automatic Control Systems	Dr B Krishnakumar	MENTOR	MENTORING	Maharshi Sanand/Dr John William Carey/Ramesh Malyala/Sameed shaik/O ameena
PC504EC	AWP - Antennas and wave propagation	Dr Ravi M Yadahali			
PC505EC	MPMC- microprocessor And Microcontroller	Zeenath			
PC551EC	Systems and Signal Processing Lab	I Srikanth-A1&A2/CH Suresh- A1/Satyanarayana-A2			
PC552EC	microprocessor And Microcontroller Lab	Zeenath-A1&A2/....			
PC553EC	Mini Projects	Dr Ravi Yadahali	C-I	Class Incharge	Zeenath


Time Table Coordinator


Head of the Department


Principal



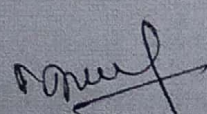
METHODIST COLLEGE OF ENGINEERING & TECHNOLOGY
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE FOR B.E VI-SEMESTER FOR THE ACADEMIC YEAR 2021-2022.
Class Time Table

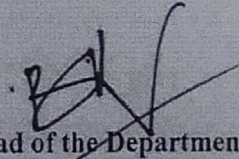
Class: B.E VI-SEM ECE-A A.Y: 2021-22 W.E.F:- 28-03-2022 LH:-C-106

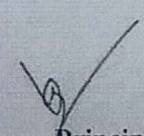
	I	II	III	IV	V	VI	VII
	9:30-10:30	10:30 - 11:30	11:30-12:30	12:30-1:15	1:15-2:15	2:15-3:15	3:15-4:15
MON	DSDV, LAB-A2/DC LAB-A1		DM		EMI	DC	SPORTS
TUE	DCCN LAB-A2/DSDV LAB-A1		DC		IOT	EMI	LIB
WED	DM	IOT	DCCN		DSDV	DCCN LAB-A1/DC LAB-A2	
THU	DC	DCCN	DSDV		EMI	IOT	INT
FRI	DSDV	DM	EMI		DCCN	FOSS	FOSS
SAT	DCCN	DM	IOT		DC	DSDV	LIB

(T) – Tutorial(A) – Additional Class*(RC) – Remedial Class *(MP) – Mini Projects

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
PC601EC	Digital Communication	Mr.CH Suresh	LIB	LIBRARY	Mr.Maharishi Sanand
PC602EC	Digital system Design with Verilog	Mr.Maharishi Sanand	SPORTS	SPORTS	Mr.P.Ramesh Babu
PC603EC	Data Communication and computer networking	Mr.T.Venugopal	MENTOR	MENTORING	Ramesh Malyala/ Maharishi Sanand/ B.Satyanarayana
PC604EC	Electronic Measurements and Instrumentation	Dr. K.Padmavathi	FOSS	FREE OPEN SOURCE SOFTWARE	
PE – I	IoT Sensors	Mrs. Zeenath			
OE – I	Disaster Management	Mr. Bharath naik	INT	INTERNET	
PC651EC	Communication Lab	Mr.CH Suresh(A-1&A-2)/ O.Ameena(A-1)Mr.T.Sravankumar(A-2)	RC	Remedial Class	
PC652EC	DCCN Lab	Mr.T.Venugopal(A-1)/ Mrs. Zeenath(A-2)			
PC653EC	Digital system Design with Verilog Lab	Mr.Maharishi Sanand (A-1&A-2)/Mr.I.Srikanth(A-2)/Mr.M.Maheshbabu(A-1)			
			C-I	Class Incharge	Dr. K.Padmavathi


Time Table Coordinator


Head of the Department


Principal