

METHODIST COLLEGE OF ENGINEERING & TECHNOLOGY [Autonomous Institution]

Estd:2008

Accredited by NBA & NAAC with A+ Grade

Approved by AICTE New-Delhi & Affiliated to Osmania University

COMPUTER SCIENCE & ENGINEERING DEPARTMENT

Date: 23-08-2022

Circular

All the faculties are hereby informed that there will be a Department Committee meeting on 24/08/2022 in R&D Lab from 11:00 AM. I request everyone to attend it without fail. The following is the agenda of the meeting.

Agenda:

1. Subject Allocation
2. Coordinators Allocation
3. Value Added Courses Discussion
4. Technical Events for forth coming semester
5. Effective Mentoring
6. Course files Submission
7. Class Coordinators Report Submission
8. Non-Teaching Lab staff Subject Allocation
9. Any other discussion with the permission of the undersigned

Department of
Methodist College
Abids, Hyderabad
HOD-CSE
23/08/2022

S. No.	Name of the Employee	Designation	Signature
1	Dr.G.Ravinder Reddy	Professor	
2	Dr. Sharada Varalakshmi	Professor	
3	Dr. Ramakanta Mohanty	Professor	
4	Dr.Syed Azahad	Assoc.Prof	
5	Dr.Diana Moses	Assoc. Prof.	
6	Dr. Shaik Khaleel Ahamed	Assoc.Prof	
7	Dr. T V R Himabindu	Assoc.Prof	T.V.R.Himabindu 23/08/22
8	Dr.Shruthi S.K.	Asst.Prof	
9	T Praveen Kumar	Asst. Prof.	
10	G Saritha	Asst. Prof.	
11	D Rajashekar	Asst. Prof.	
12	Ravikanti Sandeep	Asst. Prof.	
13	Unnati Mohan K	Asst. Prof.	
14	P.V. Ramanaiah	Asst. Prof.	
15	B Sowjanya	Asst. Prof.	
16	A. Rajesh	Asst Prof	
17	M.V.D.S Krishna Murty	Asst Prof	
18	Mrs.J Sowjanya	Asst.Prof	
19	Ms.Deepthi Joshi	Asst.Prof	
20	Ms.Shaziya Jabeen	Asst.Prof	
21	Ms.Maleka Anjum	Asst.Prof	(Maternity Leave)
22	Mr.Shaik Rasool	Asst.Prof	
23	Ms.B Vasavi Sravanthi	Asst.Prof	
24	Mr.U Moulali	Asst.Prof	
25	Srinu Dharavath	Asst.Prof	
26	Ms.A Lalitha	Asst. Prof.	
27	Mr. Venkatram Vennam	Asst. Prof.	
28	Mrs. A.Sowjanya	Asst. Prof.	
29	K.Uday Kumar	Asst. Prof.	
30	C Sravanthi	Asst Prof	
31	Mr.A.A.R Senthil Kumar	Asst Prof	

Minutes of the meeting

1. HOD madam welcomed all the faculties and new joiners to the Department of CSE.
2. Madam gave a gist of strength of all the four year intake and the other branches which are coming under CSE umbrella like
 - ① Ist year - CSE (180), AIKDS (180), AIRML (60)
 - ② IInd year - CSE (120), AIKDS (120)
 - ③ IIIrd year - CSE (120), AIKDS (60)
 - ④ IVth year - CSE (120)
3. In the academic meet council meeting it is announced that all the branches will come under CSE umbrella as Centre of excellence for AI.
4. Service subjects PPS (theory & lab) will be given for EEE, Civil and Mechanical branches.
5. All the subject Allocation for the 2022-23 odd semester was done for all the faculties.
5. SOC cell was introduced from IIIrd semester in autonomous structure and HOD man explained about the cell and asked to conduct projects with IOT club.
7. Coordinators for different committee were listed out by HOD madam. like PAC, DAC, Finance, RLO Committee Cell etc.
8. All the faculties were asked to submit their course files for 2021-22 even semester.
9. Dr. Ranabhat Mahanty suggested to have a KTS session for all the faculties so that research areas can be explored & it will help other faculties to complete their research work.

10. Exam branch coordinators were asked to submit all the files and result analysis to be done for previous semester.
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Department of CSE
Methodist College of Engg & Tech
Abids, Hyderabad
24/08/22



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College Code: 1607


Dt: 15-10-2022

**Revised ALMANAC - Autonomous
(For the Academic Year 2022-23)
B.E. – III Semester (All Branches)**

III Semester

S.No	Details	Date of Commencement
1	Commencement of Class work	10-10-2022
2	CIE (Internal Test)- I	21-11-2022 to 24-11-2022
3	Display of CIE – I marks on or before	30-11-2022
4	CIE (Internal Test)- II	16-01-2023 to 19-01-2023
5	Last day of instruction	01-02-2023
6	Display of total CIE and Submission of Attendance to Examination cell	25-01-2023
7	Preparation and Practical Examination	23-01-2023 to 04-02-2023
8	Commencement of Theory Examinations	06-02-2023
9	Commencement of IV Semester	07-03-2023




Principal
METHODIST COLLEGE OF ENGG. & TECH.
King Koti Road, Abids, Hyderabad,

Copy to:

- 1.The Director
2. All HoD's – for circulation
- 3.AO/Exam cell/SWO/TPO/Library

ODD Sem Faculty Subject Preference form for AY 2022-23

Dear All

Greetings

I am hereby sharing the subject preference form , kindly request all faculties to fill the form by EOD

Thanks and Regards
Dr P Lavanya

* Indicates required question

1. Email *


2. Name of the Faculty *

3. Designation *

Mark only one oval.

- Professor
- Associate Professor
- Assistant Professor

4. Phone Number *


Head of the Department
Department of CSE
Methodist College - Engg & Tech
Abids, Hyderabad.

5. Over All Experience *


6. Experience in Methodist *

7. Previously Taught Subjects *

8. III Sem Subject Preference *

Mark only one oval.

- III Sem CSE - 3PC301CS Database Management Systems(Theory + Lab)
- III Sem CSE - 3PC302CS Discrete Mathematics
- III Sem CSE - 3PC303CS Computer Organization and Microprocessor(Theory + Lab) CSE
- III Sem CSE - 3PC353CS Python Programming Lab
- III Sem CSE - 3PW354CS Skill Development Course- I (IOT) Lab
- III Sem AD - 1PC301AD Discrete Mathematics
- III Sem AD - 1PC302AD Database Management Systems (Theory + Lab)
- III Sem AD - 1PC303AD Computer Organization and Microprocessor(Theory + Lab)
- III Sem AD - 1PC352AD Python Programming Lab
- III Sem AD - 1PW354AD Skill Development Course- I (IOT) Lab
- III Sem Programming for Problem Solving- EEE , CIVIL , MECH


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9. V Sem Subject Preference *

Mark only one oval.

- CSE V Sem - PC 501 CS Software Engineering (Theory + Lab)
- CSE V Sem - PC 502 CS principles of Programming Languages
- CSE V Sem - PC 503 CS Automata Languages & Computation
- CSE V Sem - PC 504 CS Artificial intelligence(Theory + Lab)
- CSE V Sem - PC 505 CS Computer Networks(Theory + Lab)
- V Sem AD -PC501AD Software Engineering
- V Sem AD - PC502AD Database Management System(Theory + Lab)
- V Sem AD - PC503AD Artificial Intelligence(Theory + Lab)
- V Sem AD - PC504AD Automata languages & Computation
- V Sem AD - PC505AD Computer Vision

10. I Sem & VII Sem Subject Preference *


Mark only one oval.

- CSE VII Sem - PC 701 CS Information Security
- CSE VII Sem - PC 702 CS Data Science Using R Programming
- CSE VII Sem - PC 703 CS Distributed Systems
- Programming for Problem Solving- AI & DS , CSE , AI & ML

11. Professional Elective - I Preferences *

Mark only one oval.


- PE 515 CS Data Science
- PE 516 CS Blockchain Technology
- PE 514 CS Object oriented Analysis and Design
- PE511AD Data Visualization
- PE515AD Foundation of Cryptography
- PE512AD Pattern Recognition and Neural Networks


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Department of CSE
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12. Remarks if any

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Methodist College of Engg & Tech
Abids, Hyderabad.

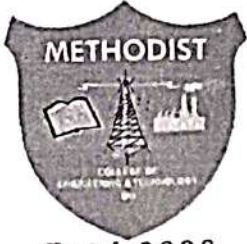
Faculty Subject Preferences 22-23

Timestamp	Email address	Name of the Faculty	Designation	Phone Number	Over Experience All in one Expertise Method List	Previously Taught Subjects	III Sem Subject Preference	V Sem Subject Preference	I Sem & VII Sem Sui	Professional Elective Remarks, if any
8-11-2022 12:17:26	praveen@gmail.com	P V Ramanaiah	Assistant Professor	9010201216	13	C++ , oops using 1) Java, doms, jsp, jdbc, dao, python, 7) Linux and shell programming	III Sem CSE - 3PC313CS Python Programming Lab	V Sem Subject Preference CSE V Sem - PC 503 CS Automata Languages & Computation	CSE VII Sem - PC 71 Cryptography	PE515AD Foundation of Cryptography
8-11-2022 12:20:32	praveenkumar@methodist.edu.in	T PRAVEEN KUMAR	Assistant Professor	9885456186	14+	1) DATA STRUCTURES 2) JAVA 3) DBMS 4) OS 5) SE 6) DATA SCIENCE 7) DATA MINING	III Sem CSE - 3PC310CS Database Management Systems(Theory + Lab)	CSE V Sem - PC 504 CS Artificial Intelligence(Theory + Lab)	CSE VII Sem - PC 71 Visualization	PE511AD Data Visualization
8-11-2022 12:34:01	rajshakar@methodist.edu.in	Rajshakar D	Assistant Professor	9491827232	14.5	9 OS, DM	III Sem CSE - 3PC313CS Python Programming Lab	CSE V Sem - PC 501 CS Software Engineering (Theory + Lab)	CSE VII Sem - PC 71 Science	PE 515 CS Data Science
8-11-2022 12:37:08	lavanya.posa@gmail.com	Dr Lavanya	Professor	8985042735	18	12 OS, DSR, DS, DM, IRS	III Sem CSE - 3PC313CS Python Programming Lab	V Sem AD - PC503AD Artificial Intelligence(Theory + Lab)	CSE VII Sem - PC 71	PE512AD Pattern Recognition and Neural Networks
8-11-2022 12:40:42	jsowjanya2593@gmail.com	J Sowjanya	Assistant Professor	9133402803	4	1.5 CO, DCCN, CNS, SE	III Sem AD - 1PC303AD Computer Organization and Microprocessor(Theory + Lab)	V Sem AD - PC501AD Software Engineering	CSE VII Sem - PC 71 Cryptography	PE515AD Foundation of Cryptography
8-11-2022 12:41:48	psd.sandeep@gmail.com	Dr Sandeep Ravikanth	Assistant Professor	9951231878	9.9 Years	Computer organization, Software Engineering and Information security	III Sem CSE - 3PC303CS Computer Organization and Microprocessor(Theory + Lab)	CSE V Sem - PC 501 CS Software Engineering (Theory + Lab)	CSE VII Sem - PC 71 Cryptography	PE515AD Foundation of Cryptography
8-11-2022 12:48:59	moulali@methodist.edu.in	U Moulali	Assistant Professor	9989045779	15	6 LINUX, CO	III Sem CSE - 3PC301CS Database Management Systems(Theory + Lab)	CSE V Sem - PC 504 CS Artificial Intelligence(Theory + Lab)	CSE VII Sem - PC 71 Science	PE 515 CS Data Science
8-11-2022 12:49:33	mkrishnamurthy@methodist.edu.in	M Krishnamurthy	Assistant Professor	9441427954	20	3 ML, SE, ML, DA, ACS, CC	III Sem CSE - 3PC313CS Python Programming Lab	CSE V Sem - PC 504 CS Artificial Intelligence(Theory + Lab)	CSE VII Sem - PC 71 Science	PE 515 CS Data Science
8-11-2022 12:49:46	srinudharavahuphd@gmail.com	SRINU DHARAVATH	Assistant Professor	9000530529	14	6 DAA, DML, AI	III Sem CSE - 3PC301CS Database Management Systems(Theory + Lab)	CSE V Sem - PC 504 CS Artificial Intelligence(Theory + Lab)	CSE VII Sem - PC 71	PE 515 CS Data Science
8-11-2022 12:58:54	venkatram@methodist.edu.in	Venkatram	Assistant Professor	9618355533	5	Information retrieval system and human computer interaction, operating system, computer networks	III Sem AD - 1PC301AD Discrete Mathematics	CSE V Sem - PC 501 CS Software Engineering (Theory + Lab)	CSE VII Sem - PC 71	PE 514 CS Object oriented Analysis and Design
8-14-2022 22:07:28	asowjanya@methodist.edu.in	A Sowjanya	Assistant Professor	6304020921	6 yrs	0	III Sem CSE - 3PC301CS Database Management Systems(Theory + Lab)	CSE V Sem - PC 502 CS principles of Programming	CSE VII Sem - PC 71	PE 514 CS Object oriented Analysis and Design
8-11-2022 13:30:26	umati.khanapurkar@gmail.com	Umati Khanapurkar	Assistant Professor	9652683857	10	Java, python, cloud computing	III Sem CSE - 3PC302CS Discrete Mathematics	CSE V Sem - PC 505 CS Computer Networks(Theory + Lab)	CSE VII Sem - PC 71	PE515AD Foundation of Cryptography
8-11-2022 13:31:37	sowjanya.k@methodist.edu.in	B Sowjanya	Assistant Professor	9948123333	9.5	OS, JAVA, CN, DEL, ST, IRS, P	III Sem CSE - 3PC313CS Python Programming Lab	CSE V Sem - PC 505 CS Computer Networks(Theory + Lab)	Programming for Data Science	PE 515 CS Data Science
8-11-2022 13:34:44	ramakanta5@gmail.com	Dr. Ramakanta Mohanty	Professor	9949489298	34	ML, Python, DAA, R, DS, Data Analytics	III Sem AD - 1PC313AD Python Programming Lab	V Sem AD - PC503AD Artificial Intelligence(Theory + Lab)	CSE VII Sem - PC 71 Science	PE 515 CS Data Science

Head of the Department
Department of CSE
Vidya Sai Institute of Engineering & Technology
Aridara, Hyderabad.

8-11-2022 13 45 34	shakrazool@methodist.edu in	Shak Razool	Assistant Professor	7672073531	10	1	WIT, PPS, DS	III Sem AD - 1PC352AD Python Programming Lab	V Sem AD - PC501AD Software Engineering	Programming for Prog Cryptography	PE515AD Foundation of Cryptography	
8-11-2022 13 45 40	deepthijayam@gmail.com	Deepthi Joshi	Assistant Professor	8019448585	3.5 yrs	1	AI, SE, Data Mining, PPS	III Sem CSE - 1PC353CS Python Programming Lab	CSE V Sem - PC501 CS Software Engineering (Theory + Lab)	Programming for Prog and Design	PE 514 CS Object oriented Analysis	
8-11-2022 13 45 42	Sharyajabecm@gmail.com	Sharyajabecm	Assistant Professor	09640071786	11 years	1	DSA, Discrete Mathematics	III Sem AD - 1PC352AD Python Programming Lab	V Sem AD - PC501AD Software Engineering	Programming for Prog and Design	PE 514 CS Object oriented Analysis	
8-11-2022 13 49 50	vvasavbvs@gmail.com	Vasavi Pravanthi B	Assistant Professor	08500088390	10+ yrs	1	Prx, ALC, DataStructures, DBMS, Java, python, R, cloud	III Sem CSE - 1PC353CS Python Programming Lab	V Sem AD - PC502AD Database Management System(Theory + Lab)	Programming for Prog and Design	PE 514 CS Object oriented Analysis	
8-11-2022 14 25 04	khaleelahmed@methodist.edu in	Dr. Shak Khaleel Ahmed	Associate Professor	9949182786	9 years	0	SE, AI, ML	III Sem CSE - 1PC301CS Database Management System(Theory + Lab)	CSE V Sem - PC501 CS Software Engineering (Theory + Lab)	CSE VII Sem - PC 7 Science	PE 515 CS Data Science	
8-11-2022 14 29 53	vthimabinduadiparth@gmail.com	TADIPARTH V R HIMABINDU	Associate Professor	9985084256	YEARS	0	Cryptography Database Management Systems C Programming Advanced Data Structures	III Sem AD - 1PC352AD Python Programming Lab	V Sem AD - PC502AD Database Management System(Theory + Lab)	Programming for Prog Cryptography	PE515AD Foundation of Cryptography	
8-11-2022 14 42 12	skshrinithi@methodist.edu in	Dr. Shruthi SK	Associate Professor	9640438111	13 yrs	1	PPS, Information Security, Other security	III Sem CSE - 1PC301CS Database Management System(Theory + Lab)	V Sem AD - PC503AD Artificial Intelligence(Theory + Lab)	CSE VII Sem - PC 7 Cryptography	PE515AD Foundation of Cryptography	
8-11-2022 15 00 36	gsanthiv@methodist.edu in	G Santha	Assistant Professor	7893221205	15 yrs	12	Information security, Artificial intelligence software engineering, Osd, computer networks	III Sem CSE - 1PC301CS Database Management System(Theory + Lab)	CSE V Sem - PC501 CS Software Engineering (Theory + Lab)	CSE VII Sem - PC 7	PE 514 CS Object oriented Analysis and Design	
8-11-2022 15 04 34	Dhananoses@methodist.edu in	Dr. Dhana Moses	Associate Professor	7708442222	14	0.5	CO, DA, DM, CC, IPR, WT	III Sem CSE - 1PC301CS Computer Organization and Microprocessor(Theory + Lab)	CSE V Sem - PC502 CS principles of Programming Languages	CSE VII Sem - PC 7 Science	PE 515 CS Data Science	Midsem, final Semester AI&DS course has OOPs using Java(PC 302AD) as per
8-29-2022 13 43 45	engjeth@methodist.edu in	Mr. A Rajesh	Assistant Prof	9963430986	7	4	Automa Languages & Computation, Distributed Systems and Compiler Design	III Sem AD - 1PC302AD Database Management Systems (Theory + Lab)	V Sem AD - PC505AD Computer Vision	CSE VII Sem - PC 7	PE 514 CS Object oriented Analysis and Design	
8-12-2022 9 38 16	maharajula@methodist.edu in	Dr. M Sharada	Professor	9912106100	20	2	PPS, OS, DBMS	III Sem CSE - 1PC353CS Python Programming Lab	V Sem AD - PC502AD Database Management System(Theory + Lab)	CSE VII Sem - PC 7 Visualization	PE511AD Data Science	
8-12-2022 9 40 42	azaharayed@methodist.edu in	Dr. Syed Azahad	Associate Pr	8639128482	22	1	Computer Networks, Operating Systems, Data Structures, Cyber Security, Python, Java, DBMS	III Sem CSE - 1PC353CS Python Programming Lab	V Sem AD - PC502AD Database Management System(Theory + Lab)	CSE VII Sem - PC 7 Science	PE 515 CS Data Science	


Head of the Department
Department of CSE
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COMPUTER SCIENCE & ENGINEERING DEPARTMENT

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23/08/2022

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5	Dr.Diana Moses	Assoc. Prof.	
6	Dr. Shaik Khaleel Ahamed	Assoc.Prof	
7	Dr. T V R Himabindu	Assoc.Prof	T.V.R.Himabindu 23/08/22
8	Dr.Shruthi S.K.	Asst.Prof	
9	T Praveen Kumar	Asst. Prof.	
10	G Saritha	Asst. Prof.	
11	D Rajashekar	Asst. Prof.	
12	Ravikanti Sandeep	Asst. Prof.	
13	Unnati Mohan K	Asst. Prof.	
14	P.V. Ramanaiah	Asst. Prof.	
15	B Sowjanya	Asst. Prof.	
16	A. Rajesh	Asst Prof	
17	M.V.D.S Krishna Murty	Asst Prof	
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24	Mr.U Moulali	Asst.Prof	
25	Srinu Dharavath	Asst.Prof	
26	Ms.A Lalitha	Asst. Prof.	
27	Mr. Venkatram Vennam	Asst. Prof.	
28	Mrs. A.Sowjanya	Asst. Prof.	
29	K.Uday Kumar	Asst. Prof.	
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

DATE:05-10-2022

CSE STAFF WORKLOAD(2022-2023 I SEMESTER)

S. No	Faculty name	Subjects	Work load	Additional Responsibilities
1.	Dr. P. Lavanya Prof. & HOD	AI V SEM(AI&DS)-A-4 AI LAB V SEM (AI&DS)-A-4	8	1. HOD-CSE 2. Dept Approval Copies 3. Mentor
2.	Dr.G.Ravinder Reddy Professor	PROJECT WORK VII SEM CSE-A-5 PROJECT WORK VII SEM CSE-B-5	10	1.Project coordinator
3.	Dr M Sharadha varalakshmi Professor	DBMS III SEM CSE-A-4 DBMS LAB III SEM CSE-A-4 FT V SEM AI&DS-A-4	12	1. Academic Council 2. Autonomous Files 3. Department Meetings
4.	Dr Ramakanth Mohanty Professor	DSR VII SEM CSE-A-4 DSR LAB VII SEM CSE-A-4 PROJECT WORK VII SEM CSE-A-5 PROJECT WORK VII SEM CSE-B-5	18	1. Coordinator of R&D 2. Consultancy Projects. 3. Major/Mini Project Coordinator
5.	Dr. Syed Azahad Professor	DBMS III SEM CSE-B-3 DBMS LAB III SEM CSE-B-4 DBMS LAB III SEM CSE-A-4 PPS III SEM MECH-A-4 PPSLAB III SEM MECH-A-4 SDC III SEM AI&DS-B-2	21	1.Head Mentor-cse 2.NSS Incharge 3.Class Incharge 4.Mentor
6.	Dr Diana Moses Professor	COM III SEM AI&DS-A-4 COM LAB III SEM AI&DS-A-4 COMLAB III SEM AI&DS-B-4 PPL V SEM CSE-A-4	16	1. Dept. Assessment Committee Head 2. ARIIA Coordinator
7.	Dr.Shaik Khaleel Ahmed Professor	DSR VII SEM CSE-B-4 DSRLAB VII SEM CSE-B-4 DSRLAB VII SEM CSE-A-4 SDC III SEM CSE-B-2	14	1. Student Affairs 2. IQAC Coordinator
8.	Dr S K Shruthi Asst. Prof	DS VII SEM CSE-A-4 DS LAB VII SEM CSE-A-4 PPS III SEM EEE-A-4 PPS LAB III SEM EEE-4	16	1. Placements Coordinator 2. Mentor
9.	Dr U .Moulali Asst. Prof	DS V SEM CSE-A-4 DBMS V SEM AI&DS-A-4 DBMSLAB V SEM AI&DS-A-4 SDC III SEM AI&DS-A-2	14	1. OU-TBI Coordinator 2. Spoken Tutorials 3. R&D Consultancy Cell
10.	Mrs. G.Saritha Asst. Prof.	IS VII SEM CSE-A-4 CN V SEM CSE-B-4 CN LAB V SEM CSE-B-4 CN LAB V SEM CSE-A-4	16	1. Class Coordinator 2. R&D Consultancy Cell 3. College Level Activities
11.	Mr. T. Praveen Kumar Asst. Prof	DBMS III SEM AIDS-A-4 DBMS LAB III SEM AIDS-A-4 PROJECT VII SEM CSE-A-5 PROJECT VII SEM CSE-B-5	18	1. Central Time Table Coordinator 2. Mentor 3. Project Coordinator Spoken Tutorials Incharge



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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

DATE:05-10-2022

12.	Mr. D. Raja Shekar Asst. Prof.	PP LAB III SEM CSE-A-4 SE V SEM CSE-A-4 SE LAB V SEM CSE-A-4 PPLAB III SEM CSE-B-4	16	1. Feedback Committee 2. Mentor 3. NBA Committee Member
13.	Mr. Sandeep R Asst. Prof.	PP III SEM ECE-A-4 PP LAB III SEM ECE -A-4 SE V SEM CSE-B-4 SE LAB V SEM CSE-B-4	16	1. Department Meetings 2. Value Added Course Coordinator 3. Internships-AICTE Coordinator 4. ALUMINI Coordinator 5. MOU Coordinator
14.	Mrs. Unnati K. Asst. Prof.	COM III SEM CSE-A-4 COM LAB III SEM CSE-A-4 COM LAB III SEM CSE-B-4 DM III SEM AIDS -B-4	16	1. CSI Coordinator 2. Mentor 3. Central Lab Incharge 4. Value Added Course Coordinator 5. MOU Coordinator
15.	Mrs. B. Sowjanya Asst. Prof.	CN V SEM CSE-A-4 CN LAB V SEM CSE-A-4 CN LAB V SEM CSE-B-4 DS V SEM CSE-B-4	16	1. Assessment Committee 2.Mentor
16.	Mr. P.V. Ramanaiah Asst. Prof.	PP III SEM AI&DS-B-4 ALC V SEM CSE-A-4 PP III SEM ECE-B-4 PP LAB III SEM ECE-B-4	16	1.Exam Branch Member 2.Mentor
17.	Mrs CH Sravanthi Reddy Asst. Prof.	PPL LAB III SEM (AI&DS)-A-2 PPL LAB III SEM (AI&DS)-B-2 PPS LAB III SEM MECH-A-4 PP LAB III SEM ECE-A-4 PP LAB III SEM ECE -B-4	16	1.Central Lab Incharge
18.	Mr A Rajesh Asst. Prof.	DS VII SEM CSE-B-4 DS LAB VII SEM CSE-B-4 DS LAB VII SEM CSE-A-4 MP LAB V SEM AI&DS-A-4	16	1.Exam Branch coordinator 2.Mentor
19.	Mr. Krishnamurthy Asst. Prof	SE V SEM AI&DS-A-4 MP LAB V SEM AI&DS-A-4 SE VII SEM CSE-A-4 DSLAB VII SEM CSE-B-4	16	1. YUKTI Central Coordinator 2. IEDC Coordinator 3. Feedback Committee
20.	Mrs J.Sowmya Asst. Prof	COM III SEM CSE-B-4 COM LAB III SEM CSE-B-4 COM LAB III SEM CSE-A-4 DM III SEM AI&DS -B-4 SDC III SEM AIDS-B-4	20	1. A2I Talentio Member 2. Mentor Sports Coordinator
21.	Mrs Deepthi joshi Asst. Prof.	PPS I SEM CSE-B-4 PPSLAB I SEM CSE-B-4 PPS I SEM AI&DS-A-4 PPSLAB I SEM AI&DS-A-4	16	Faculty coordinator 2.Mentor
22.	Mrs Vasavi Sravanthi Asst. Prof.	PPLAB III SEM CSE-B-4 DBMS III SEM AIDS-A-4 DBMSLAB III SEM AIDS-A-4 PPLAB III SEM CSE-A-4	16	1. Exam Branch Coordinator 2. Class Coordinator 3. Mentor



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DATE:05-10-2022

23.	Mrs Shaziya Jabeena Asst. Prof.	PPS III SEM CIVIL -4 PPS LAB III SEM CIVIL-4 PPS I SEM AI&DS-B-4 PPSLAB I SEM AI&DS-B-4	16	1.Department Library Incharge 2.Mentor
24.	Mr. Shaik Rasool Asst. Prof	PPS LAB III SEM AI&DS-A-4 PPS III SEM AI&DS-A-4 WT V SEM AI&DS-4 DSR LAB VII SEM CSE-B-4	16	1. Class Coordinator 2. Mentor 3. IQAC Coordinator 4. Head Mentor-AI&DS
25.	Mr.Srinudharavath	SE VII SEM CSE-B-4 ITAM I SEM(MBA)-4 ITAM LAB I SEM(MBA)-4	12	1. Class Coordinator 2.Mentor 3.News letter and Magazine coordinator
26.	K Uday Kumar Asst. Prof.	AI LAB V SEM (CSE)-A-4 AI LAB V SEM (CSE)-B-4 PPS LAB III SEM (EEE)-4 PPL LAB III SEM(CSE)-A-2 COM LAB III SEM (AI&DS)-B-4	18	1. Member of Sports Cell 2. DEPT Sports 3.Member of Public Relation Cell
27.	Mr V Venkatram Asst. Prof.	DM III SEM CSE-A-4 DM III SEM CSE-B-4 AI LAB V SEM AI&DS-A-4 AI LAB V SEM CSE-B-4	16	1.Class Coordinator 2.Mentor
28.	Mrs A.Sowjanya Asst. Prof.	COM III SEM AI&DS-B-4 COM LAB III SEM AI&DS-B-4 COM LAB III SEM AI&DS-A-4 PPL V SEM CSE-B-4	16	1. Time Table Coordinator 2. Mentor
29.	Mr Senthil Kumar Asst. Prof.	ALC V SEM CSE-B-4 PPS I SEM CSE-A-4 PPS LAB I SEM CSE-A-4	12	1. Class Coordinator 2. Mentor 3.Placement Coordinator
30.	Ms.Sana Mateen Asst. Prof.	AI V SEM CSE-A-4 AILAB V SEM CSE-A-4 AI V SEM CSE-B-4 AILAB V SEM CSE-B-4 SDC III SEM AI&DS-A-2	18	1. Mentor 2. OU-TBI Coordinator 3.Time Table Coordinator
31.	Mr Ravi Kumar Asst. Prof.	ALC V SEM AI&DS-A-4 IS VII SEM CSE-B-4 PPSLAB III SEM CIVIL-A-4 DBMSLAB V SEM AI&DS-A-4	16	1.Department Library Incharge 2.Mentor



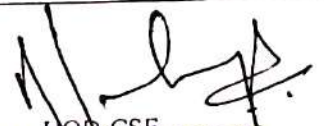
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

DATE:05-10-2022

32.	Mrs K Keerthi	PPS LAB I SEM CSE-A&B-8 PPS LAB I SEM AI&DS-A&B-8	16	I.Mentor
33.	Ms A Lalitha Asst. Prof.	SDCLAB III SEM (CSE)-A-2 SDCLAB III SEM (CSE)-B-2 PPSLAB I SEM (CSE)-A-4 PPSLAB I SEM (CSE)-B-4 COM LAB III SEM (AI&DS)-A-4	16	I.News Letter & Magazine File


HOD, CSE
Head of the Department
Department of CSE
Methodist College of Engg & Tech
Abids, Hyderabad.



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0001:2015

2023

Department of Computer Science and Engineering
Faculty Individual Time Table

Faculty Name: Dr. Diana Moses (17), Professor

A.Y: 2022 - 2023

Semester: III/IV

W. E. F. 26 - 09 - 2022

Period / Day	1	2	3	12:30 - 01:15	4	5	6
	9:30 - 10:30	10:30 - 11:30	11:30 - 12:30		01:15 - 02:15	02:15 - 03:15	03:15 - 04:15
Monday							
Tuesday	COM LAB - AI & DS - A (III SEM)						
Wednesday		COM - AI & DS - A (III SEM)		LUNCH	PPL - CSE - A (V SEM)		
Thursday			COM - AI & DS - A (III SEM)	BREAK		PPL - CSE - A (V SEM)	PPL - CSE - A (V SEM)
Friday	COM LAB - AI & DS - B (III SEM)						
Saturday					COM - AI & DS - A (III SEM)		

S. NO	COURSE CODE	COURSE NAME	CLASS	Additional Responsibilities
1	1 PC 303 AD	COM (Computer Organization and Microprocessor)	III Semester BE AI & DS - A	1. Dept. Assessment Committee Head 2. ARIIA Coordinator
2	1 PC 352 AD	COM LAB (Computer Organization and Microprocessor Lab)	III Semester BE AI & DS - A & B	
3	PC 502 CS	PPL (Principles of Programming Languages)	V Semester BE CSE - A	

Faculty

Head of the Department
 Department of CSE
 Abids, Hyderabad.



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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING COURSE DESCRIPTION FILE

Academic Year & Semester	2022-23, Semester III
Course Code	IPC303CS
Course Title	COMPUTER ORGANIZATION & MICROPROCESSORS
Curriculum Regulation	AUTONOMOUS
Semester	III
Course Instructor	Dr Diana Moses, Associate Professor, CSE Department Mrs A Sowjanya, Assistant Professor, CSE

I. PREREQUISITE(S):

Level	Credits	Semester	Prerequisites
UG	3	IV	Students should have basic knowledge of Digital and logic circuits and Electronic Devices.

II. SCHEME OF INSTRUCTIONS

Lectures	Tutorials	Practicals	Credits
3	1	-	3

III. SCHEME OF EVALUATION & GRADING

S. No	Component	Duration	Maximum Marks
Continuous Internal Evaluation (CIE)			
1.	Internal Examination – I	60 minutes	20
2.	Internal Examination - II	60 minutes	20
	Average of the two internal exams		20
3.	Assignments	-	5
4.	Quizzes	-	5
	CIE (Total)		30
5.	Semester End Examination (University Examination)	3 hours	70
	TOTAL		100

Marks Range	85-100	70 to < 85	60 to < 70	55 to < 60	50 to < 55	40 to < 50	< 40	Absent
Grade	S	A	B	C	D	E	F	Ab
Grade Point	10	9	8	7	6	5	0	-

COMPUTER ORGANIZATION AND MICROPROCESSOR

Semester III	L	T	P	Credits
Subject code – IPC303 AD	3	0	0	3

Course Objectives:	Course Outcomes:
<ul style="list-style-type: none"> ➤ To explore the I/O organizations in depth. ➤ To learn the fundamentals of computer organization and its relevance to classical and modern problems of computer design. ➤ To be familiarized with the hardware components and concepts related to the memory organization. ➤ To be familiarized with the hardware components and concepts related to the input-output organization ➤ Understand the concepts and applications of Internet of Things ,Building blocks of Internet of Things and characteristics 	<ol style="list-style-type: none"> 1. Recall and apply a basic concept of block diagram of computer (CPU) with Microprocessor processor UNIT (MPU) 2. Understand the internal architecture and register organization of 8086 3. Apply knowledge and demonstrate programming proficiency using the various addressing modes and instruction sets of 8086 4. Identify and compare different methods for computer I/O mechanisms 5. Categorize memory organization and explain the function of each element of a memory hierarchy 6. Apply knowledge and demonstrate interfaces with 8086 with outside world

UNIT I (TB 1 Chapter 8.1,8.3, 8.4,8.5,8.7)

Basic Computer Organization: Functions of CPU, I/O UNITS, Memory: Instruction: Instruction Formats- One address, two addresses, zero addresses and three addresses and comparison; addressing modes with numeric examples: Program Control- Status bit conditions, conditional branch instructions, Program Interrupts: Types of Interrupts

UNIT II (TB 3 Chapter 1)

8086 CPU Pin Diagram: Special functions of general purpose registers, Segment register, concept of pipelining, 8086 Flag register, Addressing modes of 8086

Pipelining: Introduction, processors, performance, hazards, super scalar operations and performance considerations

UNIT III (TB 3 Chapter 2)

8086-Instruction formats: assembly Language Programs involving branch & Call instructions, sorting, evaluation of arithmetic expressions.

UNIT IV (TB 1 Chapter 11.1,11.2,11.3,11.4,11.6,11.7)

Input-Output Organizations I/O Vs Memory Bus, Isolated Vs Memory-Mapped I/O, Asynchronous data Transfer Techniques, Asynchronous Serial transfer- Asynchronous Communication interface (8251), Modes of transfer Programmed I/O, Interrupt Initiated I/O, DMA; DMA Controller (8257), IOP-CPU-IOP Communication, Intel 8089 IOP

UNIT V (TB 1 chapter 12.1, 12.2,12.3,12.4,12.5,12.6)

Memory Organizations: Memory hierarchy, Main Memory, RAM, ROM Chips, Memory Address Map, Memory Connection to CPU, associate memory, Cache Memory, Data Cache, Instruction cache, Miss and Hit ratio, Access time, associative, set associative, mapping, waiting into cache, Introduction to virtual memory

TEXTBOOKS

1. Computer system Architecture: Morris Mano, Third Edition,
2. Computer Organization and Architecture–William Stallings, Sixth Edition, Pearson/PHI.
3. Advanced Micro Processor and Peripherals- Hall/ A K Ray

REFERENCE BOOKS

1. Computer Organization V. Carl Hamacher, Safwat G. Zaky, Zvonko Vranesic, Zvonko G Vranesic, Fifth Edition
2. Microprocessor Architecture, Programming, Applications with 8085, Ramesh S Gaonkar, Fifth Edition, Prentice Hall, 2002

IV. SYLLABUS

Unit	Syllabus Description	Target Hours
I	Basic Computer Organization: Functions of CPU, I/O UNITS, Memory: Instruction: Instruction Formats- One address, two addresses, zero addresses and three addresses and comparison; addressing modes with numeric examples: Program Control- Status bit conditions, conditional branch instructions, Program Interrupts: Types of Interrupts	9
II	8086 CPU Pin Diagram: Special functions of general purpose registers, Segment register, concept of pipelining, 8086 Flag register, Addressing modes of 8086 Pipelining: Introduction, processors, performance, hazards, super scalar operations and performance considerations	10
III	8086-Instruction formats: assembly Language Programs involving branch & Call instructions, sorting, evaluation of arithmetic expressions.	12
IV	Input-Output Organizations I/O Vs Memory Bus, Isolated Vs Memory-Mapped I/O, Asynchronous data Transfer Techniques, Asynchronous Serial transfer- Asynchronous Communication interface (8251), Modes of transfer Programmed I/O, Interrupt Initiated I/O, DMA; DMA Controller (8257), IOP-CPU-IOP Communication, Intel 8089 IOP	9
V	Memory Organizations: Memory hierarchy, Main Memory, RAM, ROM Chips, Memory Address Map, Memory Connection to CPU, associate memory, Cache Memory, Data Cache, Instruction cache, Miss and Hit ratio, Access time, associative, set associative, mapping, waiting into cache, Introduction to virtual memory.	8
	Total	48

Suggested Reading:

1. Computer system Architecture: Morris Mano, Third Edition,
2. Computer Organization and Architecture-William Stallings, Sixth Edition, Pearson/PHI.
3. Advanced Micro Processor and Peripherals- Hall/ A K Ray
4. Computer Organization V. Carl Hamacher, Safwat G. Zaky, Zvonko Vranesic, Zvonko G Vranesic, Fifth Edition
5. Microprocessor Architecture, Programming, Applications with 8085, Ramesh S Gaonkar, Fifth Edition, Prentice Hall, 2002

V. E – RESOURCES

1. <https://nptel.ac.in/courses/106106092>
2. <https://www.geeksforgeeks.org/computer-organization-and-architecture-tutorials/>
3. https://www.tutorialspoint.com/computer_logical_organization/index.htm

VI. COURSE OBJECTIVES:

Course Overview: This course provides a comprehensive introduction to computer organization and architecture. The course aims to provide in depth knowledge to the students about the design and organization of a digital computer, operation of various

functional units, instruction set design and factors that influence the performance of a computer. This course enables the students with the understanding of basic computer architecture with instruction set and programming of 8086 in particular. The course explores the interfacing of 8085 with Asynchronous Communication interface (8251), Modes of transfer Programmed I/O, Interrupt Initiated I/O, DMA; DMA Controller (8257), IOP-CPU-IOP Communication, Intel 8089 IOP. The course also lays the basics for memory organization and the performance of different types of organization.

The objectives of this course are:

- To explore the I/O organizations in depth.
- To learn the fundamentals of computer organization and its relevance to classical and modern problems of computer design.
- To be familiarized with the hardware components and concepts related to the memory organization.
- To be familiarized with the hardware components and concepts related to the input-output organization
- Understand the concepts and applications of Internet of Things ,Building blocks of Internet of Things and characteristics

VII. COURSE OUTCOMES

After completing this course the student will be able to:

CO. No.	Course Outcome	Taxonomy Level	PO
PC303CS.1	Explain the organization and architecture of a basic computer (CPU) with different instruction formats and addressing modes	Understanding	PO10, PO12
PC303CS.2	Describe the internal architecture and register organization of 8086 and the addressing modes in 8086	Understanding	PO1
PC303CS.3	Design and develop Assemble level programs using 8086 microprocessor instruction set	Applying	PO3
PC303CS.4	Explain various I/O Interfacing mechanisms	Understanding	PO1
PC303CS.5	Analyze the performance of different memory organization techniques	Analyzing	PO4
PC303CS.6	Applies the knowledge of program execution and its internal hardware operations during design and development of Assemble language.	Applying	PO3

VIII. MAPPING OF Cos WITH POs & PSOs

Correlation Level: High – 3; Medium – 2; Low – 1

SNO	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO10	PO 11	PO12	PS O1	PS O2	PS O3
CO1	3									1		1		2	
CO2	3	1								1		1		2	
CO3			3	2								1		2	
CO4	3	1		1								1		2	
CO5	1		2	3								1		2	
CO6	1	2	3							1		1		2	
TOT	11	4	8	6						3		6		12	
AVG	2.2	1.3	2.6	2						1		1		2	

After gaps:

Gaps Beyond The Syllabus – Mapping To PO/PSO (To Meet Industry/Profession Requirements):

1. Multiprocessor systems and Multicore Architectures

Plan of Action: Provide study material and NPTEL video Lectures to the students.

After identifying the gaps PO's i.e., PO5, PO12, PSO1 are mapped and CO-PO mapping is justified through the corrective measures taken.

Revised CO-PO Mapping

SNO	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	PS O1	PS O2	PS O3
PC303CS.1	3									1		1		2	
PC303CS.2	3	1								1		1		2	
PC303CS.3			3	2								1		2	
PC303CS.4	3	1		1								1		2	
PC303CS.5	1		2	3	2							1		2	
PC303CS.6	1	2	3		2					1		1		2	
Avg	11	4	8	6	2					3		6		12	
	2.2	1.3	2.6	2	2					1		1		2	

CO-PO/PSO mapping Justification

Mapped POs & PSOs (Direct): PO1, PO2, PO3, PO4, PO5, PO12
 Mapped POs & PSOs (Corrective measures): PO8, PO9, P10

Course outcomes:

C303.1: Explain the organization and architecture of a basic computer (CPU) with different instruction formats and addressing modes (Understanding)

PO/ PSO	Justification
PO1	Explain the organization and architecture of a basic computer (CPU) with different instruction formats and addressing modes
PO10	Debates on the pros and cons of different architectures, instruction formats and addressing modes
PO12	Explains the impact of different architectures on a particular system
PSO2	Develops code in different instruction formats

C303.2 : Describe the internal architecture and register organization of 8086 and the addressing modes in 8086 (Understanding)

PO/PSO	Justification
PO1	Fundamental knowledge of internal architecture and register organization of 8086 and the addressing modes in 8086.
P02	Able to conduct investigations on working of internal architecture and register organization of 8086 and the addressing modes in 8086
PO10	Discusses and debates on advantages and disadvantages of different addressing modes
PO12	Understands the importance of register organization and its influence on the program written for that machine
PSO2	Accesses data using different addressing modes

C303.3: Design and develop Assemble level programs using 8086 microprocessor instruction set (Applying)

PO/PSO	Justification
PO3	Compares the use of different instructions
PO4	Analyzes the different instructions and their use in various scenarios
PO12	Gains knowledge on development of code for machine dependent architectures
PSO2	Develops assembly language programs for various tasks

C303.4 : Explain various I/O Interfacing mechanisms (Understanding)

PO/PSO	Justification
PO1	Fundamental knowledge of various I/O Interfacing mechanisms.
PO2	Compares various I/O Interfacing mechanisms.
PO4	Analyzes the challenges in interfacing different devices
PO12	Gains knowledge on different hardware interfaces and their connectivity with 8086
PSO2	Explains the mechanisms for interfacing different devices with 8086

C303.5. Analyze the performance of different memory organization techniques (Analyzing)

PO/PSO	Justification
PO1	Fundamental knowledge of different memory organization techniques
PO3	Analyzes the advantages and disadvantages of different memory organization techniques
PO4	Identifies challenges in Memory hierarchy
PO5	Explores the new memory devices and management techniques available
PO12	Understands the impact of the memory hierarchy in a system during program execution
PSO12	Develops efficient programs with understanding of the memory hierarchy and its impact during execution

C303.6: Applies the knowledge of program execution and its internal hardware operations during design and development of Assembly language. (Applying)

PO/PSO	Justification
PO1	The knowledge about program execution and its internal hardware operations during design and development of Assembly language
PO2	Applies the knowledge of internal hardware for efficient design and development of assemble language programs
PO3	Analyzes efficiency of assembly language program execution and its internal hardware operations
PO5	Explores the advances in hardware devices and management techniques available
PO10	Discusses and debates on the efficiency of developed program with respect to the internal architecture
PO12	Understands the impact of the internal hardware of a system during program execution
PSO2	Develops efficient programs with understanding of the memory hierarchy and its impact during execution

IX. TEACHING-LEARNING METHODOLOGY ADOPTED

1. Chalk and Talk
2. Group Discussions/ Debates Learning by doing
3. Group Tasks / Assignments
4. PowerPoint Presentation
5. Quiz/ Moodle based Quiz
6. Videos
7. Hardware Demonstration

X. METHOD OF ASSESSMENT OF COs and POs:

COs	Relevant POs	Mode of Assessment
C303.1-C303.6	PO1: ENGINEERING KNOWLEDGE PO2: PROBLEM ANALYSIS PO3: DESIGN/ DEVELOPMENT OF SOLUTIONS PO4: CONDUCT INVESTIGATION ON COMPLEX PROBLEMS PSO1: PROFESSIONAL COMPETENCE	Assignments, Quizzes, Internal Examinations and External Examination result

C303.1-C303.6	PO5: MODERN TOOL USAGE	Exercises to learn through ICT tools and internet websites, Usage of Excel worksheets for problem solving
C303.1-C303.6	PO8: ETHICS	Assignments, Quizzes
C303.1-C303.6	PO9: INDIVIDUAL AND TEAM WORK PO10: COMMUNICATION	Group Assignments, Writing skills in documenting assignments, Presentations

XI. LESSON PLAN:

S.No.		TOPICS TO BE COVERED	Teaching Methods	Books	No. of Classes
1	UNIT - I	Basic Computer Organization: Functions of CPU, I/O UNITS, Memory	Chalk & Talk, PPT, Video	TB1, RB1	2
2		Instruction: Instruction Formats- One address, two addresses, zero addresses and three addresses and comparison	Chalk & Talk, PPT, Assignment	TB1, RB1	2
3		Addressing modes with numeric examples	Chalk & Talk, PPT, Assignment	TB1, RB1	2
4		Program Control- Status bit conditions, conditional branch instructions	Chalk & Talk, PPT	TB1, RB1	2
5		Program Interrupts: Types of Interrupts	Chalk & Talk, PPT	TB1, RB1	1
Total periods planned to complete unit I					9
6	UNIT- II	8086 CPU Pin Diagram	Chalk & Talk, PPT, Assignment	TB3, TB2	2
7		Special functions of general purpose registers, Segment register, 8086 Flag register	Chalk & Talk, PPT, Assignment	TB3, TB2	2
8		Addressing modes of 8086	Chalk & Talk, PPT	TB3, TB2	2
9		Concept of pipelining - Introduction, processors, performance, hazards	Chalk & Talk, PPT, Video	TB3, TB2	2
10		Super scalar operations and performance considerations	Chalk & Talk, PPT	TB3, TB2	2
Total periods planned to complete unit II					10
11	UNIT - III	8086-Instruction formats	Chalk & Talk, PPT, Assignment	TB3	4
12		Assembly Language Program samples	Chalk & Talk, PPT, Quiz	TB3	4
13		Branch & Call instructions	Chalk & Talk, PPT	TB3	2
14		Sorting	Chalk & Talk, PPT	TB3	1

15		Evaluation of arithmetic expressions	Chalk & Talk, PPT	TB3	1
Total periods planned to complete unit III					12
16	UNIT - IV	Input-Output Organizations I/O Vs Memory Bus, Isolated Vs Memory-Mapped I/O	Chalk & Talk, PPT	TB1, TB2	1
17		Asynchronous data Transfer Techniques	Chalk & Talk, PPT	TB1, TB2	1
18		Asynchronous Serial transfer-Asynchronous Communication interface (8251)	Chalk & Talk, PPT, Assignment	TB1, TB2	2
19		Modes of transfer Programmed I/O, Interrupt Initiated I/O, DMA	Chalk & Talk, PPT	TB1, TB2	2
20		DMA Controller (8257)	Chalk & Talk, PPT, Assignment	TB1, TB2	2
21		IOP-CPU-IOP Communication	Chalk & Talk, PPT	TB1, TB2	1
22		Intel 8089 IOP	Chalk & Talk, PPT	TB1, TB2	1
Total periods planned to complete unit IV					9
23	UNIT - V	Memory Organizations: Memory hierarchy, Main Memory, RAM, ROM Chips	Chalk & Talk, PPT	TB1, RB1	1
24		Memory Address Map, Memory Connection to CPU	Chalk & Talk, PPT	TB1, RB1	1
25		Associate memory	Chalk & Talk, PPT	TB1, RB1	2
26		Cache Memory, Data Cache, Instruction cache, Miss and Hit ratio, Access time	Chalk & Talk, PPT	TB1, RB1	1
27		Cache Mapping - associative, set associative, mapping, waiting into cache	Chalk & Talk, PPT, Assignment	TB1, RB1	2
28		Introduction to virtual memory.	Chalk & Talk, PPT	TB1, RB1	2
Total periods planned to complete unit V					8
Total No. of Classes					48

TEXTBOOKS

1. Computer system Architecture: Morris Mano, Third Edition,
2. Computer Organization and Architecture-William Stallings, Sixth Edition, Pearson/PHI.
3. Advanced Micro Processor and Peripherals- Hall/ A K Ray

G Vranesic, Fifth Edition

2. Microprocessor Architecture, Programming, Applications with 8085, Ramesh S Gaonkar, Fifth Edition, Prentice Hall, 2002

The course plan is meant as a guideline. There may probably be changes.

Prepared by: Dr Diana Moses, Associate Professor, CSE
Mrs A Sowjanya, Assistant Professor, CSE

A handwritten signature in black ink, consisting of a large, stylized 'D' followed by a horizontal line extending to the right.

Signature :
Date :



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ISO
9001:2015

ESTD : 2008

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Abids, King Koti, Hyderabad - 500001.

Department of Computer Science and Engineering.
CLASS TIME - TABLE

Class: III Semester BE AI & DS - A 2022 - 2023 Semester: III W. E. F: 26 - 09 - 2022 LH: E - 203

Day / Period	I 09:30 - 10:30	II 10:30 - 11:30	III 11:30 - 12:30	IV 12:30 - 01:15	V 01:15 - 02:15	VI 02:15 - 03:15	VII 03:15 - 04:15	VIII 04:15 - 05:15
MON	PPL	P & S	STLD	Dis. M	DBMS / COM Lab	DBMS / COM Lab	LIBRARY	
TUE	Dis. M	DBMS / COM Lab		DBMS	PPL Lab	PPL Lab	INTERNET	
WED	P & S	COM	EITK	DBMS	CRT TRAINING	CRT TRAINING	MAKE UP	
THU	DBMS	P & S	COM	SPORTS	CRT TRAINING	CRT TRAINING	MENTOR	
FRI	DBMS	STLD	Dis. M	COM	COM	SDC - I	REMEDIAL	
SAT	Dis. M	EITK	PPL	COM	STLD	P & S	SPORTS	

*(T) - Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
1 BS 305 HS	P & S - Probability & Statistics	Mrs. G. Swapna Reddy	1 PC 351 AD	DBMS Lab - Database Management Systems Lab	Mrs. B. Vasavi Sravanthi / Mr. D. Srinivas
1 PC 301 AD	Dis. M - Discrete Mathematics	Mrs. Unnati Mohan Khanapurkar	1 PC 352 AD	COM Lab - Computer Organization and Microprocessor Lab	Dr. Diana Moses / Mrs. A. Sowjanya / Ms A Lalitha
1 PC 302 AD	DBMS - Database Management Systems	Mrs. B. Vasavi Sravanthi	1 PC 353 AD	PPL - Python Programming Lab	Mr. Shaik Rasool / Mr. P. V. Ramanaiah / Mrs CH Sravanthi Reddy
1 PC 303 AD	COM - Computer Organization and Microprocessor	Dr. Diana Moses	1 PW 354 AD	SDC - I - Skill Development Course - I (IOT)	Mrs. Sara Mameen / Dr. U. Moulali
1 ES 303 EC	STLD - Switching Theory and Logic Design	Mr. D. Suresh		CRT Training	Mrs. B. Vasavi Sravanthi
1 MC 302 HS	EITK - Essence of Indian Traditional Knowledge	Mrs. Deepthi			

Class Coordinator: Mrs. B. Vasavi Sravanthi

Class In - Charge

Table Coordinator

Head of Department & In-charge

Mrs. B. Vasavi Sravanthi

Mrs. B. Vasavi Sravanthi

Methodist College
Abids, Hyderabad.

DEPARTMENT OF CSE [A&DS]

STATUS OF SYLLABUS COMPLETION BEFORE CIE - II

Semester: III

Section: AIDS-A

Class coordinator: Mrs B. Vasavi Sreewartha

Status of Syllabus as on Date: 02/01/2023

Sl. No.	Course / Subject	Name of the Faculty	No. of Classes engaged till date	Unit Percentage of Syllabus Completed	& No. of classes required to complete the remaining Units	No. of extra Classes required beyond the scheduled classes	Signature of faculty	Remarks
1	PPS	Ms Swagata.	40 /	4.5 units	3	—		
2	STLD	Mr Suresh	37 /	4 units	5	—		
3	DBMS	B. VASAVI SREAVANTHI	39 /	4 units	8	—	B.S.	
4	Dis M	Umnotik	49 /	4 1/2 units	08	—		
5	COM	DR. DIANA MOSES	46 /	4 1/2 units	4.	—		
6	EITK	Ms Deepthi						
7.	DBMS	B. VASAVI SREAVANTHI	16	9 Exp	4	—		
8.	PPCLD	SHAIK RASOOL	41	10	4	—		
9.	Code by Dr. Diana Moses, Ms. Vasavi Sreewartha, B. T. S. Verbasani, Chandrajit, B. T. S. Ling Koll, Hyderabad.		20.	20 exp	4	—		

Head of the Department
 Department of CSE & Tech
 AICTE, New Delhi

Principal