

Bipolar Junction Transistor

Unit - IV

Junction TR: - In 1951, William invented the first junction transistor, a semiconductor device that can amplify electronic signals such as radio and television signals.

Transistor is a three-terminal device; Base, Emitter, and Collector, can be operated in three configurations common, base, Common Emitter and Common Collector.

Transistor was invented Dr. William Shockley and Dr. John Bardeen at Bell Laboratory in 1951. It is a three-terminal device, the output voltage, current or power are controlled by input current in a transistor. Therefore it is called a current controlled device.

In short transistor is also called as BJT stands for Bipolar Junction Transistor because the transistor operation is carried out by two types of charge carriers i.e. majority carriers and minority carriers.

* There are 2 types of transistors.

- ① unipolar junction transistor: - Current conduction is only due to one type of carriers. i.e. maj carriers
- ② Bipolar junction transistor: - The current conduction in BJT is because of both the types of charge carriers. electrons and holes.

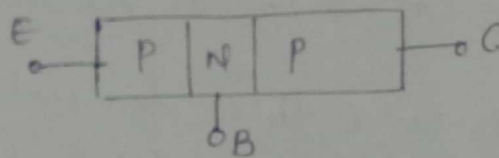
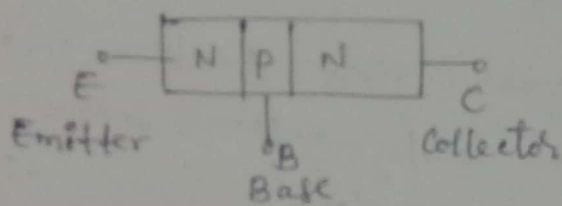
* BJT are 2 types: -

1. n-p-n types
2. P-n-p types.

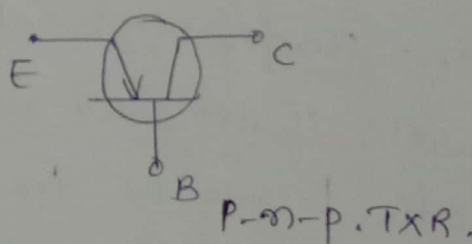
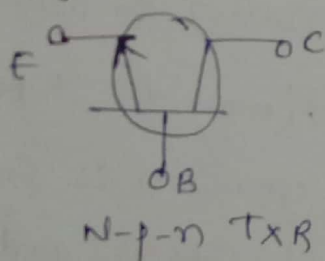
Formed by sandwiching

1. N-P-N TXR: - A single P-region b/w two n-regions.

2. P-N-P TXR: - is formed by sandwiching a single n-region b/w two P-regions.

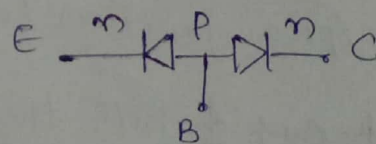


The middle region is always Base of the tXR. This region is very thin and lightly doped. The remaining two regions are called Emitter and Collector are heavily doped. But the doping level in emitter is slightly greater than collector. And collector area is slightly more than emitter.

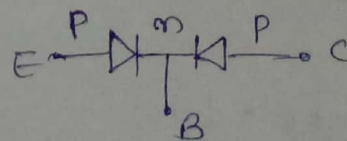


A transistor has two P-n junctions. one junction is b/w emitter and base and is called emitter base junction (J_E) The other junction is b/w the base and collector called collector base junction (J_C)

This transistor is like two P-n junction diodes connected back-to-back.



Emitter: - The function of emitter is to inject charge carriers (e^- and holes) to the base region.

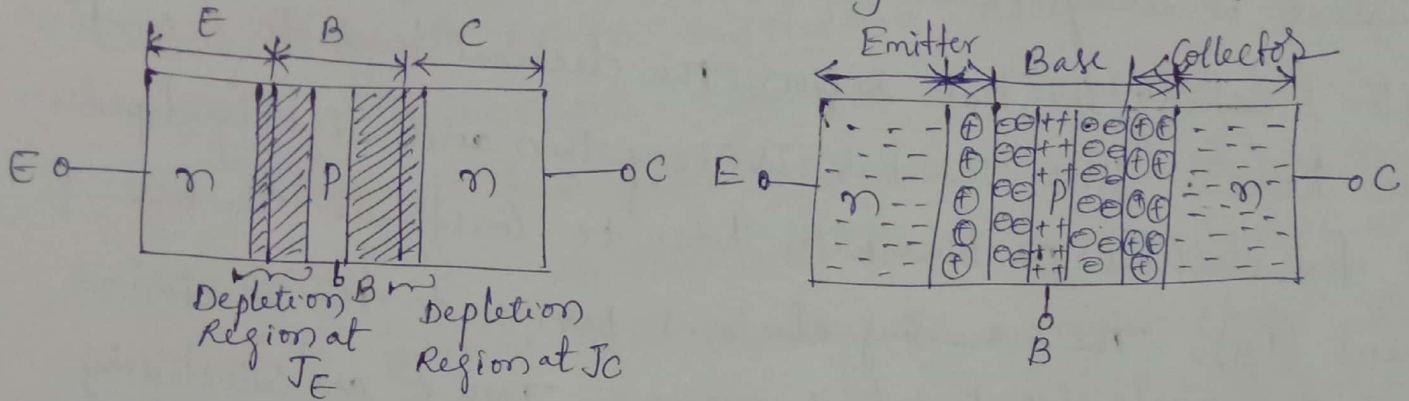


Base: - The function of base is to pass all the charge carriers onto the collector.

Collector: - The function of collector is to collect charge carriers.

Unbiased transistor :-

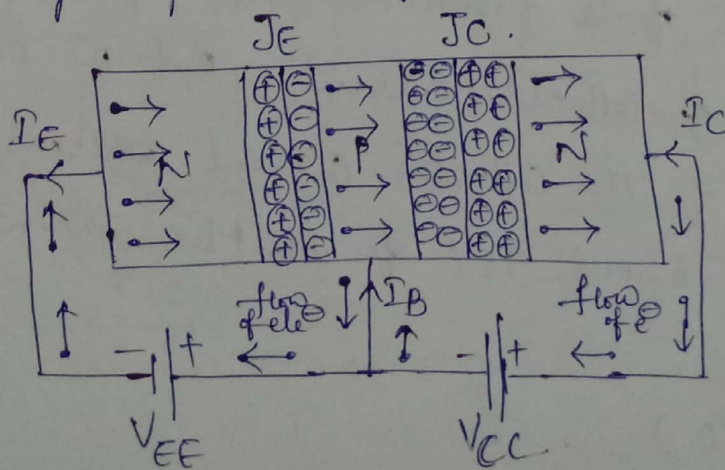
unbiased T/R means, transistor with no external voltage is applied. There will be no current flowing from any of the transistor. Since transistor is like a two p-n junction diodes connected back to back, there are depletion regions at both the junctions at emitter junction and collector junction.



During diffusion process, depletion region penetrates more deeply into to lightly doped side in order to include an equal number of impurity atoms in the each side of the junction.

Working of n-p-n Transistor :-

Let us consider n-p-n T/R, the base emitter junction is forward biased by dc source V_{EE} . Thus the depletion region width is reduced, the collector base junction is reverse biased increasing depletion region at collector to base junction.



The forward bias of the emitter-base junction pushes a large no. of free electrons in the n-type emitter towards the base. This makes the emitter current (I_E). A very few holes also pass from base to emitter region. This flow of electrons and holes constitute a current I_E . The direction of conventional current is always taken opposite to the flow of electrons.

After reaching the base region the electrons tend to recombine with holes. Since the base is very thin and lightly doped only few electrons combine with holes to constitute the base current (I_B). The remaining electrons pass on to the collector which is positively biased n-region. These e^- are collected by collector to constitute the collector current.

There is another current (Collector Current) due to the thermally generated minority carriers (holes in this case) which pass towards the base. This small current is called reverse saturation current.

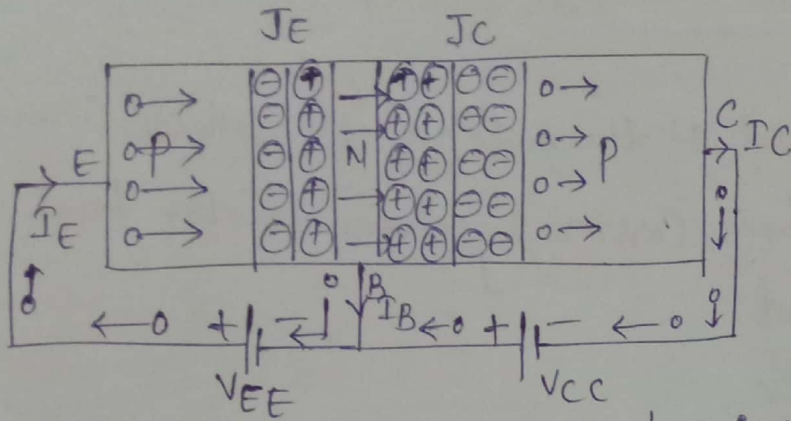
It is clear emitter current is sum of base current plus collector current

$$I_E = I_B + I_C$$

The emitter current of a transistor consists of two components. These are base current & collector current. But the base current is only about 2% of the collector current whereas the collector current is about 98% of emitter current.

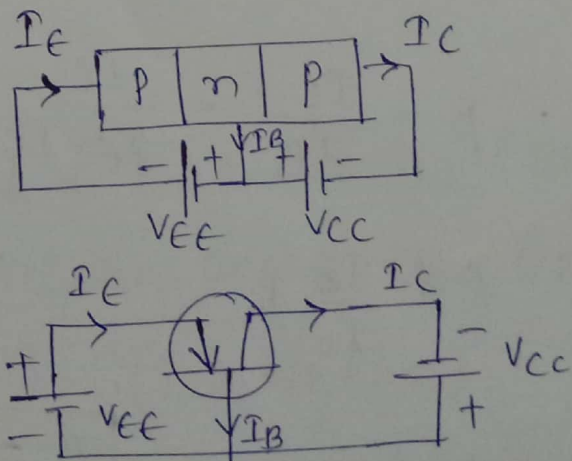
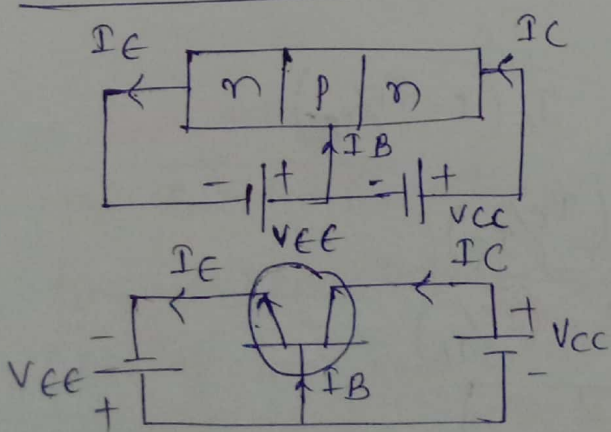
$$\therefore I_E \cong I_C \\ (I_B \cong 0)$$

Working of PNP T_xR:-



This is necessary to F-B the emitter base junction and reverse bias the collector base junction. The F-B I_E causes the holes in the p-type emitter to flow towards the base. This constitutes the emitter current I_E . As these holes flow through the n-type base, they tend to combine with electrons in n-region (base). As the base is very thin and lightly doped, very few of the holes injected into the base from the emitter recombine with electrons to constitute base current, I_B remaining large no. of holes cross the depletion region and move through the collector region to the negative terminal of the external d.c source. This constitutes collector current I_C . Thus the holes flow constitute the dominant current in an p-n-p T_xR.

* Transistor Currents:-



(*) Definition of α_{dc} and β_{dc} :-

α_{dc} :- It is defined as the ratio of the collector current resulting from carrier injection to the total emitter current.

$$\alpha \cong \alpha_{dc} \cong \frac{I_C}{I_E}$$

$\therefore \alpha$ is less than unity i.e. 0.95 to 0.995. $\because I_C < I_E$.
It represents in CB Configuration.

β_{dc} :- It is defined as the ratio of the collector current to the base current.

$$\beta_{dc} \cong \beta = \frac{I_C}{I_B}$$

(*) Relationship b/w α_{dc} and β_{dc} :-

We know that, $\beta = \frac{I_C}{I_B}$, $\alpha = \frac{I_C}{I_E}$

\therefore we have $I_E = I_B + I_C$

i.e. $I_B = I_E - I_C$

$$\therefore \beta = \frac{I_C}{I_B} = \frac{I_C}{I_E - I_C} = \frac{I_C}{I_E(1 - I_C/I_E)}$$

$$\therefore \left(\alpha = \frac{I_C}{I_E} \right) = \frac{(I_C/I_E)}{1 - (I_C/I_E)}$$

$$\boxed{\beta = \frac{\alpha}{1 - \alpha}}$$

$$\therefore \beta = \frac{\alpha}{1-\alpha}$$

By dividing $(1+\beta)$ on both sides,

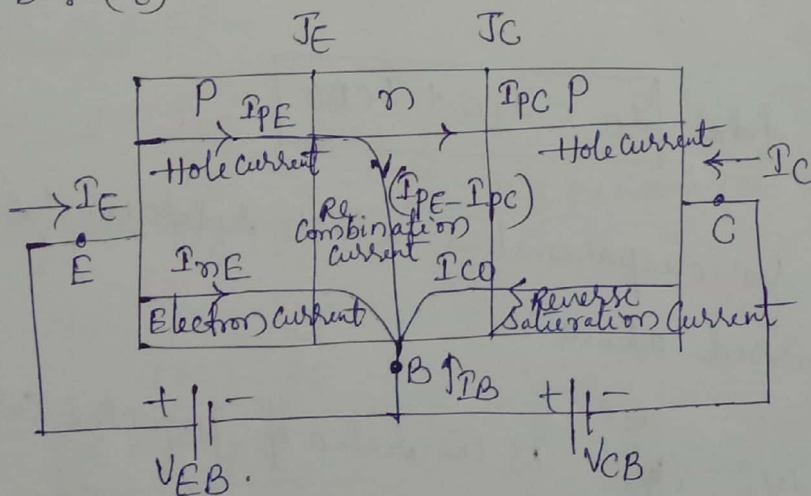
$$\frac{\beta}{(1+\beta)} = \frac{\alpha/(1-\alpha)}{(1+\beta)}$$

$$\frac{\beta}{1+\beta} = \frac{\alpha}{1-\alpha} \Rightarrow \frac{\alpha}{(1-\alpha)} = \frac{\alpha}{1-\alpha + \alpha} \Rightarrow \alpha$$

$$\boxed{\alpha = \frac{\beta}{1+\beta}}$$

* Transistor Current Components :-

Various Current Components in p-n-p transistor which flow across the forward biased emitter base junction (J_E) and the reverse biased collector base (J_C).



$$J_E = F \cdot B$$

$$J_C = R \cdot B$$

I_{pE} , I_{nE} , and I_{pC} :-

The emitter current I_E consists of hole current I_{pE} (holes crossing from emitter into base) and electron current I_{nE} (electron crossing from base to emitter). ($I_E = I_{pE} + I_{nE}$).

In transistor emitter is heavily doped as compared to base, thus, electron current is negligible as compared to current.

Thus Emitter Current in PNP transistor is mainly due to the flow of holes from emitter to base.

All the holes crossing the emitter base junction do not reach the collector junction. because some of them combine with electrons in N-type base.

Since width of the base is very small, most of holes cross the collector junction J_C and very few electrons recombine, constitute the base current $I_B = (I_{PE} - I_{PC})$

Reverse Saturation Current:-

Since Collector ~~current~~ ^{Junction} is R.B, which acts as reverse biased PN junction diode, a reverse saturation current I_{CO} flows across this junction. called I_{CBO} (^{Emitter} ~~Base~~ is open circuitary, i.e.

$I_E = 0$)

so total $I_C = I_{PC} + I_{CBO}$

Now, we define various parameters which relate the current components discussed above.

Emitter efficiency (γ) : is the ratio of injected carriers at emitter base junction to total emitter current.

$$\gamma = \frac{\text{Current of injected carriers at } I_E}{\text{Total Emitter current}}$$

In case P-n-p γ_B

$$\gamma = \frac{I_{PE}}{I_{PE} + I_{nE}} = \frac{I_{PE}}{I_E}$$

($\because I_{PE} \gg I_{nE}$
since γ nearly equal to 1)

Transport Factor (β): -

It is the ratio of injected carriers current reaching at collector base junction I_C to injected carriers at emitter base junction I_E .

$$\beta = \frac{I_{PC}}{I_{PE}}$$

Large α_{dc} Current Gain (α_{dc}): -

It is the ratio of current due to the injected carriers I_{PC} to total emitter current I_E .

$$\alpha_{dc} = \frac{I_{PC}}{I_E} = \frac{I_C - I_{CO}}{I_E}$$

$$\alpha_{dc} \cdot I_E = I_C - I_{CO}$$

$$I_C = \alpha_{dc} \cdot I_E + I_{CO}$$

$$\therefore I_C = I_{PC} + I_{CO}$$

$$I_{PC} = I_C - I_{CO}$$

Relation b/w α , β , γ : -

$$\therefore \alpha = \frac{I_{PC}}{I_E}$$

By multiplying numerator & denominator by I_{PE}

$$\alpha = \frac{I_{PC}}{I_{PE}} \times \frac{I_{PE}}{I_E}$$

$$\alpha = \beta \cdot \gamma$$

$$\left(\because \beta = \frac{I_{PC}}{I_{PE}}, \gamma = \frac{I_{PE}}{I_E} \right) //$$

The transistor alpha is the product of transport factor and emitter efficiency.

① a) Find α for each of the following values of $\beta = 50$ and 190.

b) Find β for each of the values of $\alpha = 0.995$ and 0.9765

Sol:- a) $\alpha = \frac{\beta}{1+\beta}$

$$\therefore \beta = 50; \alpha = \frac{50}{1+50} = 0.9804$$

$$\therefore \beta = 190; \alpha = \frac{190}{1+190} = 0.9947$$

$$b) \therefore \alpha = 0.995; \beta = \frac{\alpha}{1-\alpha} = \frac{0.995}{1-0.995} = 199$$

$$\therefore \alpha = 0.9765; \beta = \frac{\alpha}{1-\alpha} = \frac{0.9765}{1-0.9765} = 41.55$$

② If the base current in a transistor is $20 \mu A$ when the emitter current is 6.4 mA , what are the values of α_{dc} and β_{dc} ? Also calculate the collector current?

Sol:- $I_B = 20 \mu A$, $I_E = 6.4 \text{ mA}$

$$\alpha = \frac{\beta}{1+\beta}$$

$$\alpha = \frac{319}{1+319}$$

$$\therefore \alpha = 0.9968$$

$$\therefore I_E = I_B + I_C$$

$$I_B = \beta I_B$$

$$I_E = (1+\beta) I_B$$

$$(1+\beta) = \frac{I_E}{I_B}$$

$$\beta = \frac{I_E}{I_B} - 1 \Rightarrow \frac{6.4 \times 10^{-3}}{20 \times 10^{-6}} - 1$$

$$\beta = 320 - 1$$

$$\beta = 319$$

$$\therefore I_C = \beta I_B$$
$$= 319 \times 20 \times 10^{-6}$$
$$= 6380 \mu A$$

$$I_C = 6.38 \text{ mA}$$

$$\therefore I_C = \alpha I_E$$
$$= 0.9968 \times 6.4 \times 10^{-3}$$

$$I_C = 6.379 \text{ mA}$$

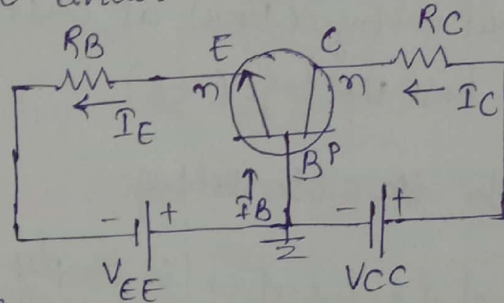
BJT Configurations:-

Its can be connected in a ckt in the following three configurations.

1. Common base Configuration
2. Common Emitter Configuration
3. Common Collector Configuration.

① Common base Configuration:-

In this configuration input is applied b/w emitter and base and o/p is taken from the collector and base. Hence base of the transistor is common to both input and output circuits and hence the name common base configuration.



Here $I_C = \alpha_{dc} I_E + I_{CBO}$.

I_{CBO} is reverse saturation current is temperature sensitive and it doubles for every $10^\circ C$ rise in temperature. Since I_{CBO} is negligible i.e. small in practical cases.

$$\therefore I_C = \alpha_{dc} I_E$$

$$\alpha_{dc} = \frac{I_C}{I_E}$$

For a transistor, $I_E = I_B + I_C$

$$I_E = I_B + \alpha_{dc} I_E + I_{CBO}$$

$$I_B = I_E - \alpha_{dc} I_E - I_{CBO}$$

$$I_B = I_E(1 - \alpha_{dc}) - I_{CBO}$$

By neglecting I_{CBO} , can be written as

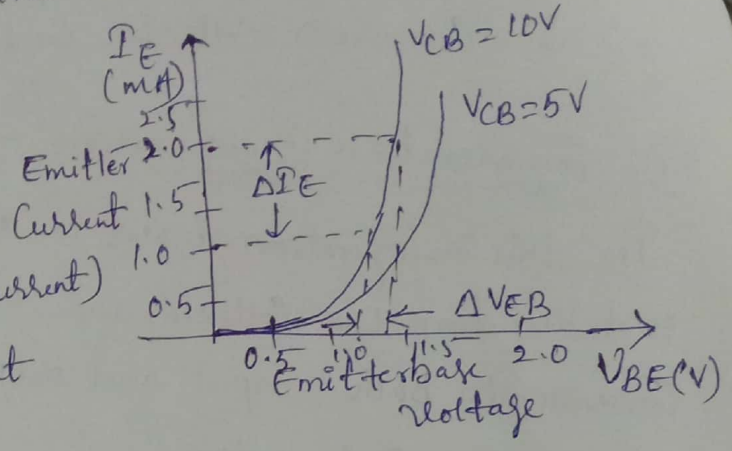
$$I_B = I_E(1 - \alpha_{dc})$$

Common Base V-I Characteristics:-

To understand the complete electrical behaviour of the transistor it is necessary to study the interrelation of the various currents and voltage. These relationships can be plotted graphically which are commonly known as the characteristics of transistor. i.e. i_p & o_p characteristics.

* Input Characteristics:-

It is the curve b/w I_E (input current) and input voltage (V_{EB}) at constant collector base voltage (V_{CB}).

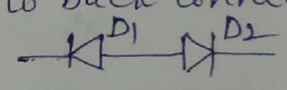


* ~~After the cut-in voltage~~

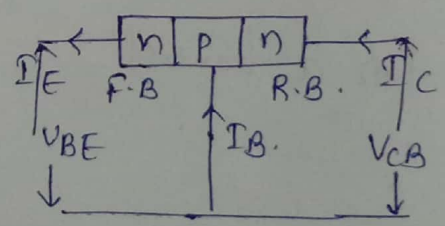
- The i_p characteristics is simply characteristics of F-B diode
- Graphical relation of i_p I vs i_p V for diff o_p ~~values~~ voltages.

$$\begin{aligned} i_p I &= I_E & o_p I &= I_C \\ i_p V &= V_{BE} & o_p V &= V_{CB} \end{aligned}$$

→ we can consider a transistor as two diodes, back to back connection for active mode.



→ The i_p characteristics is similar to i_p V characteristic of diode.

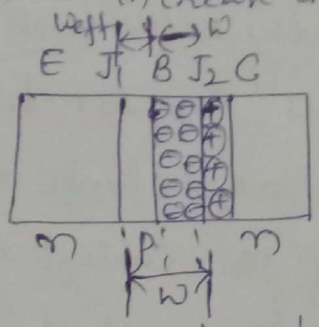


But important point is here the effect on i_p characteristic by changing V_{CB} .

→ To understand this effect, first understand early effect.

Early Effect:- Known as base width modulation. It is named after James M. Early. This effect found when we increase in V_{CB} .

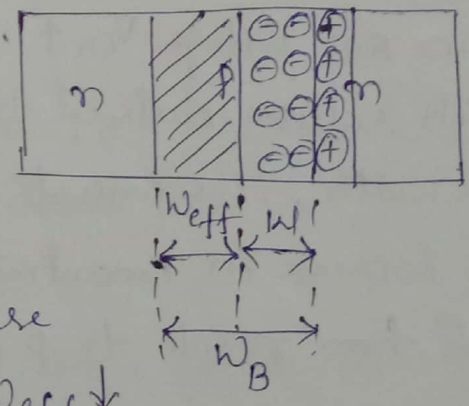
→ Here J_1 F.B, J_2 R.B in R.B the width of depletion region is more and mostly penetrate in to the Base region because base is lightly doped.



→ The depletion region has immobile ions and -ve immobile ions at Base and the immobile ions at collector because it is n-p-n T x B.

→ let say w_B = width of base (d_2) Metallurgical base width
 w = width of depletion layer penetrate into the Base region
 $w_{effective}$ = width of Base with no depletion region.

$w_B = w_{eff} + w$
 $w_{eff} = w_B - w$



Now if we increase $V_{CB} \uparrow$ the reverse voltage potential; w will \uparrow , when $w \uparrow$ $w_{eff} \downarrow$. where w_{eff} is the region where recombination takes place. ~~wid~~ when $w_{eff} \downarrow$

The chance of recombination at Base region \downarrow . In this situation $I_E \uparrow$.

→ The other reason is increase in concentration gradient \uparrow .
 The e^- in 'n' side move ~~from~~ to Base, as the area decrease the concentration gradient increases. So more no. of e^- move towards base this will also increase $I_E \uparrow$. i.e if we increase $V_{CB} \uparrow$ op voltage, the o/p current $I_E \uparrow$. And the remaining characteristics are similar to F.B of P-n junction.

① Input Characteristics:-
 1) After cut-in voltage (0.7) for Si, The emitter current I_E rapidly with small increase in emitter base voltage (V_{EB}). It means r_{iE} resistance is very small. Because r_{iE} resistance is a ratio of change in emitter base voltage (ΔV_{EB}) to resulting change in current (ΔI_E) at constant V_{CB} . This resistance is also known as dynamic r_{iE} resistance of $T_x R$ in CB configuration.

$$r_{iE} = \left. \frac{\Delta V_{EB}}{\Delta I_E} \right|_{V_{CB} = \text{const}} \quad \text{increase}$$

2) It can be observed that, there is slight change in emitter current (I_E) with increase in V_{CB} . is due to change in width of depletion region in the base region under the reverse bias condition.

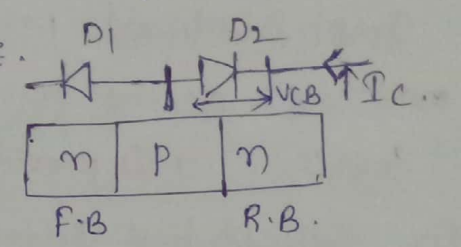
When R.B. voltage $V_{CB} \uparrow$, the width of depletion region also \uparrow , which reduces electrical base width. Due to reduction of the electrical base width, the concentration gradient \uparrow in the base region. This increase in concentration of charge carriers causes more diffusion of e^- from n side to p side, that increase in I_E slightly.

Early Effect:- When R.B. $V_{CB} \uparrow$, the width of depletion \uparrow which reduces \downarrow the electrical base width. This effect is known as early effect. (2) Base width modulation.

This \downarrow in base width has two consequences:

1. There is less chance for recombination within the base region. Hence the transport factor α and β \uparrow with increase in collector junction voltage.
2. The charge gradient increases within the base, & the current of minority carriers injected across the junction increases.

Output characteristics: - It is the curve b/w o/p voltage V_{CB} and o/p current I_C at const emitter current I_E .



→ at diode D_2 The o/p current I_C and voltage across diode is V_{CB} . So o/p characteristics simply the R.B. of diode characteristics.

→ on x-axis $V_{CB}(V)$,
y-axis $I_C(mA)$

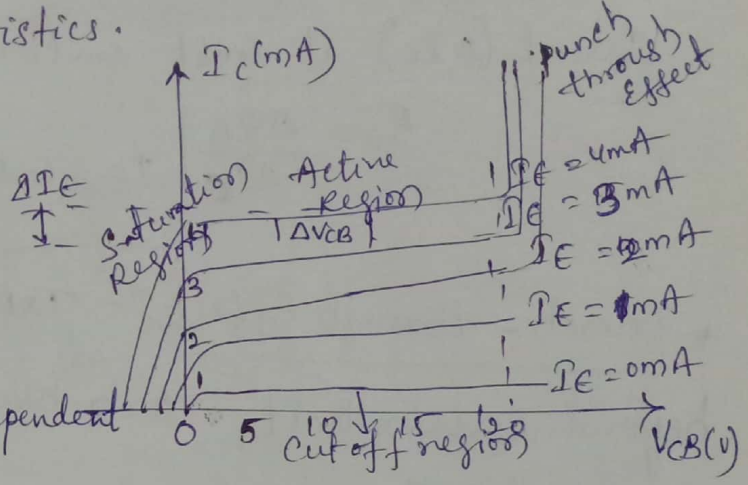
→ We know $I_C \approx \alpha I_E + I_{CBO}$

I_{CBO} is reverse leakage current which is negligible and it is independent of V_{CB} .

$I_C \approx \alpha I_E$

$I_C = I_E$ ($\because \alpha \approx 0.95 \text{ to } 0.995$ Equal to 1)

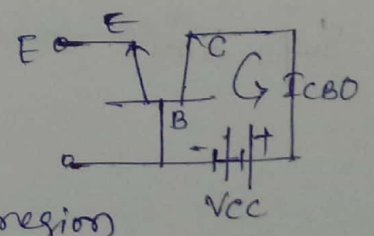
→ i.e o/p current depending on i/p current I_E . when $I_E = 0mA$. This small current is reverse saturation current I_{CBO} .



o/p characteristics has 3 basic regions:-

Cutoff region: If the emitter current is zero, the collector current is simply I_{CBO} . This current is very small in magnitude compared to the vertical curve. The region below the curve $I_E = 0$ is known as Cutoff region. when collector current $I_C = 0$. when I_E, I_C are R.B.

Saturation region: - This region is left of $V_{CB} = 0$. Exponential increase in collector current as the voltage V_{CB} increases towards zero 0V. in this region the I_E, I_C are both F.B.



Active region :- The collector current is almost const, and graph is almost parallel with x-axis. The I_C is always independent on V_{CB} . depend on I_E so almost T/R acts as const-current source. This provides high dynamic resistance (i.e. the ratio of change in collector base voltage (ΔV_{CB}) to resulting change in collector current (ΔI_C) at const emitter current I_E .

$$R_o = \left. \frac{\Delta V_{CB}}{\Delta I_C} \right|_{I_E = \text{const}}$$

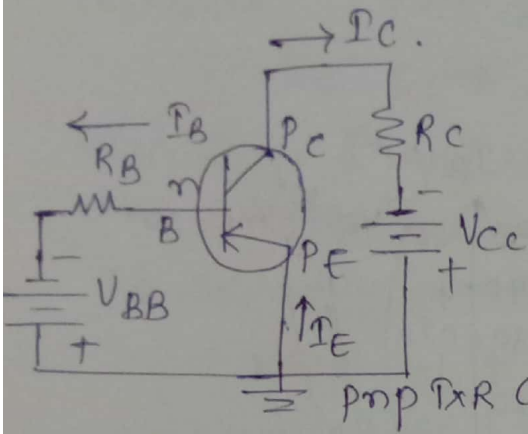
* punch-through effect :- T/R breakdown occurs when $V_{CB} \uparrow$ beyond certain limit. Such break down known as punch through or reach-through.

→ The potential barriers at the junction of transistor when EB junction is F.B and CB junction is R.B. The F.B EB junction ↓ the potential barrier by $|V_{EB}|$ and R.B of CB junction ↑. when CB Junction ↑, The effective base width reduces ↓. when CB Junction ↑ beyond certain limit effective base width reduces ↓ to zero. This result emitter & collector shorted.

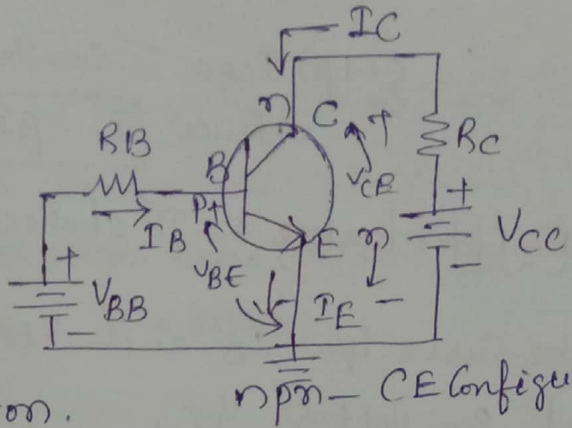
This cause large increase in emitter current (I_E) resulting breakdown. ∴ It is necessary to keep of the V_{CB} within safe limit to avoid punch through (or) reach through breakdown.

Common Emitter Transistor Configuration

In this configuration V_p is applied b/w base and emitter, and output is taken from the collector and emitter. Here emitter of transistor is common for both input and output ckt. and hence name is called Common Emitter Configuration.



PNP TR CE Configuration.



npn - CE Configuration

As shown in fig: $I_E = F.B$ by V_{BB} and I_C is R.B by V_{CC} .

The input voltage in the CE Configuration is the base emitter voltage V_{BE} , and o/p voltage is the collector emitter voltage. The i/p current is I_B and o/p current is I_C .

∴ Now how the o/p current is relate with the i/p current I_B .

∴ We have seen $I_C = \alpha I_E + I_{CBO}$

$$I_C - I_{CBO} = \alpha I_E$$

$$\frac{I_C}{\alpha} - \frac{I_{CBO}}{\alpha} = I_E \quad (\because I_E = I_B + I_C)$$

$$\frac{I_C}{\alpha} - \frac{I_{CBO}}{\alpha} = I_B + I_C$$

$$I_C \left(\frac{1}{\alpha} - 1 \right) = I_B + \frac{I_{CBO}}{\alpha}$$

$$I_C \left(\frac{1-\alpha}{\alpha} \right) = I_B + \frac{I_{CBO}}{\alpha}$$

$$I_C = \left(\frac{\alpha}{1-\alpha} \right) I_B + \frac{I_{CBO}}{1-\alpha}$$

$$I_C = \beta I_B + (1+\beta) I_{CBO}$$

$\because \beta = \frac{\alpha}{1-\alpha}$
 $\therefore (1+\beta) = \frac{1}{1-\alpha}$

The term $(1+\beta)I_{CBO}$ is the reverse leakage current.
 Common Emitter Configuration.

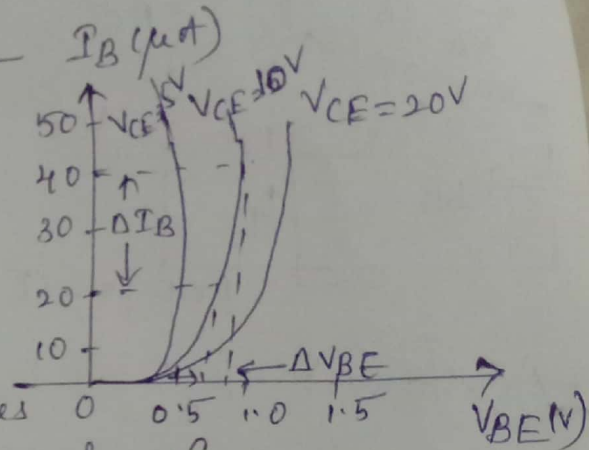
$$I_{CEO} = (1+\beta)I_{CBO}$$

The value of $\alpha = 0.995$ which is less than 1, and the value of β is from 20 to hundreds. $\beta = \frac{\alpha}{1-\alpha} = \frac{0.995}{1-0.995} = 199$.

$(1+\beta)I_{CBO}$ is smaller than βI_B .

$$I_C = \beta I_B$$

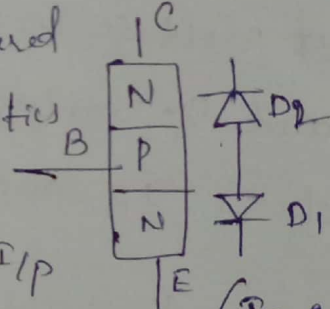
Common Emitter input V-I characteristics: -



→ It is the curve b/w I_B and V_{BE} at constant V_{CE} voltage.

V_{BB} & V_{CC} are biasing potentials.

→ We can also consider an npn TR as two diodes back to back, i.e. diode D_1 in F-B can be considered as i/p characteristics of CE TR is characteristics of F-B diode.



(I_B is current through the diode V_{BE} voltage across diode)

→ But important point, the variation of V_{CE} on i/p characteristics of TR.

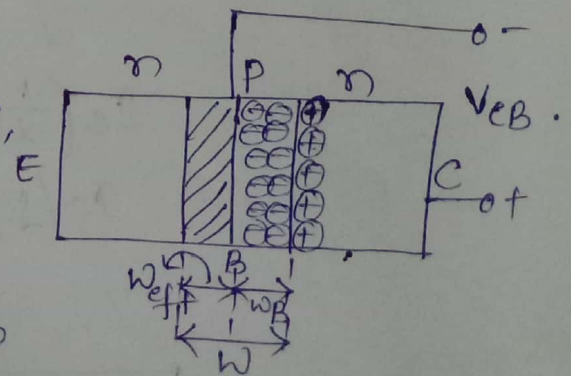
→ The current I_B exponentially increases when i/p voltage $>$ barrier potential $>$ V_{BE} voltage.

($V_{BE} > V_b$) ; Dynamic i/p resistance $r_i = \frac{\Delta V_{BE}}{\Delta I_B} \Big|_{V_{CE} = \text{const}}$

→ The effect of V_{CE} on I_B due to early effect: i.e. for a fixed V_{BE} value, I_B decreases as V_{CE} increases.

→ Since in active region J_E forward biased, J_C reverse biased. Due to R.B of J_C

The width of depletion region increases at J_C and more widely penetrate into lightly doped Base region.



W_B = Width of base region

W = Width of depletion region in Base

W_{eff} = Effective base width without depletion regions.

$$W_B = W_{eff} + W$$

$$\downarrow W_{eff} = W_B - W \uparrow$$

and V_{CE} is o/p voltage is $V_{CB} + V_{BE}$ if we increase V_{CE} , V_{CB} also increases. $\uparrow V_{CE} = \uparrow V_{CB} + V_{BE}$

i.e. $V_{CE} \uparrow \Rightarrow V_{CB} \uparrow \Rightarrow W \uparrow \Rightarrow W_{eff} \downarrow \Rightarrow I_B \downarrow$ (i/p current)

When $W_{eff} \downarrow$, the region which recombination reduces, The e^- from Emitter are not recombine with holes present in Base it will reduce Base current I_B . Vice versa.

When $V_{CE} \uparrow \rightarrow I_B \downarrow$ by.

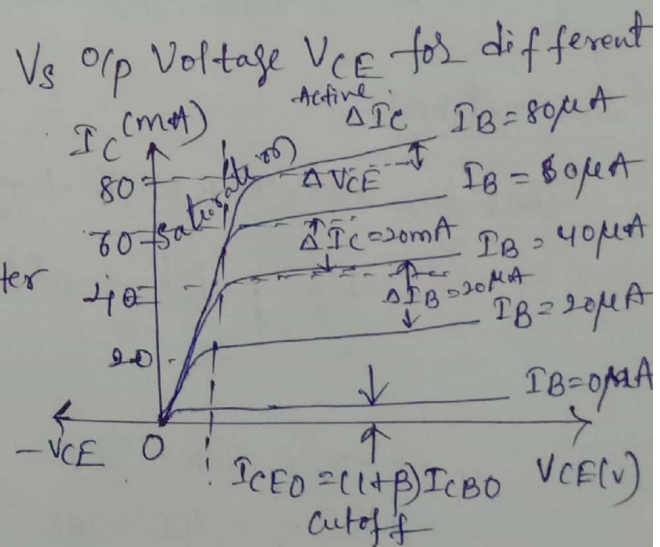
Vice versa $V_{CE} \downarrow \rightarrow I_B \uparrow$. In $V-I$ characteristics we can

observe, by decreasing o/p voltage $V_{CB} \downarrow$, it increase i/p current $I_B \uparrow$

O/p - characteristics:-

Graphical relation b/w o/p current I_C Vs o/p Voltage V_{CE} for different levels of I_B .

The o/p characteristics of Common Emitter Configuration consists of three regions Active, Saturation and Cutoff.



- Cutoff region:- when the i/p base current is made equal to zero, The collector current is reverse leakage current I_{CBO} . to make I_{CB} cutoff region, it is not enough to reduce $I_B = 0$ instead, it is necessary to R.B the emitter base junction slightly. Here in this situation collector current equal to reverse saturation current.

$$I_C = \beta I_B + (1 + \beta) I_{CBO}$$

$$I_B = 0$$

$$I_C = (1 + \beta) I_{CBO} \text{ Small current R.S.C.}$$

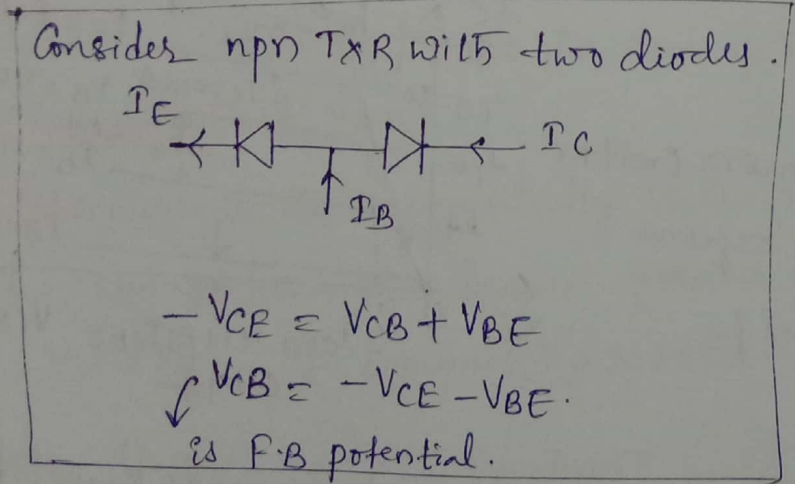
saturation
0.2V
base

Active region: - In the active region the curve is approximately horizontal is "active" region of CE Configuration. In the active region the collector junction is R.B. As V_{CE} increased, R.B \uparrow . This causes depletion to spread more in base than in collector reducing the chances of recombination in the base. This increases the value of α_{dc} . This collector current early effect causes, I_C to rise more sharply with increase in V_{CE} in the active region of o/p characteristics.

when $V_{CE} = V_{CB} + V_{BE}$ → R.B b/w c & b.

when $V_{CB} \uparrow \Rightarrow$ The $w_{eff} \downarrow \Rightarrow I_B \downarrow \Rightarrow I_C \uparrow$

Saturation: -



Here V_{CE} is very small in this region.
 where $V_{CB} = -V_{CE} - V_{BE}$

When V_{CE} is very small, V_{CB} is -ve. make D_2 diode also F.B.

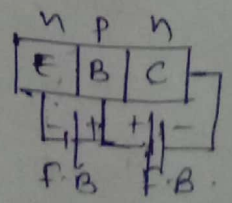
$$\beta = \frac{\Delta I_C}{\Delta I_B}$$

$$= \frac{(40 - 20) \text{ mA}}{(40 - 20) \mu\text{A}}$$

$$= \frac{20 \times 10^{-3}}{20 \times 10^{-6}}$$

$$\beta \Rightarrow 1000$$

When $I_B = 20 \mu\text{A}$
 $I_C = \beta I_B$
 $= 1000 \times 20 \times 10^{-6}$
 $I_C = 20 \text{ mA}$
 Amplifier.

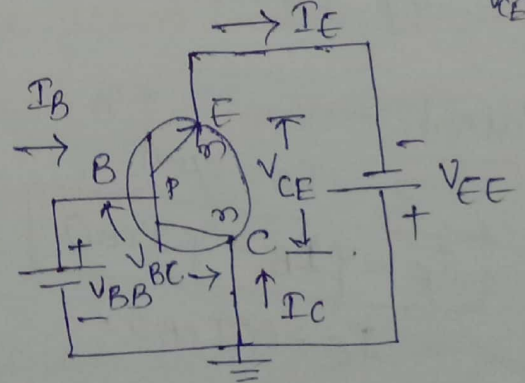
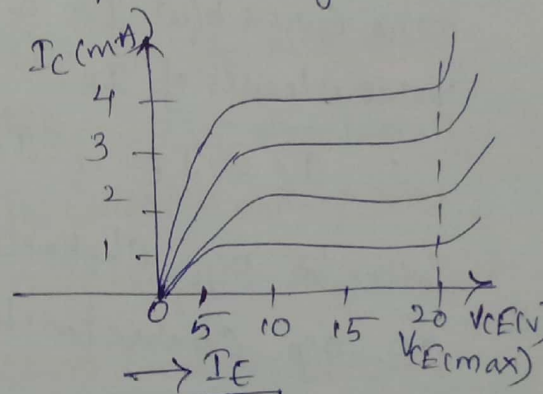


Saturation Region:— If V_{CE} is reduced to small value i.e. 0.2V, the collector base junction becomes F.B, since Emitter base junction is already F.B by 0.7V, the i/p junction in CE configuration is base to emitter junction. which is always forward to operate transistor in active region. When both the junctions are in F.B, Transistor operates in Saturation region. which is indicated in o/p characteristics. The saturation value of V_{CE} , is $(V_{CE})_{sat}$ usually ranges b/w 0.1V and 0.3V.

punchthrough:— In the active region, the collector base junction is reverse biased, for every transistor there is a limit on maximum value of R.B voltage. If the limit increases (exceeded) breakdown occurs. In the TxB this effect is known as punchthrough effect.

③ Common Collector Configuration:-

In this configuration I_p is applied b/w Base and collector and o/p is taken from Emitter and collector. Here collector is common to both i/p & o/p called Common collector Configuration.



$$\begin{aligned} \therefore I_E &= I_B + I_C \\ \div I_B & \\ \frac{I_E}{I_B} &= 1 + \frac{I_C}{I_B} \end{aligned}$$

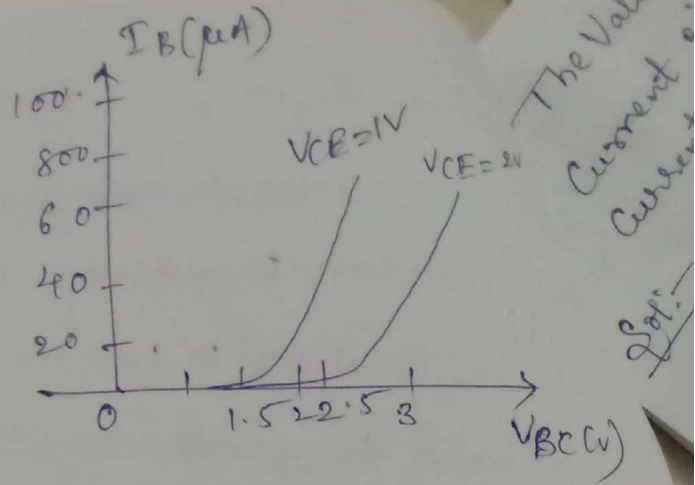
$$\boxed{\therefore V_{CE} = V_{BE} + V_{BC}}$$

o/p voltage.

$$\boxed{\gamma = 1 + \beta} = 1 + \frac{\alpha}{1 - \alpha} = \frac{1 - \alpha + \alpha}{1 - \alpha} = \frac{1}{1 - \alpha} = \gamma$$

I_p characteristics :-

I_p voltage (V_{BC}) Vs I_p current I_B for different values of V_{CE}.



$$V_{CE} = V_{CB} - V_{BE}$$

$$\gamma = (1 + \beta) = \frac{1}{(1 - \alpha)}$$

$$I_E = (1 + \beta) I_B + (1 + \beta) I_{CBO}$$

$$I_E = I_C + I_B \quad \text{--- (1)}$$

$$I_C = \alpha I_E + I_{CBO} \quad \text{--- (2)}$$

$$I_E = \alpha I_E + I_{CBO} + I_B$$

$$(1 - \alpha) I_E = I_B + I_{CBO}$$

$$I_E = \frac{I_B}{(1 - \alpha)} + \frac{I_{CBO}}{(1 - \alpha)}$$

$$I_E = \gamma I_B + \gamma I_{CBO}$$

O_p characteristics :-

The curve b/w I_E & V_{CE} for various levels of I_B.

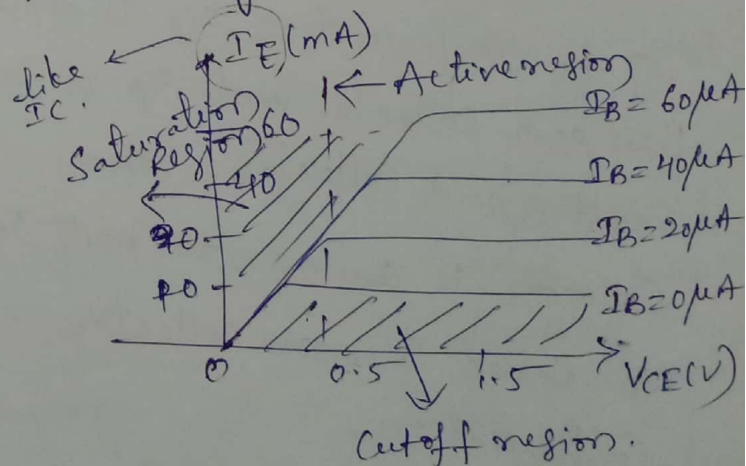
∴ I_C ≈ I_E ; I_C is approximately I_E, The common collector o/p characteristics are practically similar to those of CE o/p characteristics.

(1) Cutoff :-

$$\therefore I_B = 0 \mu A$$

$$I_E = \gamma I_B + \gamma I_{CBO}$$

$$I_E = \gamma I_{CBO}$$



(2) Active Region :-

$$I_E \approx \gamma I_B$$

We know: $I_C = \alpha I_E + I_{CBO}$

$$I_C = \alpha I_E$$

$$\alpha = 0.95 \text{ to } 0.995$$

$$I_C \approx I_E //$$

General procedure to obtain stability factor: - (S)

(14)

For Common Emitter Configuration, Collector Current Equation.

$$I_C = \beta I_B + (1 + \beta) I_{CBO}$$

Apply 'd' on both sides.

$$\partial I_C = \beta \cdot \partial I_B + (1 + \beta) \partial I_{CBO}$$

$$1 = \beta \cdot \frac{\partial I_B}{\partial I_C} + (1 + \beta) \frac{\partial I_{CBO}}{\partial I_C}$$

$$1 - \beta \cdot \frac{\partial I_B}{\partial I_C} = (1 + \beta) \frac{\partial I_{CBO}}{\partial I_C}$$

$$\frac{\partial I_{CBO}}{\partial I_C} = \frac{1 - \beta \cdot \frac{\partial I_B}{\partial I_C}}{1 + \beta}$$

$$\left(\because S = \frac{\partial I_C}{\partial I_{CBO}} \right)$$

$$\frac{1}{S} = \frac{1 - \beta \cdot \frac{\partial I_B}{\partial I_C}}{1 + \beta}$$

$$S = \frac{(1 + \beta)}{1 - \beta \cdot \left(\frac{\partial I_B}{\partial I_C} \right)} //$$

(*) Biassing Circuits: -

Need: - Transistor should be operated in the middle of the active region. (or) operating point should be in the centre of the active region.

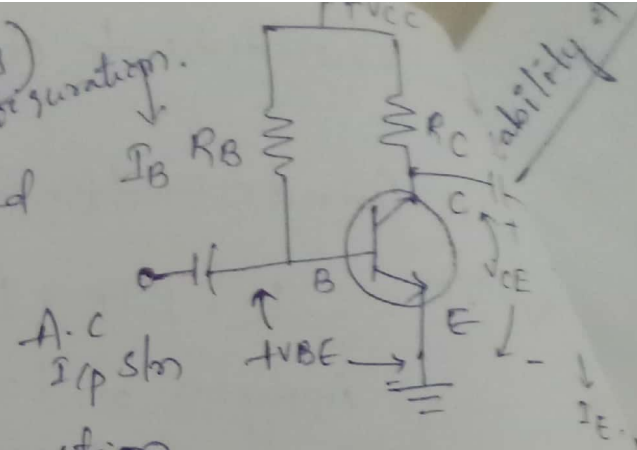
Basically, 4 types of Biassing ckt's.

1. Fixed Bias ckt
2. Collector to Base bias ckt
3. Voltage divider Bias / Self bias
4. Emitter stabilized bias ckt

① Fixed bias circuit :- (Base bias configuration)

Here the base current I_B is controlled by the value of R_B . and I_C is related to I_B by a constant β .

The magnitude of I_C is not a function of resistance R_C . Changing R_C to any level will not affect the level of I_B or I_C as long as we remain in the active region.



Apply KVL at i/p side.

$$V_{CC} - I_B R_B - V_{BE} = 0$$

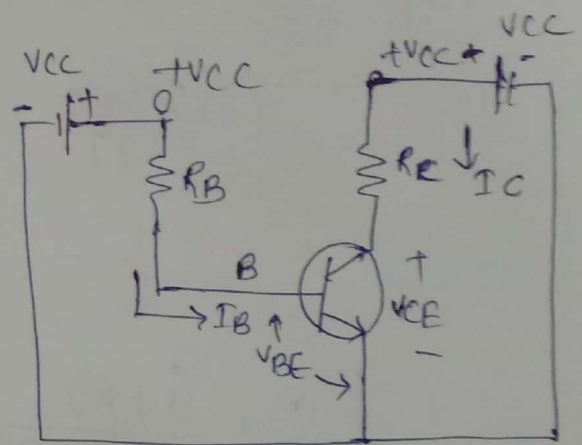
$$I_B = \frac{V_{CC} - V_{BE}}{R_B} \quad \text{--- (1)}$$

Apply K.V.L at o/p side.

$$V_{CC} - I_C \cdot R_C - V_{CE} = 0$$

$$I_C = \frac{V_{CC} - V_{CE}}{R_C}$$

$$I_C = -\frac{1}{R_C} \cdot V_{CE} + \frac{V_{CC}}{R_C}$$



Stability factor :-

$$\therefore I_B = \frac{V_{CC}}{R_B} - \frac{V_{BE}}{R_B}$$

When I_B changes by ∂I_B , V_{CC} and V_{BE} are unaffected

$$\therefore \frac{\partial I_B}{\partial I_C} = 0$$

$\therefore I_C$ is not present in the Equations.

$$\therefore S = \frac{(1+\beta)}{1 - \beta \cdot \left(\frac{\partial I_B}{\partial I_C}\right)} = \frac{1+\beta}{1-0} = (1+\beta) \gg 1$$

Stability factor (s'):

$$s' = \frac{\partial I_C}{\partial V_{BE}} \Big|_{I_{C0}, \beta \text{ const}}$$

$$\therefore I_C = \beta I_B + (1 + \beta) I_{CBO}$$

Now representing I_B in terms of V_{BE}

$$I_C = \beta \cdot \frac{(V_{CC} - V_{BE})}{R_B} + (1 + \beta) I_{CBO}$$

$$I_C = \beta \cdot \frac{V_{CC}}{R_B} - \beta \cdot \frac{V_{BE}}{R_B} + (1 + \beta) I_{CBO}$$

$$\partial I_C = \beta \cdot \frac{V_{CC}}{R_B} - \beta \cdot \frac{V_{BE}}{R_B} + (1 + \beta) \partial I_{CBO}$$

$$\frac{\partial I_C}{\partial V_{BE}} = 0 - \frac{\beta}{R_B} + 0 \Rightarrow -\beta/R_B$$

$$\boxed{s' = -\frac{\beta}{R_B}}$$

Relation b/w S and s' :

We know that $S = (1 + \beta)$, $s' = -\frac{\beta}{R_B}$

$s' = -\beta/R_B$. multiply & divide with $(1 + \beta)$

$$s' = \frac{-\beta(1 + \beta)}{R_B(1 + \beta)} = \frac{-\beta \cdot S}{R_B \cdot (1 + \beta)}$$

Stability factor (s''):

$$s'' = \frac{\partial I_C}{\partial \beta} \Big|_{V_{BE}, I_{CBO} \text{ const}}$$

$$I_C = \beta I_B + (1 + \beta) I_{CBO}$$

$$I_C = \beta \frac{(V_{CC} - V_{BE})}{R_B} + (1 + \beta) I_{CBO}$$

$$I_C = \beta \cdot \frac{V_{CC}}{R_B} - \beta \cdot \frac{V_{BE}}{R_B} + (1+\beta)I_{CBO}$$

$$\partial I_C = \beta \cdot \frac{V_{CC}}{R_B} - \beta \cdot \frac{V_{BE}}{R_B} + (1+\beta)I_{CBO}$$

$$\frac{\partial I_C}{\partial \beta} = \left(\frac{V_{CC}}{R_B} - \frac{V_{BE}}{R_B} \right) + I_{CBO}$$

$$\frac{\partial I_C}{\partial \beta} = I_B + I_{CBO}$$

($\because I_B \gg I_{CBO}$)

$$S^4 = \boxed{\frac{\partial I_C}{\partial \beta} = I_B \Rightarrow I_C/\beta}$$

Relation b/w S and S^4 :-

We know $S = (1+\beta)$, $S^4 = I_C/\beta$.

Multiplying numerator and denominator by $(1+\beta)$

$$S^4 = \frac{I_C(1+\beta)}{\beta(1+\beta)} = \frac{I_C \cdot S}{\beta \cdot (1+\beta)} //$$

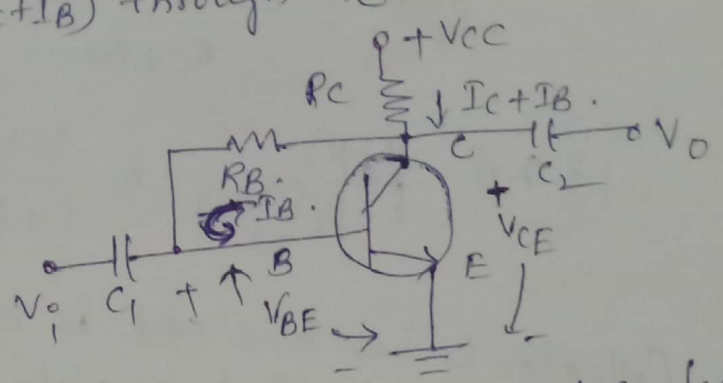
* Disadvantages :- Since $I_C = \beta I_B$, and I_B is already fixed I_C depends on β which changes unit to unit and shifts the operating point.

Thus stabilization of operating point is very poor in the fixed bias circuit.

Collector to base bias: - (Collector feedback bias) (3)

In this biasing resistor is connected b/w collector and base of the transistor. to provide feedback path. Thus I_B flows through R_B and $(I_C + I_B)$ through R_C . (16)

By Applying KVL to the base ckt.



$$V_{CC} - (I_B + I_C)R_C - I_B R_B - V_{BE} = 0$$

D.C bias with voltage feed back.

$$V_{CC} = (R_B + R_C) I_B + I_C R_C + V_{BE}$$

$$V_{CC} = (R_B + R_C) I_B + \beta \cdot I_B \cdot R_C + V_{BE}$$

$$I_B = \frac{V_{CC} - V_{BE}}{(1 + \beta)R_C + R_B} \quad (\beta \gg 1)$$

$$I_B = \frac{V_{CC} - V_{BE}}{R_B + \beta R_C}$$

By Applying KVL to Collector ckt

$$V_{CC} - (I_C + I_B)R_C - V_{CE} = 0$$

$$V_{CE} = V_{CC} - (I_C + I_B)R_C$$

$$V_{CC} - V_{CE} = (I_C + I_B)R_C$$

$$I_C = \frac{V_{CC} - V_{CE} - I_B R_B}{R_B}$$

Stability factor for collector to base bias: -

$$V_{CC} - (I_C + I_B)R_C - I_B R_B - V_{BE} = 0$$

$$V_{CC} = I_C R_C + (R_C + R_B) I_B + V_{BE}$$

Apply 'd' on both sides.

$$0 = \partial I_C R_C + \partial I_B (R_C + R_B)$$

$$-\partial I_C \cdot R_C = \partial I_B \cdot (R_C + R_B)$$

$$\frac{\partial I_B}{\partial I_C} = \frac{-R_C}{R_C + R_B}$$

$$\therefore S = \frac{(1+\beta)}{1 - \beta \left(\frac{\partial I_B}{\partial I_C} \right)} = \frac{(1+\beta)}{1 + \beta \left(\frac{R_C}{R_C + R_B} \right)}$$

Collector to base bias ckt is having lesser stability factor than for fixed bias ckt. Hence this ckt provides better stability than fixed bias.

Stability factor (S'): -

$$S' = \frac{\partial I_C}{\partial V_{BE}} \Big|_{I_{CO}, \beta \text{ const}}$$

From base ckt

$$V_{CC} - (I_C + I_B)R_C - I_B R_B + V_{BE} = 0 \Rightarrow V_{CC} - I_C R_C - I_B (R_B + R_C) + V_{BE} = 0$$

$$I_B = \frac{V_{CC} - V_{BE} - I_C R_C}{R_B + R_C}$$

$$\frac{I_C}{\beta} = \frac{V_{CC} - I_C R_C - V_{BE}}{R_B + R_C}$$

$$\frac{I_C}{\beta} + \frac{I_C R_C}{R_B + R_C} = \frac{V_{CC} - V_{BE}}{R_B + R_C}$$

$$I_C \left[\frac{1}{\beta} + \frac{R_C}{R_B + R_C} \right] = \frac{V_{CC} - V_{BE}}{R_B + R_C}$$

$$I_C \left[\frac{R_B + R_C + \beta R_C}{\beta (R_B + R_C)} \right] = \frac{V_{CC} - V_{BE}}{R_B + R_C}$$

$$I_C (R_B + R_C + \beta R_C) = (V_{CC} - V_{BE}) \beta$$

$$R_C (1 + \beta) I_C = (V_{CC} - V_{BE}) \beta$$

$$I_C [R_B + (1 + \beta) R_C] = (V_{CC} - V_{BE}) \beta$$

$$I_C = \frac{(V_{CC} - V_{BE}) \beta}{R_B + (1 + \beta) R_C}$$

$$\frac{\partial I_C}{\partial V_{BE}} = -\frac{\beta}{R_B + (1 + \beta) R_C}$$

Relation b/w S and S' :-

$$S = \frac{(1 + \beta)}{1 + \beta \left(\frac{R_C}{R_C + R_B} \right)} \quad \text{and} \quad S' = \frac{-\beta}{R_B + (1 + \beta) R_C}$$

$$S = \frac{(1 + \beta) \cdot (R_C + R_B)}{R_C + R_B + \beta R_C} = \frac{(1 + \beta) (R_C + R_B)}{R_B + (1 + \beta) R_C}$$

$$\frac{S}{(1 + \beta) (R_C + R_B)} = \frac{1}{R_B + (1 + \beta) R_C}$$

$$\frac{-S \cdot \beta}{(1 + \beta) (R_C + R_B)} = \frac{-\beta}{\underbrace{R_B + (1 + \beta) R_C}_{S'}}$$

(Multiply with $-\beta$ on both sides)

$$S' = \frac{-S \cdot \beta}{(1 + \beta) (R_C + R_B)}$$

If 'S' is small, S' is still smaller, If we provide stability against I_{CO} variations, we get stability against V_{BE} conditions also.

Stability factor (S^V): -

$$S^V = \frac{\partial I_C}{\partial \beta} \quad \left| \quad I_{CO}, V_{BE} \text{ const.} \right.$$

KVL at Base circuitary.

$$V_{CC} - (I_C + I_B)R_C - I_B R_B - V_{BE} = 0.$$

$$\begin{aligned} V_{CC} - V_{BE} &= (\beta I_B + I_B)R_C + I_B R_B \\ &= I_B [(1 + \beta)R_C + R_B] \end{aligned}$$

$$I_B = \frac{V_{CC} - V_{BE}}{(1 + \beta)R_C + R_B}$$

$$\textcircled{a} \quad I_C = \frac{\beta (V_{CC} - V_{BE})}{(1 + \beta)R_C + R_B} \quad \text{--- (a)}$$

$$\frac{\partial I_C}{\partial \beta} = \frac{[(1 + \beta)R_C + R_B][V_{CC} - V_{BE}] - \beta [V_{CC} - V_{BE}]R_C}{[(1 + \beta)R_C + R_B]^2}$$

$$\frac{\partial I_C}{\partial \beta} = \frac{(V_{CC} - V_{BE}) [(1 + \beta)R_C + R_B - \beta R_C]}{[(1 + \beta)R_C + R_B]^2}$$

$$= \frac{[V_{CC} - V_{BE}] [\cancel{(1 + \beta)}R_B + R_C]}{[(1 + \beta)R_C + R_B]^2}$$

$$= \frac{(V_{CC} - V_{BE})(R_B + R_C)}{[(1 + \beta)R_C + R_B]^2}$$

$$\left[\because \frac{d}{dt} \left(\frac{u}{v} \right) = \frac{v \frac{du}{dt} - u \frac{dv}{dt}}{v^2} \right]$$

$$\boxed{S^V = \frac{\partial I_C}{\partial \beta} = \frac{I_C (R_B + R_C)}{\beta [(1 + \beta)R_C + R_B]}}$$

$\left(\frac{I_C}{\beta} \right)$ from (a)

Relation b/w S and S^4 :-

$$S'' = \frac{I_C (R_B + R_C)}{\beta [(1+\beta) R_C + R_B]}, \quad S' = \frac{-S \cdot \beta}{(1+\beta) (R_C + R_B)}$$

$$S^4 = \frac{I_C}{\beta} \times \frac{S}{1+\beta}$$

$\rightarrow S/(1+\beta)$

$$S = \frac{(1+\beta)}{1+\beta \left(\frac{R_C}{R_C + R_B} \right)}$$

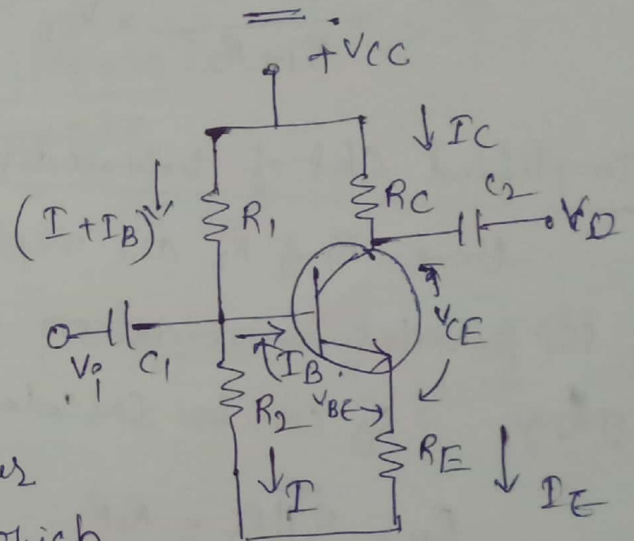
$$S = \frac{(R_C + R_B) (1+\beta)}{\beta (1+\beta) R_C + R_B}$$

If S is small, S^4 is also very small.

Thus if we provide stability against I_C variation, we get stability β variations also.

③ Voltage divider Bias :-

\rightarrow Here biasing is provided by three resistors R_1, R_2, R_E . The resistors R_1, R_2 acts as potential divider giving a fixed voltage to point B which is base.

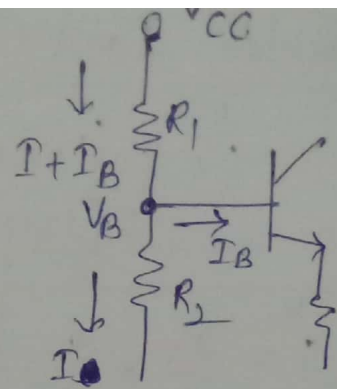


\rightarrow If collector current increases due to change in temperature or change in β , the I_E increases and voltage drop across R_E increases. that reduces voltage drop b/w Base and Emitter V_{BE} . Due to reduction in V_{BE} collector current I_C reduces.

\rightarrow Therefore we can say that negative feedback exists in the emitter bias ckt. This reduction in collector current I_C compensates for original change in I_C .

Base circuit :-

Let consider base circuit



Voltage across R_2 is the base voltage

V_B . Applying voltage divider theorem

$$V_B = \frac{R_2 \cdot I}{R_1(I + I_B) + R_2 \cdot I} \times V_{CC} \quad (\because I > I_B)$$

$$V_B = \frac{R_2}{R_1 + R_2} \times V_{CC}$$

Simplified ckt of voltage divider ckt:

Here R_1 & R_2 are replaced by R_B and V_T , where R_B is the parallel combination of R_1 & R_2 and V_T is the Thevenin's Voltage. R_B can be calculated as

$$R_B = R_1 || R_2 = \frac{R_1 R_2}{R_1 + R_2}$$

Applying KVL at base ckt

$$V_T - I_B R_B - V_{BE} - I_E R_E = 0$$

$$V_T = I_B R_B + V_{BE} + I_E R_E$$

$$V_T = I_B R_B + V_{BE} + (I_B + I_C) R_E \quad (\because I_E = I_B + I_C)$$

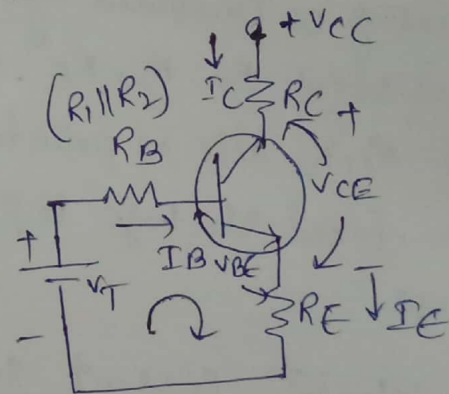
$$V_T = I_B (R_B + R_E) + V_{BE} + I_C R_E$$

Apply 'g' on both sides.

$$V_T = \frac{\partial I_B (R_B + R_E)}{\partial I_C} + V_{BE} + \frac{\partial I_C R_E}{\partial I_C}$$

$$0 = \frac{\partial I_B}{\partial I_C} (R_B + R_E) + 0 + R_E \Rightarrow$$

$$\boxed{\frac{\partial I_B}{\partial I_C} = \frac{-R_E}{R_B + R_E}}$$



Thevenin's equivalent ckt for voltage divider bias

$$\therefore \text{Stability factor } (S) = \frac{(1+\beta)}{1-\beta\left(\frac{\partial I_B}{\partial I_C}\right)}$$

(5)
(19)

$$S = \frac{(1+\beta)}{1+\beta\left(\frac{R_E}{R_E+R_B}\right)}$$

Applying KVL to collector ckt:—

$$V_{CC} - I_C R_C - V_{CE} - I_E R_E = 0$$

$$I_C = \frac{V_{CC} - V_{CE} - I_E R_E}{R_C}$$

$$\therefore S = \frac{(1+\beta)}{1+\beta\left(\frac{R_E}{R_E+R_B}\right)} \Rightarrow \frac{(1+\beta)(R_E+R_B)}{R_E+R_B+\beta R_E}$$

÷ with R_E

$$S = \frac{(1+\beta)\left(1 + \frac{R_B}{R_E}\right)}{(1+\beta) + \left(\frac{R_B}{R_E}\right)}$$

($\because R_B/R_E$ is very small) $\ll 1$
we can neglect

$$S = \frac{(1+\beta)}{(1+\beta)} = 1$$

To become S — small.

R_B — reasonably small.

R_E = not very large.

\therefore If Ratio R_B/R_E is fixed, S increases with β . Therefore stability decreases with increases β .

Not: — Stability factor 'S' for voltage divider bias (or) self bias is less compared to other biasing circuits studied. So this ckt is most commonly preferred.

Stability factor (S) -

$$S = \frac{\partial I_C}{\partial V_{BE}} \quad | \quad I_{C0}, \beta \text{ Const}$$

i.e. Varying of I_C with
when I_{C0}, β are

$$\therefore \text{We know that } I_C = (1+\beta) I_{C0} + \beta I_B \quad \text{--- (1)}$$

$$\text{and } V_T = I_B R_B + V_{BE} + (I_B + I_C) R_E$$

$$V_{BE} = V_T - (R_B + R_E) I_B - R_E I_C \quad \text{--- (2)}$$

$$\text{By Eqn (1) } I_B = \frac{I_C - (1+\beta) I_{C0}}{\beta} \quad \text{--- (3)}$$

Sub (3) in (2)

$$V_{BE} = V_T - (R_B + R_E) \cdot \left[\frac{I_C - (1+\beta) I_{C0}}{\beta} \right] - R_E I_C$$

$$V_{BE} = V_T - \frac{(R_E + R_B) I_C}{\beta} + \frac{(R_B + R_E) (1+\beta) I_{C0}}{\beta} - R_E I_C$$

$$V_{BE} = V_T - \left[\frac{(1+\beta) R_E + R_B}{\beta} \right] I_C + \frac{(R_B + R_E) (1+\beta) I_{C0}}{\beta} \quad \text{--- (4)}$$

Diff (4) with ~~I_C~~ and V_{BE} where I_{C0}, β .

$$1 = 0 - \left[\frac{(1+\beta) R_E + R_B}{\beta} \right] \frac{\partial I_C}{\partial V_{BE}} + 0$$

$$\frac{\partial I_C}{\partial V_{BE}} = \frac{-\beta}{R_B + (1+\beta) R_E}$$

$$S = \frac{-\beta}{R_B + (1+\beta) R_E}$$

Relation b/w S and S'

$$S = \frac{(1+\beta)(1 + R_B/R_E)}{(1+\beta) + R_B/R_E}$$

multiplying both numerator & denominator by R_E we get

$$S = \frac{(1+\beta)(R_E + R_B)}{R_E(1+\beta) + R_B}$$

$$\therefore \frac{S}{(1+\beta)(R_E + R_B)} = \frac{1}{R_B + (1+\beta)R_E} \quad \text{--- (1)}$$

$$\therefore S' = \frac{-\beta}{R_B + (1+\beta)R_E} \Rightarrow \text{--- (2) sub (1) in (2)}$$

$$\boxed{S = \frac{-S' \beta}{(1+\beta)(R_E + R_B)}}$$

we can see lower value of ' S '
" lower is the value of ' S' '
as we reduce the value of ' S '

towards unity, we minimize the change of I_C with respect to both, V_{BE} and I_{CO} .

Stability factor (S'): -

$$S' = \frac{\partial I_C}{\partial \beta} \Big|_{I_{CO}, V_{BE} \text{ Const.}}$$

It is the variation of I_C with β when I_{CO} and V_{BE} are considered const. we recall.

$$V_{BE} = V_T - \frac{(R_B + (1+\beta)R_E)}{\beta} I_C + \left[\frac{(R_E + R_B)(1+\beta)}{\beta} \right] I_{CO}$$

$$V_{BE} = V_T - \frac{[R_B + (1+\beta)R_E] I_C}{\beta} + V'$$

$$\text{Where } V' = \left[\frac{(R_B + R_E)(1+\beta)}{\beta} \right] I_{CO} = (R_B + R_E) I_{CO} \quad (\because \beta \gg 1)$$

$$\therefore I_C = \frac{(V_{BE} + V' + V_T) \beta}{R_B + R_E(1+\beta)}$$

Diff I_C w.r.t β (u/v) where V' independent of β , we get

$$\frac{\partial I_C}{\partial \beta} = \frac{[R_B + R_E(1+\beta)] [-V_{BE} + V' + V_T] - \beta [V_T + V' - V_{BE}] R_E}{[R_B + R_E(1+\beta)]^2}$$

Multiply numerator & denominator by $(1+\beta)$ we get

$$= \frac{(1+\beta)(R_B + R_E)(V_T + V' - V_{BE})}{(1+\beta)[R_B + R_E(1+\beta)][R_B + R_E(1+\beta)]}$$

$$\therefore S = \frac{(1+\beta)(R_E + R_B)}{R_B + (1+\beta)R_E}$$

$$\frac{\partial I_C}{\partial \beta} = \frac{S \cdot (V_T + V' - V_{BE})}{(1+\beta)(R_B + R_E(1+\beta))}$$

$$\div \beta$$

$$\frac{\partial I_C}{\partial \beta} = \frac{\beta(V_T + V' - V_{BE}) \cdot S}{\beta(1+\beta)(R_B + R_E(1+\beta))} \Rightarrow \frac{I_C \cdot S}{\beta(1+\beta)}$$

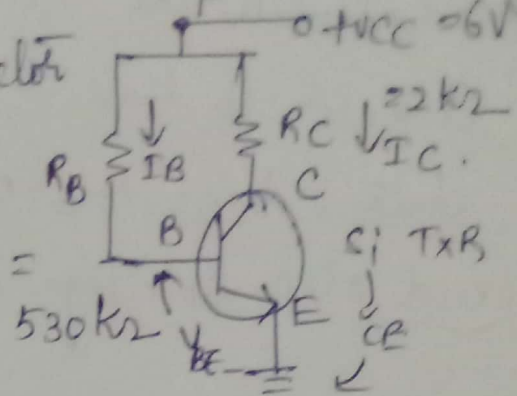
$$\therefore I_C = \frac{\beta(V_T + V' - V_{BE})}{R_B + R_E(1+\beta)}$$

$$S = \frac{\partial I_C}{\partial \beta} = \frac{I_C \cdot S}{\beta(1+\beta)}$$

if $S \downarrow$, S' also \downarrow .

Figure shows fixed bias ckt. Determine with $\beta = 100$
 i) operating point (ii) the stability factor

Given: - $R_B = 530k\Omega$ $\beta = 100$
 $R_C = 2k\Omega$
 $V_{CC} = 6V$; For Si T_{XR}
 $V_{BE} = 0.7V$.



Cal: - (V_{CEQ}, I_{CQ}) : Base ckt

$$V_{CC} - I_B R_B - V_{BE} = 0 \Rightarrow 6 - I_B \cdot (530k\Omega) - 0.7 = 0$$

$$I_B = \frac{6 - 0.7}{530} \times 10^{-3} = \frac{5.3}{530} \text{ mA} \Rightarrow \frac{5.3}{53000} \times 10^{-3} = 10^{-5} = 10\mu\text{A}$$

$$I_B = 10\mu\text{A}$$

$$I_C = \beta I_B \Rightarrow 100 \times 10 \times 10^{-6} = 1\text{mA}$$

collector ckt $V_{CC} - I_C R_C - V_{CE} = 0$

$$6 - 1 \times 10^{-3} \times 2 \times 10^3 - V_{CE} = 0$$

$$V_{CE} = 6 - 2 = 4V$$

operating

point $(V_{CEQ}, I_{CQ}) = (4V, 1\text{mA})$

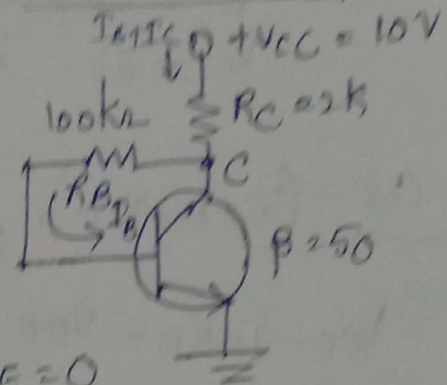
Stability factor (S) = $1 + \beta = 101$. // (For fixed bias)

⊗

Note: - The small value of stability factor indicates good bias stability, whereas large value of stability factor indicates poor bias stability. Ideal value of stability factor is zero. Since β is large quantity, this is a very poor bias stable ckt.

② Calculate the quiescent Current and Voltage of Collector to base bias arrangement using the following data. $V_{CC} = 10V$, $R_B = 100k\Omega$, $R_C = 2k\Omega$, $\beta = 50$,
 (and also specify a value of R_B so that $V_{CE} = 7V$)

I_{CQ} , $V_{CEQ} = ?$



By applying KVL to Base circuit

$$V_{CC} - (I_B + I_C)R_C - I_B R_B - V_{BE} = 0$$

$$V_{CC} - (I_B + \beta I_B)R_C - I_B R_B - V_{BE} = 0$$

$$V_{CC} - I_B (R_B + (1 + \beta)R_C) - V_{BE} = 0$$

$$I_B = \frac{V_{CC} - V_{BE}}{R_B + (1 + \beta)R_C} = \frac{10 - 0.7}{100k + (1 + 50) \cdot 2 \times 10^3}$$

$$I_B = 46 \mu A$$

$$\therefore I_C = \beta \cdot I_B$$

$$I_C = 50 \times 10^{-6} \times 46$$

$$I_C = 2.3 \text{ mA} //$$

∴ By applying KVL to Collector circuit

$$V_{CC} - (I_B + I_C)R_C - V_{CE} = 0$$

$$V_{CE} = V_{CC} - (I_B + I_C) \cdot R_C$$

$$V_{CE} = 10 - \left(\frac{46 \mu A}{1000} + 2.3 \text{ mA} \right) (2 \times 10^3)$$

$$V_{CE} = 5.308 \text{ V}$$

$$\underline{\underline{(V_{CEQ}, I_{CQ}) = (5.308 \text{ V}, 2.3 \text{ mA})}}$$

① Given $V_{CE} = 7V$; $R_B = ?$

$$V_{CC} - (I_C + I_B) R_C - V_{CE} = 0$$

$$\Rightarrow (I_C + I_B) R_C = V_{CC} - V_{CE}$$

$$(\beta I_B + I_B) R_C = V_{CC} - V_{CE}$$

Solve

$$I_B R_C = \frac{V_{CC} - V_{CE}}{(1 + \beta) R_C} = \frac{10 - 7}{(1 + 50) \times 2 \times 10^3}$$

$$I_B = \frac{3}{51 \times 2 \times 10^3} = 29.41 \mu A$$

② Emitter stabilized bias :-

By Applying KVL to Base ckt

$$V_{CC} - I_B R_B - V_{BE} - I_E R_E = 0$$

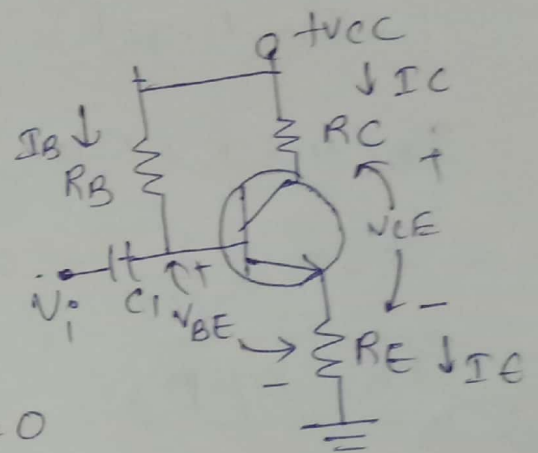
$$V_{CC} - I_B R_B - V_{BE} - (I_B + I_C) R_E = 0$$

$$V_{CC} - V_{BE} - I_C R_E = I_B (R_B + R_E)$$

$$I_B = \frac{V_{CC} - V_{BE} - I_C R_E}{R_B + R_E}$$

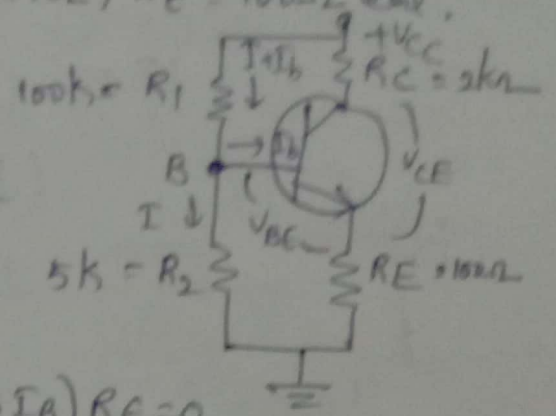
$$\frac{\partial I_B}{\partial I_C} = \frac{-R_E}{R_B + R_E} \quad , \quad S = \frac{(1 + \beta)}{1 + \beta \left(\frac{\partial I_B}{\partial I_C} \right)}$$

$$\therefore S = \frac{(1 + \beta)}{1 + \beta \left(\frac{R_E}{R_B + R_E} \right)}$$



③ Circuit shown in fig. $V_{CC} = 20V$, $R_C = 2k\Omega$, $\beta = 50$.

$V_{BE} \text{ sat} = 0.2V$, $R_1 = 100k\Omega$, $R_2 = 5k\Omega$, $R_E = 100\Omega$ Cal.
 I_B , V_{CE} , I_C .



:- By Applying KVL at baseckt

$$V_T - I_B \cdot R_B - V_{BE} - I_E \cdot R_E = 0$$

$$V_T - I_B (R_1 || R_2) - V_{BE} - (I_B + \beta I_B) R_E = 0$$

$$1 - I_B (4.76k) - 0.2 - I_B (1 + 50)(100) = 0$$

$$R_1 || R_2 = \frac{100 \times 5 \times 10^3}{(105)k} = \frac{500k}{105}$$

$$I_B (4.76k + 5,100) = 1 - 0.2$$

$$I_B = \frac{0.8}{9860} = 0.0000811$$

$$R_B = 4.76k \Omega$$

$$V_T = \frac{V_{CC} \times R_2}{R_1 + R_2}$$

$$V_T = \frac{20 \times 5k}{(105)k}$$

$$V_T = \frac{100}{105}$$

$$V_T \approx 1V$$

$$I_B = 81 \mu A$$

$$I_C = \beta \times I_B = 50 \times 81 \mu A = 4050 \mu A$$

$$I_C = 4.050 mA$$

$$V_{CC} - I_C R_C - V_{CE} - I_E R_E = 0$$

$$20 - 4.050 mA - V_{CE} - (81 \mu A + 4.050 mA) = 0$$

$$V_{CE} = 20 - 4.050 mA - (0.081 mA + 4.050 mA)$$

$$= 20 - 4.050 mA - 4.131 mA$$

$$V_{CE} = 20 - 0.00818$$

$$V_{CE} = 19.991 V$$