

Amplifiers -

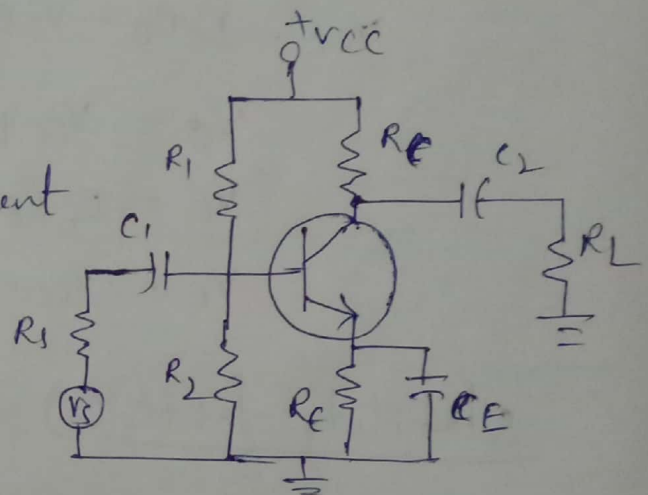
- Amplifiers are used to increase the signal level, i.e., amplifiers are used to get the larger output signal from the smaller input signal.
- Assume that, input with the sinusoidal input the output is same sinusoidal with the same frequency.
- To make transistor works as an amplifier, it should be biased and operate in active region.
 - i.e. base-emitter junction is forward biased.
 - base-collector junction is reverse biased.

CE Amplifier Circuit :-

→ i.e. CE Amplifier with different circuit components

i) Biasing Circuit :-

R_1, R_2, R_E are the forms the voltage divider biasing circuit for CE amplifier, It sets proper operating point for the CE amplifier.

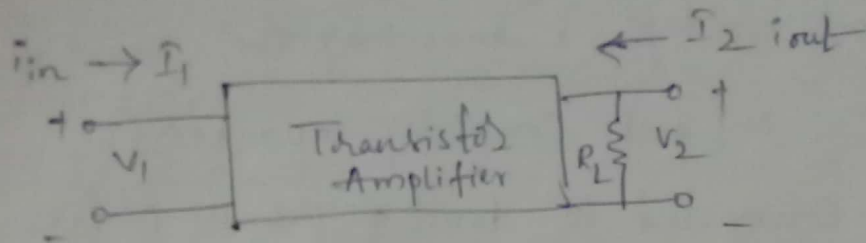


ii) Input Capacitance C_1 :-

C_1 is connected to base of the T.R. It blocks the d.c. component present in the ac signal, and passes only a.c. signal for amplification. Because biasing conditions maintaining constant.

Two port network:-

Two port network is a linear active device, with 4 terminals. two terminals are used for input port other two terminals are used for output called o/p port



TR Amplifiers. $V_2 = -I_2 R_L$

$$V_1 = f(I_1, V_2)$$

$$I_2 = f(I_1, V_2)$$

Here
 I_1 = i/p current to the Amplifier
 I_2 = o/p current of the Amp's
 V_1 = i/p voltage to the Amp's
 V_2 = o/p voltage of the Amp's

This can be written as,

(Hybrid parameters).

$$V_1 = h_{11} I_1 + h_{12} V_2$$

$$I_2 = h_{21} I_1 + h_{22} V_2$$

\Rightarrow make o/p is short ckt $V_2 = 0$.

$$V_1 = h_{11} I_1 + 0$$

$$h_{11} = \left. \frac{V_1}{I_1} \right|_{V_2=0} = \text{input impedance. } (h_i) z_i$$

$$I_2 = h_{21} I_1 + 0$$

$$h_{21} = \frac{I_2}{I_1} = (\text{Current gain } A_i) (h_f)$$

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 ⇒ make i/p is open circuit i.e. $I_1 = 0$:-

$$V_1 = 0 + h_{12} V_2$$

$$h_{12} = \frac{V_1}{V_2} \text{ (Reverse voltage gain } A_r) \text{ (hr.)}$$

$$I_2 = 0 + h_{22} V_2$$

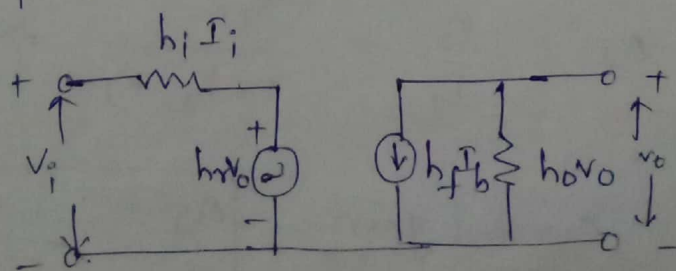
$$h_{22} = \frac{I_2}{V_2} \text{ (o/p Admittance } Y_o) = h_o$$

$$\begin{aligned} \therefore V_i &= h_i I_i + h_r V_o \\ I_o &= h_f I_i + h_o V_o \end{aligned}$$

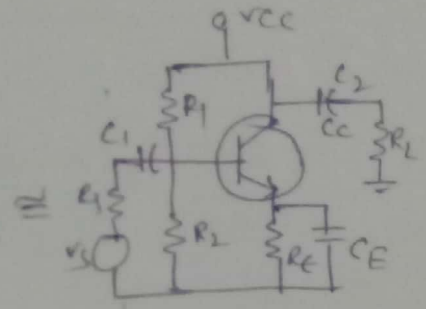
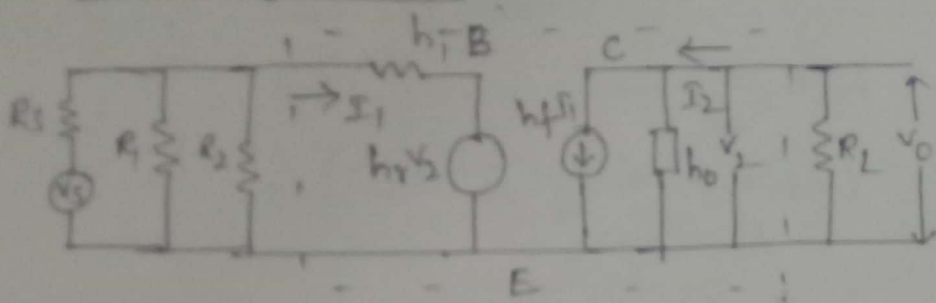
	CE	CB	CC
$h_{11} = h_i$	h_{ie}	h_{ib}	h_{ic}
$h_{12} = h_r$	h_{re}	h_{rb}	h_{rc}
$h_{21} = h_f$	h_{fe}	h_{fb}	h_{fc}
$h_{22} = h_o$	h_{oe}	h_{ob}	h_{oc}

★ Transistor Hybrid model:-

To analyze the transistor amplifier circuit and calculate the input impedance, output impedance, current gain, voltage gain, it is necessary to replace the circuit with its equivalent model with the help of these two equations.



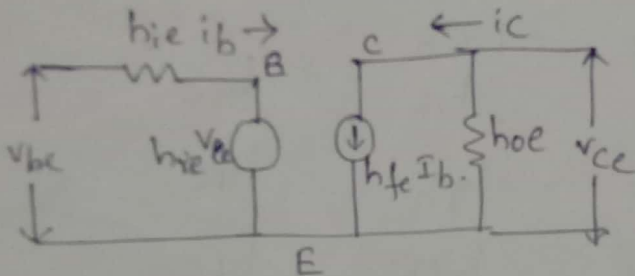
① CE Configuration :-



\therefore Current gain $A_V = \frac{I_0}{I_i} = \frac{-I_2}{I_1}$

$V_2 = -I_2 R_L$

Why -ve sign :- from the circuit, the current direction is opposite when compared to input current I_1 .



$V_1 = h_{11} I_1 + h_{12} V_2$ — (1)

$I_2 = h_{21} I_1 + h_{22} V_2$ — (2)

$\therefore (V_2 = -I_2 R_L = V_0)$

$A_I = \frac{-I_2}{I_1}$

Ratio of output current to input current

① Current gain :-

from Eq (2)

$I_2 = h_{21} I_1 + h_{22} V_2$

$I_2 = h_{21} I_1 - h_{22} I_2 R_L$

$I_2 (1 + h_{22} R_L) = h_{21} I_1$

$A_I = \frac{I_2}{I_1} = \frac{h_{21}}{1 + h_{22} R_L} \Rightarrow \frac{-h_{21}}{1 + h_{22} R_L}$

Current gain

$A_I = \frac{-h_{fe}}{1 + h_o R_L}$

② Input impedance:- (Z_i)

$$Z_i = \frac{V_1}{I_1} \quad h_{11} I_1 + h_{12} V_2 = V_1 \quad \text{--- (1)}$$

divide Eq (1) with I_1 .

$$\frac{V_1}{I_1} = h_{11} + h_{12} \frac{V_2}{I_1}$$

$$Z_i = h_{11} + h_{12} \left(\frac{-I_2 R_L}{I_1} \right)$$

$$Z_i = h_{11} + h_{12} \left(\frac{-I_2}{I_1} \right) R_L$$

$$Z_i = h_{11} + h_{12} A_i R_L \quad \left[\because A_i = \frac{-I_2}{I_1} \right]$$

$$\boxed{Z_i = h_i + h_{12} A_i R_L}$$

③ voltage gain:- (A_v)

o/p voltage V_2 to the i/p voltage V_1

$$A_v = \frac{V_2}{V_1}$$

$$V_2 = -I_2 R_L$$

$$V_2 = A_i I_1 R_L$$

$$A_v = \frac{A_i I_1 R_L}{V_1}$$

$$\boxed{A_v = \frac{A_i R_L}{Z_i}}$$

$$\left[\because A_i = \frac{-I_2}{I_1} \right]$$

$$-I_2 = A_i I_1$$

$$\left[\because Z_i = \frac{V_1}{I_1} \Rightarrow \frac{1}{Z_i} = \frac{I_1}{V_1} \right]$$

④ Admittance Output Y_o :-

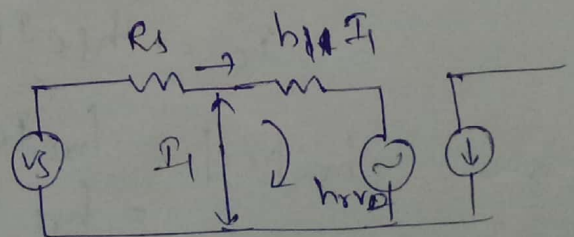
$$Y_o = \frac{I_2}{V_2}$$

Apply KVL

$$R_s I_1 + h_{11} I_1 + h_{12} V_o = 0$$

$$I_1 (R_s + h_{11}) = -h_{12} V_o$$

$$\frac{I_1}{V_2} = \frac{-h_{12}}{R_s + h_{11}}$$



$$-I_2 = h_{21} I_1 + h_{22} V_2$$

Divide $\div V_2$

$$\frac{-I_2}{V_2} = h_{21} \frac{I_1}{V_2} + h_{22}$$

$$Y_0 = h_{21} \left(\frac{-h_r}{R_s + h_{11}} \right) + h_{22}$$

$$Y_0 = h_o - \frac{h_r h_f}{R_s + h_i}$$

* Analysis of Transistor Amplifier using simplified CE

① Hybrid model:-

→ we studied exact values of A_v, A_i, R_i, Y_o of TXR.
But in practical cases, we use approximate values of A_i, A_v, R_i, Y_o of TXR. i.e done by following these conditions.

→ $\frac{1}{h_{oe}}$ is parallel with R_L & R_C .

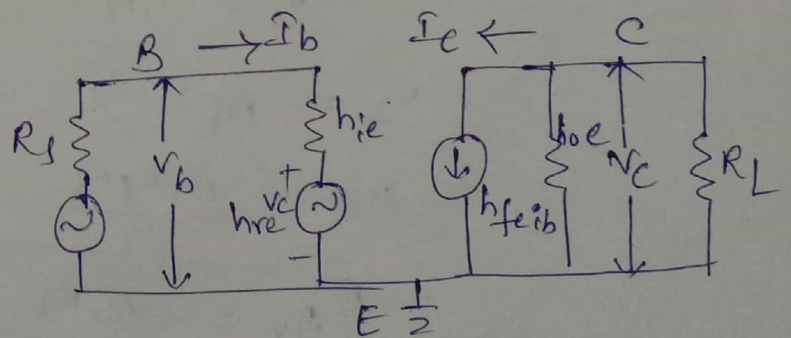
→ If $\frac{1}{h_{oe}} \gg R_L \parallel R_C$,

h_{oe} is neglected, If we neglect h_{oe} , I_C is given by

$$I_C = h_{fe} I_b$$

$$\begin{aligned} h_{re}(V_{ce}) &= h_{re} I_C (R_C \parallel R_L) \\ &= h_{re} h_{fe} I_b (R_C \parallel R_L) \end{aligned}$$

$\therefore h_{re} h_{fe} \approx 0.001$, This voltage maybe neglected.



Current gain (A_i) = $-\frac{h_{fe}}{1+h_{oe}R_L}$

By neglecting h_{oe} we have ($\because h_{oe}R_L < 0.1$)

$A_i = -h_{fe}$

Input impedance (Z_i) = $h_{ie} + h_{re}A_iR_L$

By neglecting h_{re}

$Z_i = h_{ie}$

Voltage gain (A_v):-

$A_v = \frac{A_i R_L}{Z_i} = \frac{-h_{fe} R_L}{h_{ie}}$

Output Admittance: Y_o

$Y_o = h_{oe} - \frac{h_{fe} h_{re}}{h_{ie} + R_s}$

By neglecting h_{oe} , h_{re} .

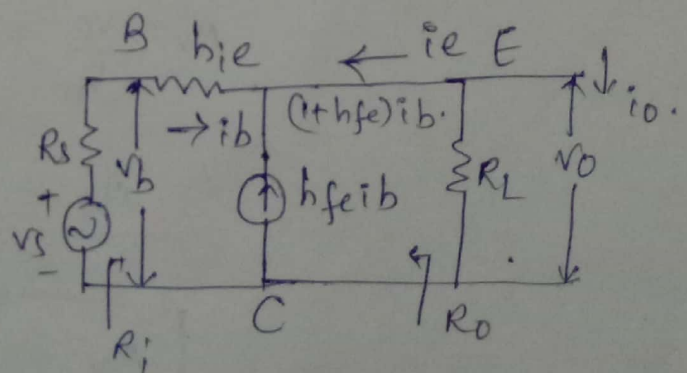
$Y_o = 0$
 $R_o = \frac{1}{Y_o} = \frac{1}{0} = \infty$

② Analysis of simplified hybrid model of CC Amplifier:-

Amplifier:-

In CE amplifier, input is applied to base and output is collected

from the collector and emitter is common to input and output.



But in the case of CC Amplifier, input is connected to base and output is collected from emitter and collector is connected to both input and output.

→ In the figure h_{fe} direction is opposite to CE model because h_{fe} always points towards emitter.

$$\underline{\text{Current gain } (A_i)} = \frac{I_o}{I_b} = \frac{-I_e}{I_b} = \frac{(1+h_{fe})I_b}{I_b}$$

$$\boxed{A_i = (1+h_{fe})}$$

$$\underline{\text{Input Resistance } (R_i)} = \frac{V_b}{I_b}$$

Applying KVL we have

$$V_b - I_b h_{ie} - I_o R_L = 0$$

$$V_b = I_b h_{ie} + I_o R_L$$

$$\frac{V_b}{I_b} = h_{ie} + \frac{I_o}{I_b} R_L$$

$$\boxed{R_i = \frac{V_b}{I_b} = h_{ie} + (1+h_{fe}) R_L}$$

Voltage gain :- (A_v)

$$A_v = \frac{V_o}{V_b} = \frac{I_o R_L}{I_b R_i} = \frac{I_o}{I_b} \cdot \frac{R_L}{R_i}$$

$$A_v = \frac{A_i R_L}{R_i}$$

$$\boxed{A_v = \frac{(1+h_{fe}) R_L}{h_{ie} + (1+h_{fe}) R_L}}$$

output resistance (R_o):-

It is the ratio of output voltage (V_o) to input current I_e with V_s = 0.

$$R_o = \frac{V_o}{I_e} \Big|_{V_s=0}$$

Apply KVL

$$V_s - I_b R_s - I_b h_{ie} - V_o = 0$$

$$V_o = V_s - I_b R_s - I_b h_{ie}$$

$$V_o = V_s - I_b (R_s + h_{ie}) \quad (\because V_s = 0)$$

$$V_o = 0 - I_b (R_s + h_{ie})$$

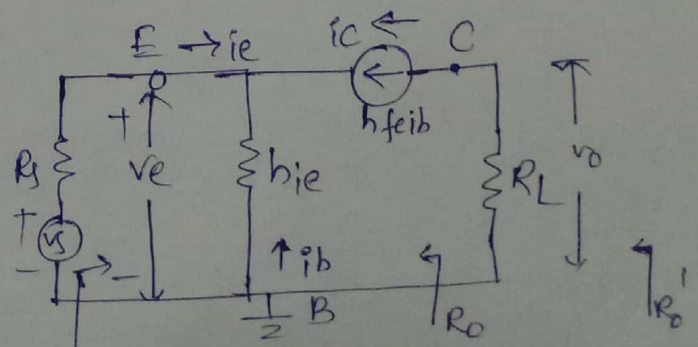
$$I_e = -(1 + h_{fe}) I_b$$

$$\frac{V_o}{I_e} = \frac{-I_b (R_s + h_{ie})}{-(1 + h_{fe}) I_b} = \frac{R_s + h_{ie}}{1 + h_{fe}}$$

$$R_o = \frac{V_o}{I_e} = \frac{R_s + h_{ie}}{1 + h_{fe}}$$

⑧ Analysis of CB Amplifier using simplified Hybrid model :-

In CB model, we will give input to emitter and will take output from the collector, and will give Base to Common to both (p_n, p_n)



Current gain (A_i): - Ratio of output to input currents 17

$$A_i = \frac{I_o}{I_e} = \frac{-I_c}{I_e} = \frac{-h_{fe} I_b}{I_e}$$

$$A_i = \frac{-h_{fe} I_b}{-(1+h_{fe}) I_b}$$

$$\begin{aligned} (\because I_c &= h_{fe} I_b \\ I_e &= -(1+h_{fe}) I_b) \end{aligned}$$

$$\boxed{A_i = \frac{h_{fe}}{1+h_{fe}}}$$

Input Resistance (R_i): - Ratio of i/p voltage to i/p current

$$R_i = \frac{V_e}{I_e} = \frac{-h_{ie} i_b}{-(1+h_{fe}) I_b}$$

$$\boxed{R_i = \frac{h_{ie}}{1+h_{fe}}}$$

voltage gain (A_v): - Ratio of output to input voltages

$$A_v = \frac{V_o}{V_e} = \frac{I_o R_L}{I_e R_i} = \frac{A_i R_L}{R_i}$$

$$= \frac{\left(\frac{h_{fe}}{1+h_{fe}}\right) R_L}{\left(\frac{h_{ie}}{1+h_{fe}}\right)} = \frac{h_{fe} R_L}{h_{ie}}$$

$$\boxed{A_v = \frac{h_{fe} \cdot R_L}{h_{ie}}}$$

output resistance (R_o): - Ratio of output voltage to output current at $V_s = 0$.

$$R_o = \frac{V_o}{I_c} \Big|_{V_s=0}$$

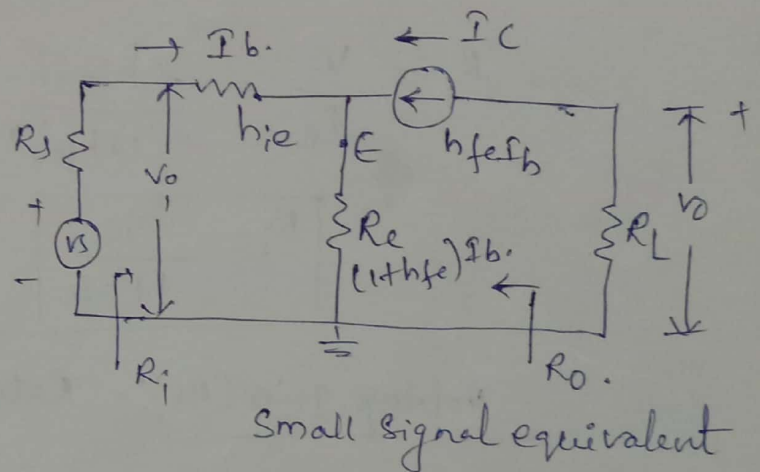
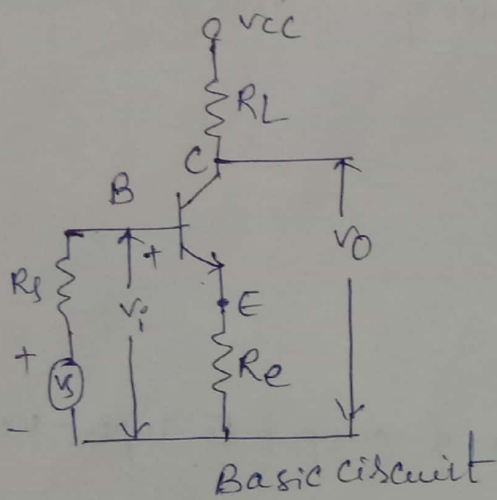
When $i_b = 0$; $i_c = 0$. Then $V_s = 0$; $\boxed{R_o = \infty}$

* CE Amplifiers with Emitter resistor R_e :- in emitter

Circuit :-

→ we know that resistors parameters vary with temperature, this then to result in variation and deduction in voltage gains of the transistors. so

→ The need for stabilization and of voltage gain, it is important to place a proper emitter resistor R_e , in the CE circuit.



A_i, A_v, R_i, R_o

Current gain (A_i)

$$A_i = \frac{I_C}{I_B} = \frac{-h_{fe} I_B}{I_B} = -h_{fe} \checkmark$$

$$R_i = \frac{V_i}{I_B} = \text{Apply KVL}$$

$$V_i = h_{ie} i_b + R_e (1+h_{fe}) i_b$$

$$R_i = \frac{V_i}{i_b} = \frac{h_{ie} i_b + R_e (1+h_{fe}) i_b}{i_b}$$

$$R_i = h_{ie} + R_e (1+h_{fe}) \checkmark$$

$$A_v = \frac{A_i R_L}{R_i} = \frac{-h_{fe} R_L}{h_{ie} + (1+h_{fe}) R_e} \checkmark$$

$$R_o = \frac{V_o}{I_o} = \frac{V_o}{I_c} = \frac{V_o}{0} = \infty \checkmark$$

$$R_o' = R_o \parallel R_L = \infty \parallel R_L$$

$$R_o' = R_L$$

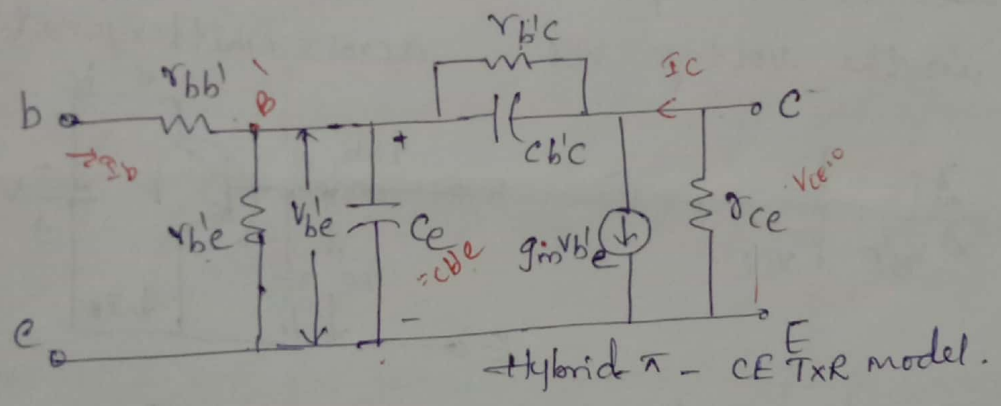
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Expressions for A_i, A_v, R_i, R_o for using Approximate model:-

Quantity	CE	CB	CC	CE with R_e
A_i	$-h_{fe}$	$-h_{fb} = \frac{h_{fe}}{1+h_{fb}}$	$1+h_{fe}$	$-h_{fe}$
R_i	h_{ie}	$h_{ib} = \frac{h_{ie}}{1+h_{fe}}$	$h_{ie} + (1+h_{fe})R_L$	$h_{ie} + (1+h_{fe})R_L$
A_v	$\frac{-h_{fe} \cdot R_L}{h_{ie}}$	$\frac{h_{ie} \cdot R_L}{h_{ie}}$	$1 - \frac{h_{ie}}{R_i}$	$\frac{-h_{fe} \cdot R_L}{R_i}$
R_o	∞	∞	$\frac{h_{ie} + R_s}{1+h_{fe}}$	∞
R_o'	R_L	R_L	$R_o \parallel R_L$	R_L

- At high frequencies h-parameters become Complex and not Constant at frequencies. Therefore it is necessary to analyze transistor at each and every frequency, which is impractical.
- Due to above reason Hybrid- π model used for High frequency analysis.
- These model provides accuracy in The High frequency Analysis.

1. Hybrid- π CE Transistor model:-



At room temperature

parameter	Meaning	Values.
g_m	Mutual Conductance of T_xR.	50 mA/V
$r_{bb'}$	base spreading Resistance.	100 Ω
$r_{be'}$ (or) $r_{b'e}$	Resistance between B' and E	1 K Ω
$g_{b'e}$	Conductance b/w B' and E	1 m mho.
$r_{b'c}$ (or) $r_{b'c}$	Resistance between inverse biased of PN junction b/w Base and collector	4 M Ω
$g_{b'c}$	Conductance of Inverse biased PN junction between Base and collector	0.25 x 10 ⁻⁶ mho

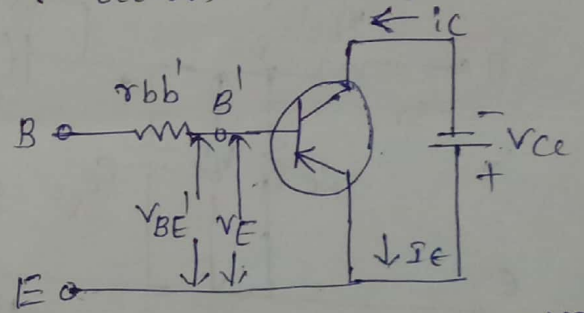
r_{ce}	output resistance b/w C and E	80K Ω
g_{ce}	conductance b/w C and E	$12.5 \times 10^{-6} \Omega^{-1}$
C_e	Junction Capacitance b/w B and E	100pF
C_c	Junction Capacitance b/w B and C.	3pF.

(i) Transconductance (g_m)

Let Take p-n-p T_{XR} of CE Configuration Amplifier with +V_{CC} biased.

transconductance is nothing but ratio ^{change in} Collector Current to changes in the voltage V_{BE'} across emitter junction.

$$g_m = \left. \frac{\Delta I_C}{\Delta V_{BE'}} \right|_{V_{CE}} \quad \text{--- (1)}$$



Collector current at active region.

$$I_C = I_{C0} + \alpha I_E$$

$$\Delta I_C = \alpha \Delta I_E \quad (\text{I}_{C0} = \text{const})$$

$$g_m = \alpha \cdot \frac{\Delta I_E}{\Delta V_{BE'}}$$

$$g_m = \alpha \cdot \frac{\Delta I_E}{\Delta V_E}$$

Emitter diode resistance, r_e is given as

$$\therefore r_e = \frac{\Delta V_E}{\Delta I_E}$$

$$\boxed{g_m = \frac{\alpha}{r_e}}$$

n/p/n
 $I_C = \alpha I_E + I_{C0}$
 $\frac{\Delta I_C}{\Delta I_E} = \alpha$
 $\Delta I_C = \alpha \Delta I_E$
 $g_m = \frac{\Delta I_C}{\Delta V_{BE'}}$
 $g_m = \alpha \cdot \frac{\Delta I_E}{\Delta V_{BE'}}$
 $g_m = \frac{\alpha \cdot I_C}{\Delta V_E}$
 $g_m = \frac{\alpha}{r_e}$
 $(r_e = \frac{V_T}{I_E})$

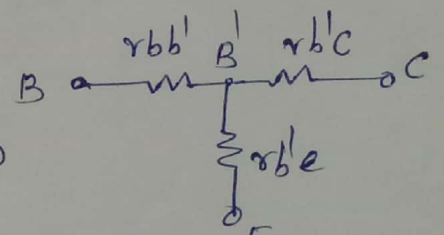
$r_{bb'}$:- Base spreading resistance.

A bulk resistance between External ^{Base} node and internal (node) base B' .

$r_{b'e}$:- A resistance b/w base node B' to emitter.
($h_{ie} = r_{bb'} + r_{b'e}$.)

$r_{b'c}$:- A resistance b/w b' and collector node terminal.
This feedback effect b/w output and input is taken into account by connecting $g_{b'c}$ (or) $r_{b'c}$ b/w b' and c .

$c_{b'e}$:- Capacitive effect due to F.B
PN junction exhibits. called diffusion
Capacitance.



→ F.B base-emitter junction of the T.R is represented by $c_{b'e}$.
(Virtual Base)

$c_{b'c}$:- A R.B collector base junction of the T.R is represented by $c_{b'c}$.

A R.B PN junction diode exhibits capacitive effect is called transition capacitance.

g_m :- (trans conductance) : Due to small change in collector current due to base emitter voltage ($V_{b'e}$).

$$g_m = \left. \frac{\Delta I_c}{\Delta V_{b'e}} \right|_{V_{CE} = \text{const}} \Rightarrow \left. \frac{\partial I_c}{\partial V_{b'e}} \right|_{V_{CE} = \text{const}}$$

$\therefore v_e = \frac{V_T}{I_E}$ $V_T =$ "voltage equivalent of temperature"

$V_T = \frac{KT}{q}$

$g_m = \frac{\alpha}{r_e} = \frac{\alpha I_E}{V_T}$

$(I_C = I_{C0} - \alpha I_E)$
 $\alpha I_E = I_{C0} - I_C$

$g_m = \frac{I_{C0} - I_C}{V_T}$

\therefore for pnp TXR I_C is negative,
 for npn TXR I_C is positive.

$g_m = \frac{d}{V_T / I_C}$

$g_m = \frac{d \cdot I_C}{V_T}$

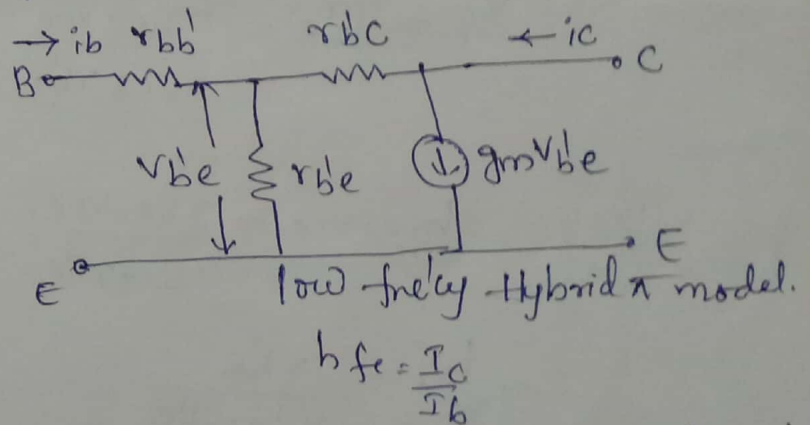
$\therefore g_m = \frac{I_C - I_{C0}}{V_T}$ ($\because I_C \gg I_{C0}$)

$g_m = \frac{I_C}{V_T}$

$g_m = \frac{I_C}{V_T}$

(ii) Input conductance:- $g_{b'e}$

Compare to h-parameter



$i_c = g_m v_{b'e}$

$i_c = g_m \cdot i_b \cdot r_{b'e}$

$\frac{i_c}{i_b} = g_m \cdot r_{b'e}$

$h_{fe} = g_m \cdot r_{b'e}$

$v_{b'e} = I_b \cdot (r_{b'e} \parallel r_{b'c})$
 $v_{b'e} = I_b \cdot r_{b'e}$

(or) $r_{b'e} = \frac{h_{fe}}{g_m}$ (or) $g_{b'e} = \frac{g_m}{h_{fe}}$ $\therefore g_m = \frac{I_C}{V_T}$

$r_{b'e} = \frac{h_{fe} \cdot V_T}{I_C}$

$r_{b'e} \propto \text{temp.}; r_{b'e} = \alpha \frac{1}{I_C}$

(ii) The feedback conductance g_{bc} :

Consider a parameter CE model, input is open circuit, $I_B = 0$

Hybrid π model of CE

$$V_i = h_{re} \cdot V_{ce} \quad \text{--- (1)}$$

When $I_B = 0$

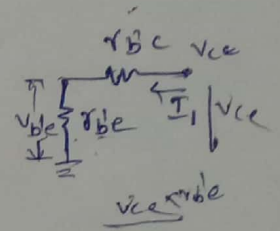
$$V_{ce} = I_1 (r_{be}' + r_{bc}')$$

$$I_1 = \frac{V_{ce}}{r_{be}' + r_{bc}'}$$

From ckt, voltage between B' and E is V_{be}' .

$$V_{be}' = I_1 \cdot r_{be}'$$

$$V_{be}' = \frac{V_{ce}}{r_{be}' + r_{bc}'} \cdot r_{be}'$$



$$V_i = V_{be}' = \frac{r_{be}' \cdot V_{ce}}{r_{bc}' + r_{be}'} \quad | \quad I_B = 0 \quad \text{--- (2)}$$

$$\frac{h_{re} \cdot V_{ce}}{1} = \frac{r_{be}' \cdot V_{ce}}{r_{bc}' + r_{be}'}$$

$$h_{re} = \frac{r_{be}'}{r_{bc}' + r_{be}'}$$

$$r_{be}' = h_{re} r_{bc}' + h_{re} r_{be}'$$

$$(1 - h_{re}) r_{be}' = h_{re} r_{bc}'$$

$$r_{bc}' = \frac{(1 - h_{re}) r_{be}'}{h_{re}}$$

($\because (1 - h_{re}) \approx 1$)

$$\boxed{r_{bc}' = \frac{r_{be}'}{h_{re}}} \quad \checkmark$$

$$\boxed{g_{bc}' = \frac{h_{re}}{r_{be}'}}$$

(iv) Base spreading Resistance ($r_{bb'}$):-

Consider h-parameter model for CE, The input resistance with o/p is short circuit ($v_{ce} \rightarrow 0$) is h_{ie} .

Hybrid π model with o/p short ckt is $r_{bb'} + r_{b'e}$.

$$h_{ie} = r_{bb'} + r_{b'e}$$

$$\boxed{r_{bb'} = h_{ie} - r_{b'e}}$$

$$r_{bb'} + \frac{r_{b'e} || r_{b'c}}{(1 + \beta)}$$

$$r_{bb'} + r_{b'e}$$

(v) output resistance (g_{ce}):-

h-parameter o/p resistance

$$h_{oe} = \frac{I_c}{V_{ce}}$$

for now, hybrid π model CE configuration, Apply KCL

$$\boxed{I_c = \frac{V_{ce}}{r_{ce}} + g_m v_{b'e} + (I_1)}$$

$$(I_1 = \frac{V_{ce}}{r_{b'c} + r_{b'e}})$$

$$I_c = \frac{V_{ce}}{r_{ce}} + g_m v_{b'e} + \frac{V_{ce}}{r_{b'c} + r_{b'e}}$$

$$I_c = \frac{V_{ce}}{r_{ce}} + g_m \left[\frac{r_{b'e} \cdot V_{ce}}{r_{b'c} + r_{b'e}} \right] + \left[\frac{V_{ce}}{r_{b'c} + r_{b'e}} \right] \quad v_{b'e} = \left(\frac{r_{b'e} \cdot V_{ce}}{r_{b'c} + r_{b'e}} \right)$$

$\div V_{ce}$

$$\frac{I_c}{V_{ce}} = \frac{1}{r_{ce}} + \frac{r_{b'e} \cdot g_m}{r_{b'c} + r_{b'e}} + \frac{1}{r_{b'c} + r_{b'e}}$$

$$(\because h_{fe} = g_m r_{b'e})$$

$$\frac{I_c}{V_{ce}} = h_{oe} = \frac{1}{r_{ce}} + \frac{(h_{fe} + 1)}{r_{b'c} + r_{b'e}}$$

$$h_{oe} = \frac{1}{r_{ce}} + \frac{h_{fe}}{r_{b'c} + r_{b'e}} \quad (\because h_{fe} \gg 1)$$

$$h_{oe} = \frac{1}{r_{ce}} + \frac{h_{fe} \cdot 1}{r_{b'c}} \quad (\because r_{b'c} \gg r_{b'e})$$

$$h_{oe} = g_{ce} + h_{fe} \cdot g_{b'c}$$

$$g_{ce} = h_{oe} - g_{b'c} \cdot h_{fe}$$

$$\frac{1}{r_{ce}} = g_{ce} = h_{oe} - g_{b'c} \cdot h_{fe}$$

Hybrid π -parameters

$$1. \quad g_m = \frac{I_C}{V_T}$$

$$2. \quad r_{b'e} = \frac{h_{fe}}{g_m}$$

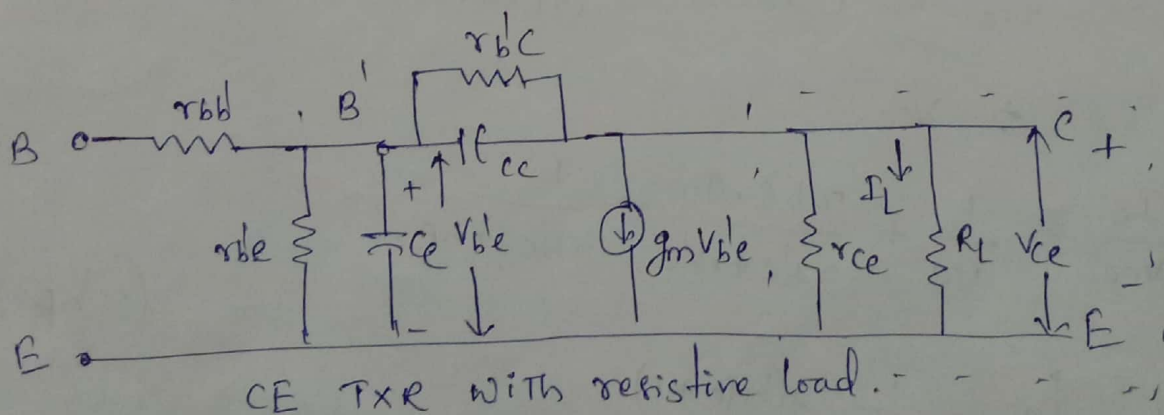
$$3. \quad r_{bb'} = h_{ie} - r_{b'e}$$

$$4. \quad r_{b'c} = \frac{r_{b'e}}{h_{re}}$$

$$5. \quad g_{ce} = \frac{1}{r_{ce}} = h_{oe} - g_{b'c} \cdot h_{fe}$$

② CE short circuit current gain: - (A_i)

CE ^{TR} Amplifier, with load resistance R_L .



When o/p is short circuited, $R_L = 0$

$$\therefore r_{ce} \approx 0$$

$r_{b'e}$, C_e and $C_{b'c}$ appear in parallel.