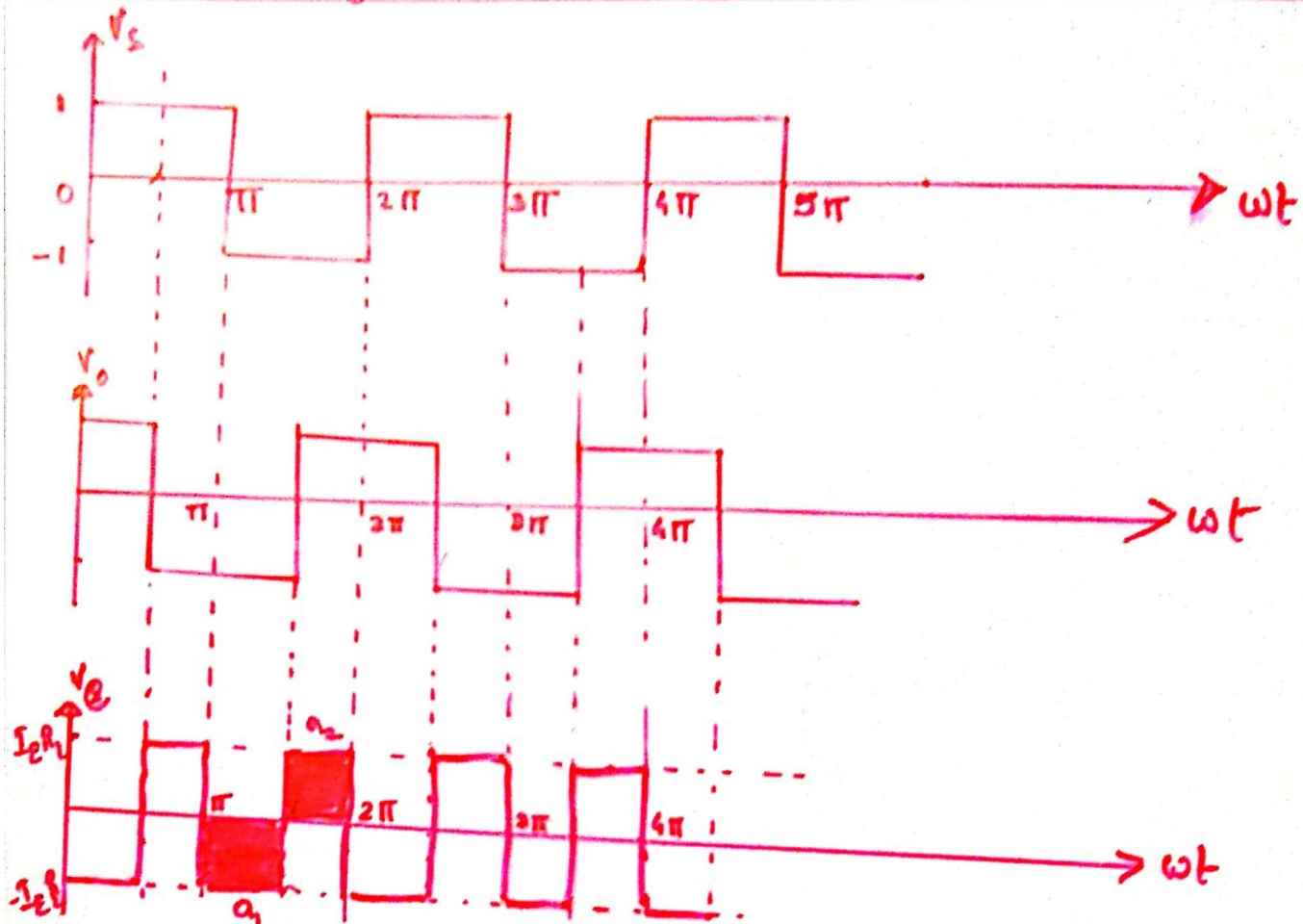


Waveforms of balanced modulator phase detector \Rightarrow



$$V_e = I_e R_L$$

$$V_e(\text{avg}) = \frac{1}{\pi} (a_1 + a_2)$$

$$V_e(\text{avg}) = \frac{1}{\pi} [(-I_e R_L)(\pi - \phi) + (I_e R_L)(\phi)] = \frac{I_e R_L}{\pi} [-\pi + \phi + \phi]$$

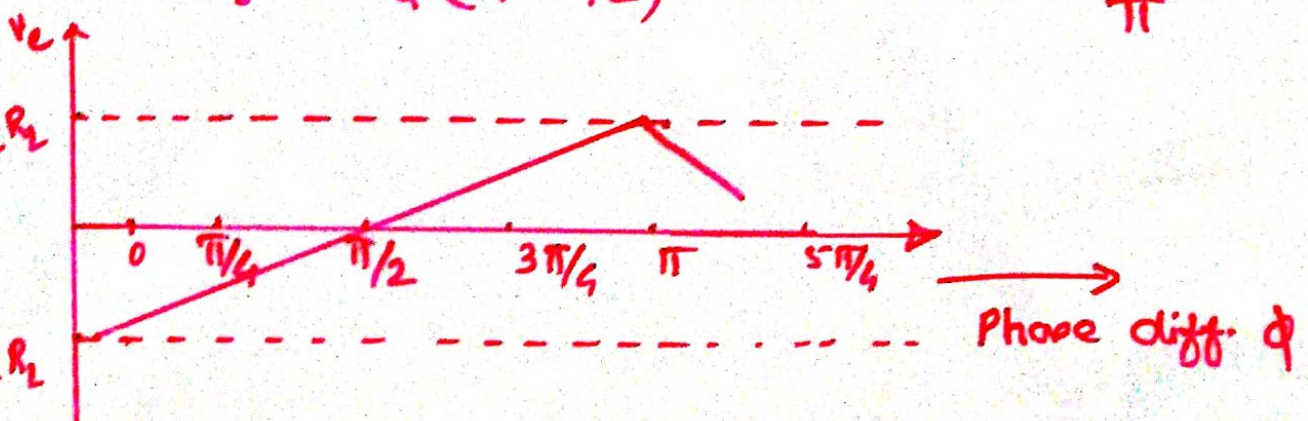
$$= \frac{2 I_e R_L}{\pi} [\phi - \pi/2]$$

$$V_e(\text{avg}) = \frac{4 I_L R_L}{\pi} [\phi - \pi/2] \quad \because I_e = 2 I_L$$

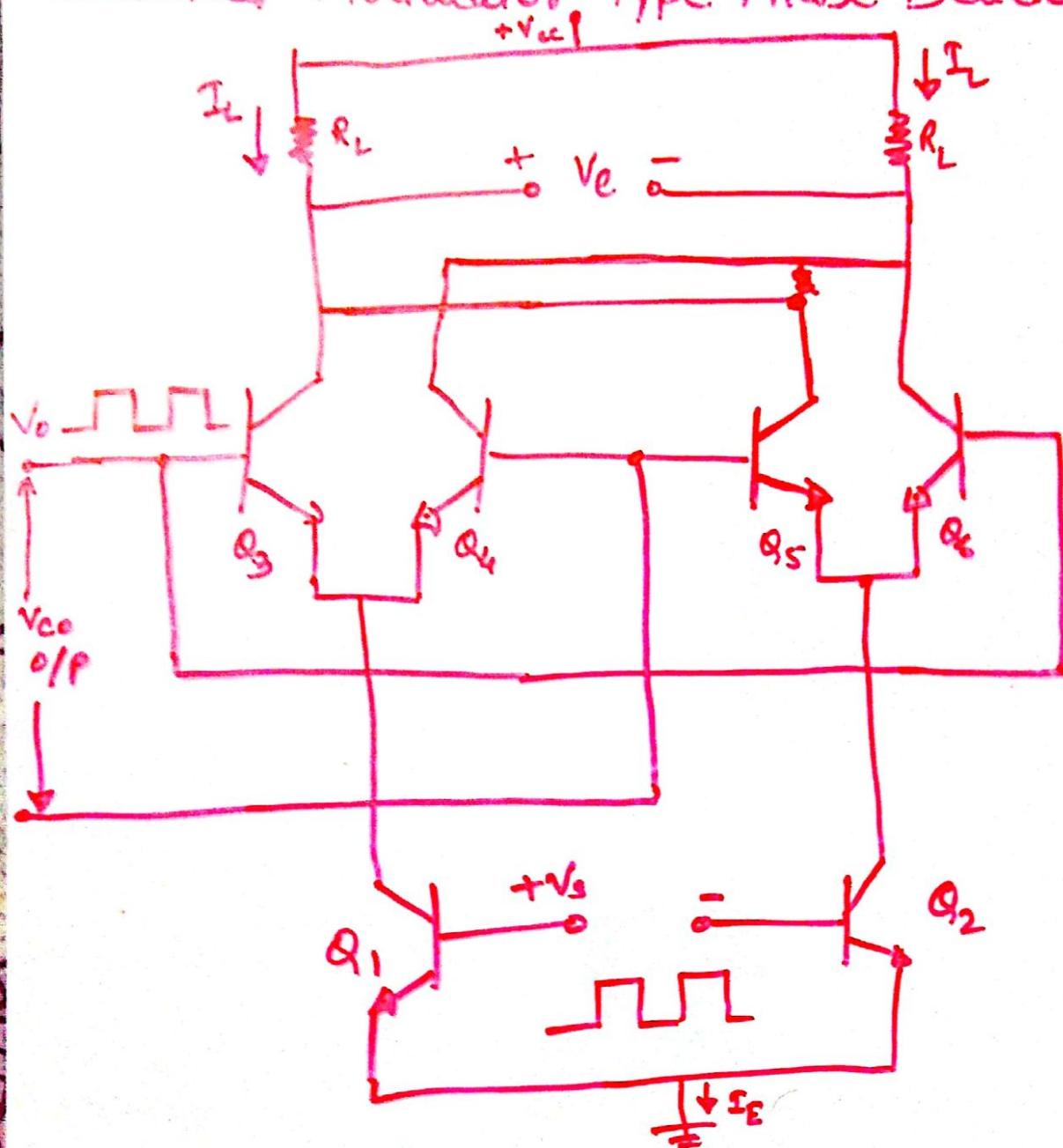
$$V_e(\text{avg}) = K_\phi (\phi - \pi/2)$$

$$\therefore K_\phi = \frac{4 I_L R_L}{\pi}$$

DC component of phase detector o/p

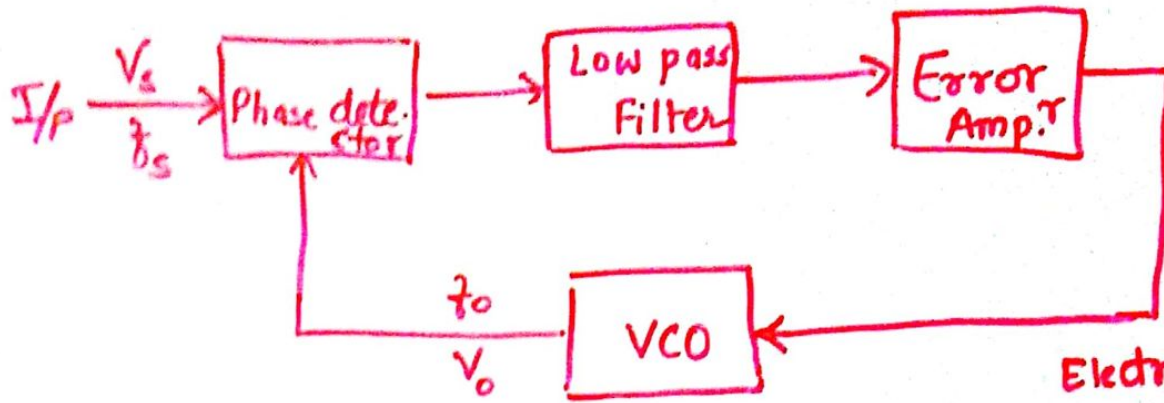


Balanced Modulator Type Phase Detector :-

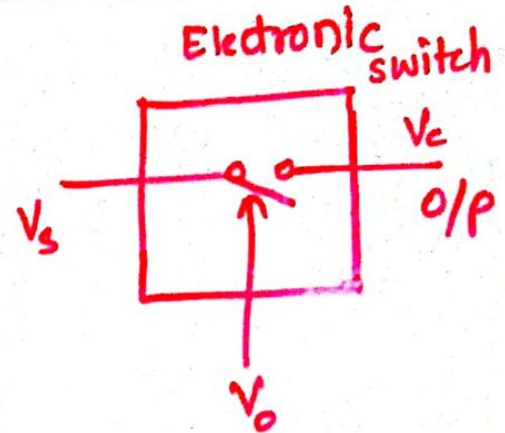
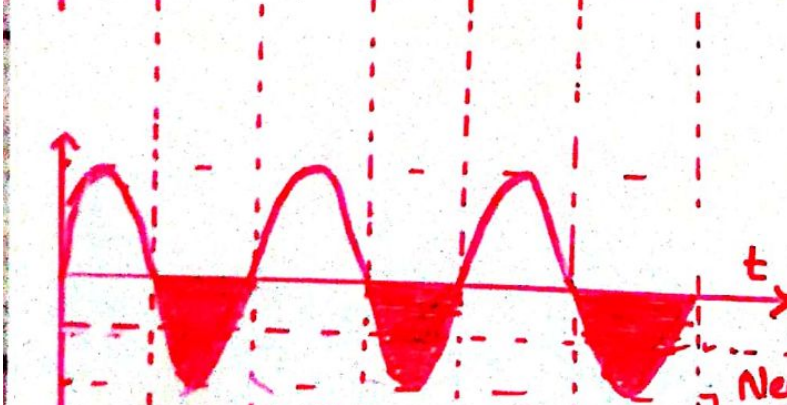
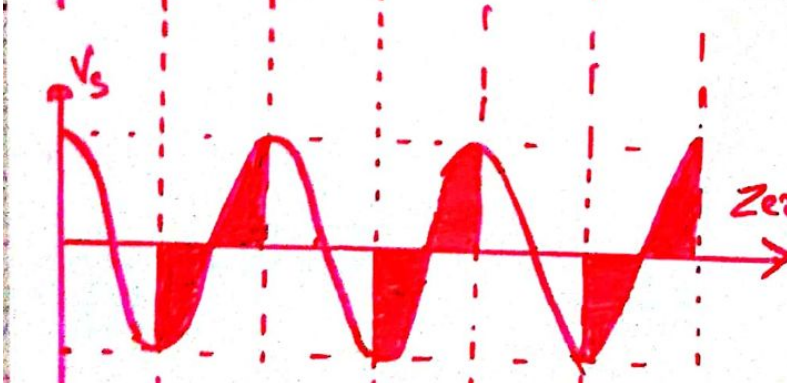
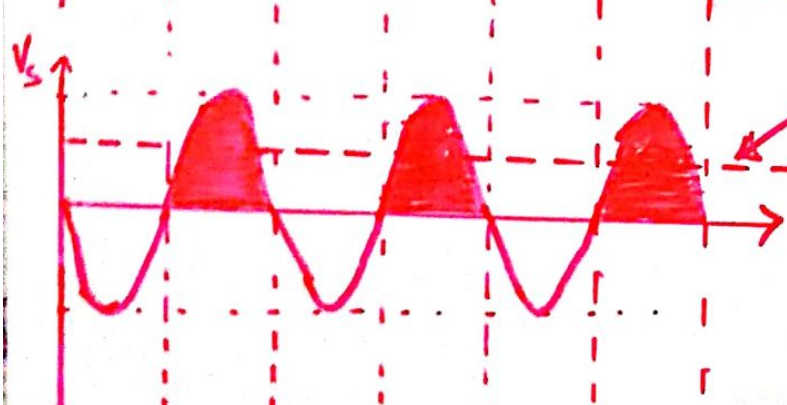
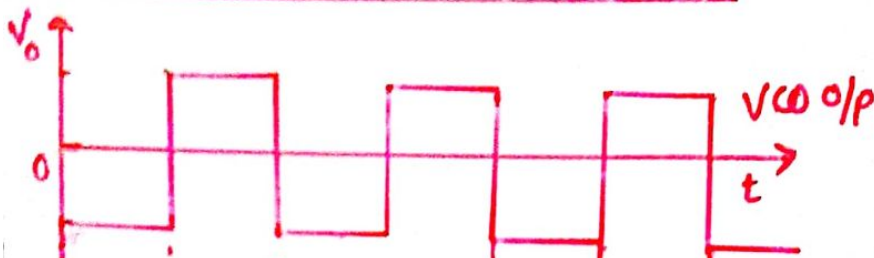


V_s	V_o	Q_1	Q_2	Q_3-Q_6	Q_4-Q_5	V_e
Positive	Positive	ON	OFF	ON	OFF	Negative
Positive	Negative	ON	OFF	OFF	ON	Positive
Negative	Positive	OFF	ON	ON	OFF	Positive
Negative	Negative	OFF	ON	OFF	ON	Negative

BLOCK DIAGRAM OF PLL:-



Phase detector Waveforms =>



Positive Error Voltage $V_s = V_s \sin(2\pi f_s t)$
 $V_o = V_o \sin(2\pi f_o t + \phi)$

$$V_e = K V_s V_o \sin(2\pi f_s t) \sin(2\pi f_o t + \phi)$$

$K \rightarrow$ Phase detector gain or attenuation const.

The eq.ⁿ for V_e can be simplified as

$$V_e = \frac{K V_s V_o}{2} [\cos(2\pi f_s t - 2\pi f_o t - \phi) - \cos(2\pi f_s t + 2\pi f_o t + \phi)]$$

At lock condition $t_s = t_o$

$$V_e = \frac{K V_s V_o}{2} [\cos(-\phi) - \cos(2\pi n_2 f_o t + \phi)]$$

Negative Error volt