SCHEME OF INSTRUCTION AND EXAMINATION
BE IV YEAR
(Electronics and Communication Engineering)

SEMESTER – I

S.No.	Course Code	Course Title	Scheme of Instruction Periods Per Week		Scheme of Examination		
			L/T	D/P	Duration in Hours	Univ. Exams	Sessio nals
1	EC 401	Microwave Engineering	4	-	3	75	25
2	EC 402	VLSI Design	4	-	3	75	25
3	EC 403	Electronic Instrumentation	4	-	3	75	25
4		Elective – I	4	-	3	75	25
5		Elective – II	4	-	3	75	25
6		Industrial Administration and Financial Management	4	-	3	75	25
		PRACTICALS					
1	EC 431	Microwave Lab	-	3	3	50	25
2	EC 432	Embedded C and VLSI Design Lab	-	3	3	50	25
3	EC 433	Project Seminar	-	3	--	--	25
		TOTAL	24	6		550	225

	Elective – I		Elective – II
EC 411	Optical Communication	EC 421	Embedded Systems
EC 412	Digital Image Processing	EC 422	Digital Signal Processor & Architecture
EC 413	Multi Rate Signal Processing	EC 423	Optimization Techniques
EC 414	FPGA	EC 424	System Automation and Control
EC 415	Artificial Neural Networks	EC 425	Internet of Things
CS XXX	Information Security	ME XXX	Entrepreneurship

SCHEME OF INSTRUCTION AND EXAMINATION
BE IV YEAR
(ELECTRONCIS AND COMMUNICATION ENGINEERING)

SEMESTER – II

S.No.	Code No.	Subject	Scheme of Instruction		Scheme of Examination		
			L/T	D/P	Duration in Hours	Max. Marks	
		THEORY				Univ. Exams	Sessionals
1.	EC 451	Data Communication Computer Networks	4	-	3	75	25
2		Elective – III	4	-	3	75	25
3		Elective – IV	4	-	3	75	25
		PRACTICALS					
1	EC 481	General Seminar	-	3	--	--	25
2	EC 482	Project	-	6	Viva-voce	Grade	50
		TOTAL	12	9		225	150

	Elective – III		Elective – IV
EC 461	Real Time Operating System	EC 471	Nano Electronics
EC 462	Coding Theory and Techniques	EC 472	Global Navigational Satellite Systems
EC 463	Design of Fault Tolerant Systems	EC 473	Fuzzy Logic and Applications
EC 464	Radar Systems	EC 474	Wireless Sensor Networks
EC 465	Mobile and Cellular Communication	EC475	EMIC
EC 466	System Verilog	EC 476	Speech Signal Processing
EC 467	Analog VLSI Design	EC 477	Advanced Digital Design
		EC 478	Scripting Language
LA XXX	Intellectual Property Rights	CE XXX	Disaster Mitigation and Management

HEAD OF THE DEPARTMENT
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METHODIST COLLEGE OF ENGG. & TECH.
ABIDS, HYDERABAD