

METHODIST

COLLEGE OF ENGINEERING AND TECHNOLOGY

Approved by AICTE New Delhi | Affiliated to Osmania University, Hyderabad Abids, Hyderabad, Telangana, 500001

Number of Certificate/diploma program introduced during last five years TIMELINE:2014-15 TO 2018-19					
		13 10 2010			
DEPARTMENTS	2018-19	2017-18	2016-17	2015-16	2014-15
CIVIL	1	1	0	0	0
CSE	2	0	0	0	0
ECE	2	3	1	0	2
EEE	1	1	1	1	1
MECH(UG)	0	0	1	0	0
MECH(PG)-CAD CAM	0	0	0	1	0
MBA	0	0	0	0	0
CERTIFICATE PROGRAMS					
INRODUCED YEARWISE	6	5	3	2	3
CERTIFICATE PROGRAMS INRODUCED OVER LAST 5 YRS				19	

SUMMARY

- ■Total no. of certificate programs organized over past 5 years under 5- Engineering programs & MBA are summarised in above table
- ■These programs are conducted for 5 -6 days @ 6-7 hours per day, spread across 1-2 weeks/ months in a semester
- The Resource persons are drawn either from reputed training center/industry/ in-house expertise
- A significant improvement in the total number of courses from year to year is observed
- The Eventwise 1). schedule & syllabus, 2). Sample participation certificate, 3). Participants list are herewith attached

METHODIST COLLEGE OF ENGINEERING AND TECHNOLOGY

ABIDS, HYDERABAD

EEE Department

Date: 05/01/2016

TO

The Principal/Director,

MCET,

Hyderabad.

SUB: Minute of Meeting for approval for conduction of CERTIFICATE PROGRAMMES for the current semester -Reg

Respected sir,

The dept. of EEE is planning to conduct the following CERTIFICATE PROGRAMMES (workshops) for the current semester 2015-16 (II SEM) . The details are specified below. I request your kind approval for the same.

The syllabus is also attached for your perusal

Sl. no	Programme name	Dates	In collaboration with	coordinator
1.	ONE WEEK TECHNICAL CERTICATE COURSE ON DESIGN AND ANALYSIS OF PHOTO- VOLTAIC POWER SYSTEMS	FEB 2016	VSES Pvt Ltd	Mrs. V.SAKETHA Asst. Professor

Thanking you sir,

Per mi Her

Head of Department

Department of EEE Tech.

Department of EEE

Department of EEE

Abids, Hyderabad 500 001.

ABOUT EEE DEPARTMENT:

The Department of Electrical and Electronics Engineering was started in the year 2008 with an intake of 60, with an objective of imparting quality education in the Electrical and Electronics Engineering.EEE is the oldest of the engineering streams, and its applications are many. The number of career prospects for this group is huge, and it is the reason for many people to join this course. These students are offered with high salaried jobs, especially in the public sector. They are needed in power industry, electrical machine industries, etc. A number of private companies also offer lucrative job roles for these engineers.

Due to this requirement, Methodist gives a special preference to EEE, and we had built a special base for this group. The laboratories, faculty, etc. for this group are of top notch, and we chalk out various industrial tours, seminars workshops, etc. for the benefit of our students. EEE is one of the core streams of engineering, and it is required in any case. Hence the demand for this group never decreases, and it is the steadiest stream ever.



Register on or before 10-02-2016 & Feel the energy of SOLAR

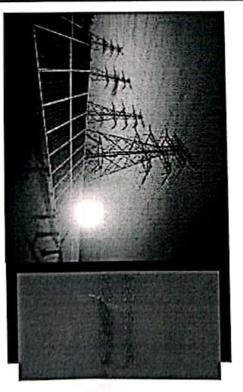
SPEAKER

Eminent person from VSES Pvt. Ltd.



ONE WEEK TECHNICAL CERTIFICATE COURSE ON DESIGN AND ANALYSIS OF PHOTO-VOLTAIC POWER SYSTEMS

ORGANISED BY DEPARTMENT OF ELECTRICAL & ELECTRODICS ENGINEERING







METHODIST COLLEGE OF ENGINEERING AND TECHNOLOGY (Alfiliated to Osmania University)
King Koti Road, Abids, Hyderabad, Telangana-500001

METHODIST COLLEGE OF ENGINEERING & TECHNOLOGY DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

CERTIFICATE COURSE ON MATLAB/SIMULINK

SCHEDULE

	Times	Activity/Session	Location	
	9.30AM	Registration,		
DAYI	10.00AM	Welcome & Introductions	Seminar hall	
	11.00AM -12.30 PM	Presentation on fundamentals of matlab		
	12.30PM-1.00pm	lunch		
	1.00PM-4.00PM	practice session on fundamental diagrams	Simulation lab	
	9.30AM	Welcome & Introductions		
DAY2	10.00AM-12.30 PM	Presentation on creation of Simulink blocks	Seminar hall	
	12.30PM-1.00pm	LUNCH		
	1.00PM-4.00PM	Practice session	Simulation lab	
DAY3	10.00AM-12.30 PM	Presentation on design sub-circuits in matlab		
	12.30PM-1.00pm	lunch		
	1.00PM-4.00PM	Practice session on computers by using matlab software	Simulation lab	
DAY4	10.00AM-12.30 PM	Presentation on new techniques in matlab		
	12.30PM-1.00pm	LUNCH		
	1.00PM-4.00PM	Programming work	Simulation lab	
	10.00AM-12.30 PM	Presentation on innovative methods in power system and linked with matlab	Seminar hall	
DAY5	12.30AM-1.00pm	LUNCH		
	1.00PM-4.00PM	Coding and Simulink practice session	Simulation lab	
	4.00PM – 4.15PM	VOTE OF THANKS	Simulation lab	

Department men.

Scanned by CamScanner

METHODIST COLLEGE OF ENGINEERING & TECHNOLOGY DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

CERTIFICATE COURSE ON MATLAB/SIMULINK

SCHEDULE

	Times	Activity/Session	Location
	9.30AM	D. sistuation	
DAY1	10.00AM	Registration,	
		Welcome & Introductions	Seminar hall
	11.00AM -12.30 PM	Presentation on fundamentals of matlab	
	12.30PM-1.00pm	lunch	
	1.00PM-4.00PM	practice session on fundamental diagrams	Simulation lab
	9.30AM	Welcome & Introductions	
DAY2	10.00AM-12.30 PM	Presentation on creation of Simulink blocks	Seminar hall
	12.30PM-1.00pm	LUNCH	

SYLLABUS ON MATLAB

Matlab Basics

- Capabilities of MATLAB and its Applications.
- Arithmetic operations with scalars
- Matrix calculations
- MATLAB built-in functions
- Creating Script files
- Arrays operation
- Mathematical and statistical operations
- Performing bitwise operations.
- Reading & writing Text file.
- Introduction to operators

Matlab Graphics

- Basic plots and subplots
- 3D plots
- Multidimensional data interpolation
- Plotting multiple graphs and multiple plots

Programming Using Matlab

- Creating own scripts and user defined function file
- Program structure
- Loops, Nested loops and nested conditional statements
- Digital electronics
- Analog electronics
- Communication
- Control system
- Signals & systems
- · Programs based on Project building

Simulink

- Commonly Used Blocks Library
- Creating Simulink Models
- Using Simulink from MATLAB command window.
- Using simulink for Digital electronic concept
- Using simulink for analog electronic concept
- Using simulink control system concept

Department ment

METHODIST COLLEGE OF ENGINEERING & TECHNOLOGY

King Koti Road, Abids, Hyderabad - 500001

Department of Electrical & Electronics Engineering

One Week certificate course on Design and Analysis of Photo Voltaic power systems (during 12th feb to 18th feb 2016)

LIST OF REGISTERED STUDENTS

S.No	Hall Ticket No	Name of the Student	SIGNATURE
1	160712734002	PULLAGURA ARAVIND	N/S
2	160712734003	L TEJESWARA RAO	Trave
3	160712734005	P. PROMODH REDDY	200
4	160712734006	BODDU SHIVA PRASAD	Clare
5	160712734007	KAKI GOPAL +	Cont
6	160712734008	DUDDYALA VARUN RAJ	u_v
7	160712734009	D. V. NAGA SRIVALLI	- Signal
8	160712734010	M PHANINDRA	CO
9	160712734011	G. ESHWAR CHARAN	Acy
10	160712734012	KOLIPAKA RAKESH	125
11	160712734013	CHATHARAJU AKHILA,	ALL
12	160712734014	DASARI BHANU MAHESH	THE
13	160712734015	MALLAK BALU	Led_
14	160712734016	S ISRAEL JESSI	Sur
15	160712734018	ALETI MOHITHRAJ	And
16	160712734019	GAŅDE NAGATEJASWINI	Gel
17	160712734020	SIRSUN MANISHAREDDY	· Boul
18	160712734021	NEERATI SURESH	rue
19	160712734023	KAVALI KARAN SHALON	Ky.
20	160712734024	T VINAY KUMAR	Vela.
21	160712734027	PONNAM RAMYA	R
22	160712734031	ANJUM SULTANA	Ad
23	160712734038	M RAJA RAVI CHANDRA	K
24	160712734047	RAHMAN QUADRI	BACOL
25	160712734051	DAMMA ANIL REDDY	RUL
26	160712734052	MD AZARUDDIN	M
27	160712734302	MOHAMMED IMRAN	THU.
28	160712734306	OSMAN ALI	Out
29	160712734307	PARNE KRISHNA REDDY	Ry
30	160712734308	MY&KALA HARISH	dill
31	160712734309	SHAIK ABDUL JAWWAD	Sell
32	160712734310	Y RAHUL	yen
33	160712734311	NALLA RAJU	Alel
34	160712734314	DEVSOTH SRINIVAS	horn
35	160712734316	M BRAHMANANDAM	Bed
36	160712734317	VENKATESH MANEKER	Jeges
37	160712735110	K.AKSHAY KUMAR	Dogs.
38	160711734049	M PRASHANTH (RA)	Peel
39	160711734002	VIDYA SAGAR	Vula
40	160711734008	MD ABDUL MALIK Ur RAHMAN	Hd Au
41	160711734051	S SRIDEVI	S.M.
42	160710734056	DUGNI SWETHA	Suct -



METHODIST COLLEGE OF ENGINEERING & TECHNOLOGY

King Koti Road, Abids, Hyderabad - 500001 Department of Electrical & Electronics Engineering

One Week certificate course on Design and Analysis of Photo Voltaic power systems (during 12th feb to 18th feb 2016)

S. No.	Hall Tist	LIST OF PARTICIPAL	NTS
1	Hall Ticket No.	Name of the Student	
	160713734001	CH PRAGNA	SIGNATURE
2	160713734002	V K SHINEY PRIYANKA	(Gro)
3	160713734003	G SARITHA	LHINFD
4	160713734004	K PRASHANTHI	(g)-4;
5 '	160713734005	MD ABDUL SOHAIL	Prashanthi
- 6	160713734006	J CHARANRAJ	ABBUL
7	160713734007		chandlason
- 8	160713734008	G MURALIDHAR	Murlider
9	160713734009		ilravas
10	160713734010	R NARSIMULU	asy
- 11	160713734011	MD RAWOOF KHAN T SAIBABA	* Farresy
12	160713734012		1. Just
13	160713734015	M SREEKANTH	Pikanth
14	160713734017	MD AAMAR KHAN	ALWI
15	160713734018		Brumos
16	160712724030		Vive k.C
17	160713734024	SHAIK YOUSUF MD JUNAID'AHMED	- your
18	160713734025	K SUSHMITHA	dunaid
19	160713734026	R SUSHMITHA	eleintha
20	160713734031	DRAJESH	KOLAT
21	160713734033	TAYYABA SULTANA	Clather
22		MD MUJAHID HUSSAIN	HU8870
23		MOHD UMED	UMED
24	160713734061	SYED AHMED BASHEERUDDIN	There of
. 25	160712724201		thing
26		BOJJA VENKATESH	renai
27		K ARUN KUMAR	aun
28	160712721207	M VIJAY NAIK	Myan
29		KALAVALA UDAY BHASKAR	udapunere
30	160713734309	SRINAM NIKHIL	NIKHLL
	160713734310	BUSHIPAKA JAGAN	Sagar
31	160713734312	MD ABDUL WAHAB	Abarl.
32	160713734313	SK TAHEER	Touthat



METHODIST COLLEGE OF ENGINEERING & TECHNOLOGY

(Affiliated to O.U. & Approved by AICTE)

King Koti Road, Abids, Hyderabad – 500 001, Telangana, INDIA



Certificate of Participation

Certified that Mr/Ms Ch. Paggaa

Bearing R.No: 160713734001

off3-2 Semester EEE

during____

Feb ap

Branch, Participated in Certification Courses

in Collaboration with

on 12th Feb - 18th Feb

USES Put Led

Principal

Director